Bibliography on inductance simulation using gyrator m

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Citation Report

#	Article	IF	Citations
1	100 MHz quartz delay line utilizing Rayleigh waves. Proceedings of the IEEE, 1968, 56, 1635-1636.	21.3	13
2	An inductance realization using two operational amplifiers. Proceedings of the IEEE, 1968, 56, 1636-1637.	21.3	1
3	A reactance circuit derivation of the gyrator. Proceedings of the IEEE, 1970, 58, 1965-1966.	21.3	0
4	On inductor simulation using a unity-gain amplifier. IEEE Journal of Solid-State Circuits, 1970, 5, 95-98.	5.4	15
5	A generalized method of active RC network synthesis. IEEE Transactions on Circuit Theory, 1971, 18, 643-650.	0.6	9
6	A circuit for realizing a high-quality solid state inductance. Solid-State Electronics, 1971, 14, 345-347.	1.4	3
7	Active Synthesis of Biquadratic Inductive Impedance with Grounded Capacitors. IETE Journal of Research, 1972, 18, 336-339.	2.6	0
8	Polynomial decomposition for the minimization of quality factor sensitivity. IEEE Transactions on Circuit Theory, 1972, 19, 397-398.	0.6	3
9	Inductor simulation derived from an amplifier rolloff characteristic. IEEE Transactions on Circuit Theory, 1972, 19, 395-397.	0.6	8
10	Bibliography of inductance simulation by active RC methods. Microelectronics Reliability, 1976, 15, 637-639.	1.7	10
11	Realisation of single-resistance-controlled lossless floating inductance. Electronics Letters, 1978, 14, 828.	1.0	10
12	New Single-Capacitor Simulations of Floating Inductors. Electrocomponent Science and Technology, 1982, 10, 7-12.	0.0	15
13	A Realization of Multi-port Gyrator Using CC IIs with Current Followers. IEEJ Transactions on Electronics, Information and Systems, 1999, 119, 1175-1179.	0.2	0