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## FAULT-TOLERANT ADAPTIVE XY ROUTING FOR MULTIPROCESSORS IN HPC NETWORK

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2	A Novel Interconnection Network with Improved Network Cost Through Shuffle-Exchange Permutation Graph. <i>Electronics (Switzerland)</i> , <b>2021</b> , 10, 943	2.6	
1	A 3-disjoint path design of non-blocking shuffle exchange network by extra port alignment. <i>Journal of Supercomputing</i> , 1	2.5	O