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Long short-term memory networks in memristor crossbar arrays

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228	Inference of Long-Short Term Memory networks at software-equivalent accuracy using 2.5M analog Phase Change Memory devices. **2019**,		10
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224	Dopamine-like STDP modulation in nanocomposite memristors. **2019**, 9, 065116		22
223	Memristive Circuit Design of Emotional Generation and Evolution Based on Skin-Like Sensory Processor. **2019**, 13, 631-644		27
222	Reservoir Computing Using Diffusive Memristors. **2019**, 1, 1900084		65
221	A survey on LSTM memristive neural network architectures and applications. **2019**, 228, 2313-2324		52
220	Bridging Biological and Artificial Neural Networks with Emerging Neuromorphic Devices: Fundamentals, Progress, and Challenges. **2019**, 31, e1902761		220
219	In situ training of feed-forward and recurrent convolutional memristor networks. *Nature Machine Intelligence*, **2019**, 1, 434-442	22.5	93
218	Memristive crossbar arrays for brain-inspired computing. **2019**, 18, 309-323		582
217	A Survey on Architecture Advances Enabled by Emerging Beyond-CMOS Technologies. **2019**, 36, 46-68		10
216	Reinforcement learning with analogue memristor arrays. **2019**, 2, 115-124		166
215	Associative Memory for Image Recovery with a High-Performance Memristor Array. **2019**, 29, 1900155		37
214	Unconventional Inorganic-Based Memristive Devices for Advanced Intelligent Systems. **2019**, 4, 1900080		9
213	Learning with Resistive Switching Neural Networks. **2019**,		4
212	Gradual resistive switching: Insights from inverse nonexponential decay and unified theoretical modeling. **2019**, 115, 243501		0

211	A Simple BJT Inverse Memristor Emulator and Its Application in Chaotic Oscillators. **2019**,		3
210	Gate Modulation of Excitatory and Inhibitory Synaptic Plasticity in a Low-Temperature Polysilicon Thin Film Synaptic Transistor. **2019**, 1, 132-140		14
209	A role for analogue memory in AI hardware. *Nature Machine Intelligence*, **2019**, 1, 10-11	22.5	10
208	Memristive LSTM Architectures. **2020**, 155-167		
207	Theoretical Foundations of Memristor Cellular Nonlinear Networks: Memcomputing With Bistable-Like Memristors. **2020**, 67, 502-515		27
206	Self-adaptive STDP-based learning of a spiking neuron with nanocomposite memristive weights. **2020**, 31, 045201		49
205	Design and characterization of superconducting nanowire-based processors for acceleration of deep neural network training. **2020**, 31, 025204		5
204	Enhanced Spiking Neural Network with forgetting phenomenon based on electronic synaptic devices. **2020**, 408, 21-30		3
203	Memristor-based vector neural network architecture. **2020**, 29, 028502		5
202	Improvement of analogue switching characteristics of MoS2 memristors through plasma treatment. **2020**, 53, 135305		6
201	Soft eSkin: distributed touch sensing with harmonized energy and computing. **2020**, 378, 20190156		45
200	. **2020**, 31, 1328-1342		10
199	Targeting Multistable Dynamics in a Second-Order Memristor Circuit. **2020**,		3
198	Water-Mediated Ionic Migration in Memristive Nanowires with a Tunable Resistive Switching Mechanism. **2020**, 12, 48773-48780		13
197	High Robustness Memristor Neural State Machines. **2020**, 2, 3633-3642		5
196	Multichannel parallel processing of neural signals in memristor arrays. **2020**, 6,		12
195	Picosecond multilevel resistive switching in tantalum oxide thin films. **2020**, 10, 16391		15
194	Neuro-inspired computing chips. **2020**, 3, 371-382		139

193	Automating Analogue AI Chip Design with Genetic Search. **2020**, 2, 2000075	5
192	From Memristive Materials to Neural Networks. **2020**, 12, 54243-54265	21
191	Memristor-based LSTM network with in situ training and its applications. **2020**, 131, 300-311	12
190	Neuromorphic Engineering for Hardware Computational Acceleration and Biomimetic Perception Motion Integration. **2020**, 2, 2000124	2
189	InputOutput Characterization of the Dynamical Properties of Circuits with a Memelement. **2020**, 30, 2050110	4
188	Implementation of Dropout Neuronal Units Based on Stochastic Memristive Devices in Neural Networks with High Classification Accuracy. **2020**, 7, 2001842	10
187	Transiently chaotic simulated annealing based on intrinsic nonlinearity of memristors for efficient solution of optimization problems. **2020**, 6, eaba9901	22
186	Transport Properties of Magnetic Nanogranular Composites with Dispersed Ions in an Insulating Matrix. **2020**, 131, 160-176	8
185	A memristor-based hybrid analog-digital computing platform for mobile robotics. **2020**, 5,	11
184	Recent Progress on Memristive Convolutional Neural Networks for Edge Intelligence. **2020**, 2, 2000114	11
183	Long-Short Term Memory-Based Application on Adaptive Cross-Platform Decoder for Bit Patterned Magnetic Recording. **2020**, 8, 155248-155259	5
182	Variability-Aware Modeling of Filamentary Oxide-Based Bipolar Resistive Switching Cells Using SPICE Level Compact Models. **2020**, 67, 4618-4630	17
181	Hyperchaotic Behavior in the Novel Memristor-Based Symmetric Circuit System. **2020**, 8, 151535-151545	6
180	Committee machines-a universal method to deal with non-idealities in memristor-based neural networks. **2020**, 11, 4273	20
179	Memristive Devices for New Computing Paradigms. **2020**, 2, 2000105	20
178	MTL: Memristor Ternary Logic Design. **2020**, 30, 2050222	5
177	A Multivariate Long Short-Term Memory Neural Network for Coalbed Methane Production Forecasting. **2020**, 12, 2045	1
176	Integration and Co-design of Memristive Devices and Algorithms for Artificial Intelligence. **2020**, 23, 101809	20

175	Memristive GAN in Analog. **2020**, 10, 5838	17
174	Evolving conductive polymer neural networks on wetware. **2020**, 59, 060601	8
173	Mixed-Precision Deep Learning Based on Computational Memory. **2020**, 14, 406	31
172	Memristive Device Characteristics Engineering by Controlling the Crystallinity of Switching Layer Materials. **2020**, 2, 1529-1537	3
171	Associative STDP-like learning of neuromorphic circuits based on polyaniline memristive microdevices. **2020**, 53, 414001	17
170	Silicon-based optoelectronic synaptic devices. **2020**, 29, 070703	10
169	Forecasting of Coalbed Methane Daily Production Based on T-LSTM Neural Networks. **2020**, 12, 861	5
168	Research progress on solutions to the sneak path issue in memristor crossbar arrays. **2020**, 2, 1811-1827	55
167	Spike-Timing-Dependent and Spike-Shape-Independent Plasticities with Dopamine-Like Modulation in Nanocomposite Memristive Synapses. **2020**, 217, 1900938	10
166	A Behavioral Model of Digital Resistive Switching for Systems Level DNN Acceleration. **2020**, 67, 956-960	4
165	A Memristor with Low Switching Current and Voltage for 1S1R Integration and Array Operation. **2020**, 6, 1901411	21
164	Sneak, discharge, and leakage current issues in a high-dimensional 1T1M memristive crossbar. **2020**, 19, 565-575	8
163	Memristor Neural Networks for Linear and Quadratic Programming Problems. **2020**, PP,	1
162	A Multilayer Neural Network Merging Image Preprocessing and Pattern Recognition by Integrating Diffusion and Drift Memristors. **2020**, 1-1	16
161	A Memristive Circuit Implementation of Eyes State Detection in Fatigue Driving Based on Biological Long Short-Term Memory Rule. **2021**, 18, 2218-2229	2
160	Breaking the Quantum PIN Code of Atomic Synapses. **2020**, 20, 1192-1200	3
159	Resistive switching materials for information processing. **2020**, 5, 173-195	318
158	. **2020**, 67, 895-901	18

157	Brain-inspired computing with memristors: Challenges in devices, circuits, and systems. **2020**, 7, 011308	105
156	Conductive-bridging random-access memories for emerging neuromorphic computing. **2020**, 12, 14339-14368	18
155	Parallel Operation of Self-Limited Analog Programming for Fast Array-Level Weight Programming and Update. **2020**, 2, 2000014	1
154	Deep Learning for Medical Decision Support Systems. **2021**,	6
153	Flexible 3D memristor array for binary storage and multi-states neuromorphic computing applications. **2021**, 3, 212-221	16
152	Unfolding Nonlinear Dynamics in Analogue Systems With Mem-Elements. **2021**, 68, 14-24	7
151	Molecular structure incorporated deep learning approach for the accurate interfacial tension predictions. **2021**, 323, 114571	
150	Necessary conditions for STDP-based pattern recognition learning in a memristive spiking neural network. **2021**, 134, 64-75	37
149	The Future of Memristors: Materials Engineering and Neural Networks. **2021**, 31, 2006773	62
148	Non-spike timing-dependent plasticity learning mechanism for memristive neural networks. **2021**, 51, 3684-3695	4
147	Nonvolatile Boolean logic in the one-transistor-one-memristor crossbar array for reconfigurable logic computing. **2021**, 129, 153542	3
146	Competing memristors for brain-inspired computing. **2021**, 24, 101889	13
145	Transhumanism: The Proper Guide to a Posthuman Condition or a Dangerous Idea?. **2021**,	0
144	An Unequal Deep Learning Approach for 3-D Point Cloud Segmentation. **2021**, 17, 7913-7922	9
143	Detecting Dynamic Behavior of Brain Fatigue Through 3-D-CNN-LSTM. **2021**, 1-11	6
142	Memristive Circuit Design of Brain-Like Emotional Learning and Generation. **2021**, PP,	3
141	Analog circuit integration of backpropagation learning in memristive HTM architecture. **2021**, 427-438	
140	Integrated neuromorphic computing networks by artificial spin synapses and spin neurons. **2021**, 13,	9

139	Guidelines for benchmarking non-ideal analog memristive crossbars for neural networks. **2021**, 287-298	
138	Memristor-Based Image Enhancement: High Efficiency and Robustness. **2021**, 68, 602-609	5
137	Learning to Approximate Functions Using Nb-Doped SrTiO Memristors. **2020**, 14, 627276	0
136	A Review of Resistive Switching Devices: Performance Improvement, Characterization, and Applications. **2021**, 2, 2000109	34
135	Photonic pattern reconstruction enabled by on-chip online learning and inference. **2021**, 3, 024006	0
134	Advances in Memristor-Based Neural Networks. **2021**, 3,	10
133	Functional Applications of Future Data Storage Devices. **2021**, 7, 2001181	8
132	Threshold-type memristor-based memory circuit. **2021**, 49, 1515-1531	1
131	Array-level boosting method with spatial extended allocation to improve the accuracy of memristor based computing-in-memory chips. **2021**, 64, 1	2
130	Brain-Like Networks in Random Memristor Array Based on FORCE Training. **2021**,	
129	In-Memory Computing with Resistive Memory Circuits: Status and Outlook. **2021**, 10, 1063	13
128	Doping-Enabled Reconfigurable Strongly Correlated Phase in a Quasi-2D Perovskite. **2021**, 12, 5091-5098	
127	Transient Control in Targeting Multistable Dynamics of a Memristor Circuit. **2021**,	1
126	Noise-assisted persistence and recovery of memory state in a memristive spiking neuromorphic network. **2021**, 146, 110890	32
125	Competitive Neural Network Circuit Based on Winner-Take-All Mechanism and Online Hebbian Learning Rule. **2021**, 29, 1095-1107	1
124	Dynamically-biased Fixed-point LSTM for Time Series Processing in AIoT Edge Device. **2021**,	0
123	Memristive Crossbar Arrays for Storage and Computing Applications. **2021**, 3, 2100017	18
122	The viability of analog-based accelerators for neuromorphic computing: a survey. **2021**, 1, 012001	4

121	Light-Emitting Memristors for Optoelectronic Artificial Efferent Nerve. **2021**, 21, 6087-6094	13
120	Memristor crossbar architectures for implementing deep neural networks. 1	4
119	A Marr@ Three-Level Analytical Framework for Neuromorphic Electronic Systems. **2021**, 3, 2100054	0
118	Architecting for Artificial Intelligence with Emerging Nanotechnology. **2021**, 17, 1-33	1
117	Hardware-Friendly Stochastic and Adaptive Learning in Memristor Convolutional Neural Networks. **2021**, 3, 2100041	6
116	Memristor-based circuit implementation of Competitive Neural Network based on online unsupervised Hebbian learning rule for pattern recognition. 1	4
115	Generalised Analog LSTMs Recurrent Modules for Neural Computing. **2021**, 15, 705050	1
114	Nonlinearity in Memristors for Neuromorphic Dynamic Systems. 2100049	12
113	A novel deep interval type-2 fuzzy LSTM (DIT2FLSTM) model applied to COVID-19 pandemic time-series prediction. **2021**, 123, 103920	2
112	Comprehensive Model of Electron Conduction in Oxide-Based Memristive Devices. **2021**, 3, 3674-3692	9
111	Memristive Hodgkin-Huxley Spiking Neuron Model for Reproducing Neuron Behaviors. **2021**, 15, 730566	1
110	Parylene-based memristive synapses for hardware neural networks capable of dopamine-modulated STDP learning. **2021**, 54, 484002	1
109	Memristive devices based on Cu-doped NbO films with large self-rectifying ratio. **2021**, 369, 115732	1
108	Planar analog memimpedance behavior in reduced GO-Based Metal-Semiconductor-Metal. **2021**, 210, 110077	3
107	Photonics for artificial intelligence and neuromorphic computing. **2021**, 15, 102-114	166
106	Multiply accumulate operations in memristor crossbar arrays for analog computing. **2021**, 42, 013104	7
105	Artificial Neural Networks Based on Memristive Devices: From Device to System. **2020**, 2, 2000149	16
104	Review of resistive switching mechanisms for memristive neuromorphic devices. **2020**, 29, 097305	8

103	ROA: A Rapid Learning Scheme for Memristor Networks. **2021**, 4, 692065	0
102	Spike-Enabled Audio Learning in Multilevel Synaptic Memristor Array-Based Spiking Neural Network. 2100151	1
101	Deep Learning Based Silicon Content Estimation in Ironmaking Process. **2020**, 53, 10737-10742	1
100	Psychological Personal Support System with Long Short Term Memory and Facial Expressions Recognition Approach. **2021**, 129-144	
99	Transhumanism as a Derailed Anthropology. **2021**, 21-47	
98	In-memory computing with emerging nonvolatile memory devices. **2021**, 64, 1	6
97	CCCS. **2020**,	0
96	Power-Efficient Deep Neural Networks with Noisy Memristor Implementation. **2021**,	0
95	Memristor-based Brain-like Reconfigurable Neuromorphic System. **2021**,	
94	In-Memory Realization of Eligibility Traces Based on Conductance Drift of Phase Change Memory for Energy-Efficient Reinforcement Learning. **2021**, e2107811	3
93	A Consistent Model for Short-Term Instability and Long-Term Retention in Filamentary Oxide-Based Memristive Devices. **2021**, 13, 58066-58075	2
92	Memristor-based multi-synaptic spiking neuron circuit for spiking neural network.	0
91	Artificial Adaptive and Maladaptive Sensory Receptors Based on a Surface-Dominated Diffusive Memristor. **2021**, e2103484	8
90	Gradient Decomposition Methods for Training Neural Networks With Non-ideal Synaptic Devices. **2021**, 15, 749811	1
89	NbO2-Mott Memristor: A Circuit- Theoretic Investigation. **2021**, 68, 4979-4992	11
88	Long Short-Term Memory Implementation Exploiting Passive RRAM Crossbar Array. **2021**, 1-9	0
87	Memristive Circuit Design of Brain-inspired Emotional Evolution based on Theories of Internal Regulation and External Stimulation. **2021**, PP,	2
86	Memristive devices and arrays for neuromorphic computing. **2020**,	

85	Hybrid Analog-Digital Sensing Approach for Low-power Real-time Anomaly Detection in Drones. **2021**,	0
84	Defect tolerant in-memory analog computing with CMOS-integrated nanoscale crossbars: Invited. **2021**,	
83	Thermal Effects on Initial Volatile Response and Relaxation Dynamics of Resistive RAM Devices. **2022**, 1-1	1
82	2022 roadmap on neuromorphic computing and engineering.	24
81	Neuromorphic behaviors of N-type Locally-active Memristor. **2022**,	0
80	Artificial synapse arrays based on SiOx/TiOx memristive crossbar with high uniformity for neuromorphic computing. **2022**, 120, 043101	3
79	Full-Circuit Implementation of Transformer Network Based on Memristor. **2022**, 1-13	5
78	Oscillatory Circuits With a Real Non-Volatile Stanford Memristor Model. **2022**, 10, 13650-13662	
77	Amorphous InGaZnO (a-IGZO) Synaptic Transistor for Neuromorphic Computing.	2
76	Neuromorphic applications using MOx-based memristors. **2022**, 465-508	1
75	Interpretable Memristive LSTM Network Design for Probabilistic Residential Load Forecasting. **2022**, 1-14	2
74	Research Progress on Memristor: From Synapses to Computing Systems. **2022**, 1-13	6
73	Nanostructured perovskites for nonvolatile memory devices.. **2022**,	7
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71	BiOSe-Based True Random Number Generator for Security Applications.. **2022**,	5
70	Lightweight memristive gated recurrent unit networks. **2022**,	
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68	Memristor-based affective associative memory neural network circuit with emotional gradual processes. 1	4

67	Dropout neuronal unit with tunable probability based on NbOx stochastic memristor for efficient suppression of overfitting. **2022**, 111778	1
66	Implementation of Neuronal Intrinsic Plasticity by Oscillatory Device in Spiking Neural Network. **2022**, 69, 1830-1834	0
65	New Method to Analyze the Invariant Manifolds of Memristor Circuits. **2022**,	
64	Frequency-Coded Control of the Conductance of Memristors Based on Nanoscale Layers of LiNbO3 and (Co40Fe40B20)x(LiNbO3)100 −x Composite in Trained Spiking Neural Networks. **2021**, 47, 656-660	1
63	Temporal Coding of Binary Patterns for Learning of Spiking Neuromorphic Systems Based on Nanocomposite Memristors. **2021**, 16, 732-736	
62	Prospects for Analog Circuits in Deep Networks. **2022**, 49-61	
61	Memristive System Based Image Processing Technology: A Review and Perspective. **2021**, 10, 3176	1
60	Neuromorphic Computing Systems with Emerging Devices. **2022**, 173-216	
59	Two-Terminal Neuromorphic Memristors. **2022**, 1-46	
58	A neuromorphic core based on threshold switching memristor with asynchronous address event representation circuits. **2022**, 65, 1	0
57	Neuromorphic Resistive Random Access Memory for Image Processing. **2022**,	
56	Ta/HfO2 memristors: from device physics to neural networks.	0
55	Data_Sheet_1.pdf. **2020**,	
54	A Mini Tutorial of Processing in Memory: From Principles, Devices to Prototypes. **2022**, 1-1	0
53	Arrays of Nanocomposite Crossbar Memristors for the Implementation of Formal and Spiking Neuromorphic Systems. **2022**, 17, 118-125	0
52	Neural Network Training With Asymmetric Crosspoint Elements. **2022**, 5,	1
51	Toward memristive in-memory computing: principles and applications. **2022**, 15,	2
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49	Ferroelectric polymers for neuromorphic computing. **2022**, 9, 021309	2
48	Physical Compact Model for Three-Terminal SONOS Synaptic Circuit Element. 2200070	1
47	Ionic liquid multistate resistive switching characteristics in two terminal soft and flexible discrete channels for neuromorphic computing. **2022**, 8,	2
46	Ta/HfO2-based Memristor and Crossbar Arrays for In-Memory Computing. **2022**, 167-188	
45	2022 roadmap on neuromorphic devices & applications research in China.	
44	VSDCA: A Voltage Sensing Differential Column Architecture Based on 1T2R RRAM Array for Computing-in-Memory Accelerators. **2022**, 1-14	0
43	Overview of Memristor-Based Neural Network Design and Applications. 10,	0
42	Convergence of a Class of Delayed Neural Networks with Real Memristor Devices. **2022**, 10, 2439	0
41	A new nano-structure design of a 2 ⊠ 2 crossbar switch based on quantum dots for a nano-communication network. **2022**, 169753	0
40	Perspective on Nanofluidic Memristors: from Mechanism to Application.	1
39	AMemImp: Novel Analog Memimpedance Device and Circuits for MAC Unit. **2022**,	
38	Memristive Circuit Implementation of Operant Cascaded With Classical Conditioning. **2022**, 1-13	1
37	Stacked One-Selector-One-Resistive Memory Crossbar Array With High Nonlinearity and On-Current Density for the Neuromorphic Applications. 2200656	0
36	Neuromorphic artificial intelligence systems. 16,	1
35	Self-organization of an inhomogeneous memristive hardware for sequence learning. **2022**, 13,	0
34	Neuromorphic Computing Based on Memristor Dynamics. **2022**, 1-31	0
33	Convolutional Neural Network Based on Crossbar Arrays of (Co-Fe-B)x(LiNbO3)100⊠ Nanocomposite Memristors. **2022**, 12, 3455	0
32	Analog Tunnel Memory Based on Programmable Metallization for Passive Neuromorphic Circuits. **2022**, 14, 47941-47951	0

31	Essential Characteristics of Memristors for Neuromorphic Computing. 2200833	1
30	All-atomristor logic gates.	0
29	Experimentally validated memristive memory augmented neural network with efficient hashing and similarity search. **2022**, 13,	1
28	Convergence of Neural Networks with a Class of Real Memristors with Rectifying Characteristics. **2022**, 10, 4024	0
27	Dynamic Precision Analog Computing for Neural Networks. **2022**, 1-13	0
26	Design and Implementation of a Flexible Neuromorphic Computing System for Affective Communication via Memristive Circuits. **2022**, 1-7	1
25	A tool for emulating neuromorphic architectures with memristive models and devices. **2022**,	0
24	Combinatorial Optimization in Hopfield Networks with Noise and Diagonal Perturbations. **2022**,	0
23	HYPERLOCK: In-Memory Hyperdimensional Encryption in Memristor Crossbar Array. **2022**,	0
22	A Dynamic Charge-Transfer-Based Crossbar with Low Sensitivity to Parasitic Wire-Resistance. **2022**,	0
21	A MoS 2 Hafnium Oxide Based Ferroelectric Encoder for Temporal-Efficient Spiking Neural Network. 2204949	0
20	A Novel In-Sensor Computing Architecture Based on Single Photon Avalanche Diode and Dynamic Memristor. **2022**, 489-500	0
19	A memristive deep belief neural network based on silicon synapses. **2022**, 5, 870-880	1
18	Reconfigurable neuromorphic memristor network for ultralow-power smart textile electronics. **2022**, 13,	4
17	Humanlike spontaneous motion coordination of robotic fingers through spatial multi-input spike signal multiplexing. **2023**, 14,	0
16	Gate-tunable plasticity in artificial synaptic devices based on four-terminal amorphous gallium oxide memristors.	0
15	Threshold-type memristor-based crossbar array design and its application in handwritten digit recognition. **2023**, 1-11	0
14	Interface engineering of amorphous gallium oxide crossbar array memristors for neuromorphic computing.	0

13	Memristive Cosine-Similarity-Based Few-Shot Learning with Lifelong Memory Adaptation. 2200173	0
12	Memristive True Random Number Generator with Intrinsic Two-Dimensional Physical Unclonable Function. **2023**, 5, 714-720	0
11	Synaptic Plasticity of a Microfluidic Memristor with a Temporary Memory Function Based on an Ionic Liquid in a Capillary Tube. **2023**, 127, 3307-3315	0
10	Roadmap on artificial intelligence and big data techniques for superconductivity. **2023**, 36, 043501	0
9	Implementing hardware primitives based on memristive spatiotemporal variability into cryptography applications. **2023**, 2, 100040	1
8	Combination of Organic-Based Reservoir Computing and Spiking Neuromorphic Systems for a Robust and Efficient Pattern Classification. 2200407	0
7	From memristive devices to neuromorphic systems. **2023**, 122, 110501	0
6	High-speed CMOS-free purely spintronic asynchronous recurrent neural network. **2023**, 1, 016107	0
5	Self-Curable Synaptic Ferroelectric FET Arrays for Neuromorphic Convolutional Neural Network.	0
4	Natural Exponential and Three-Dimensional Chaotic System.	0
3	Efficient Signed Arithmetic Multiplication on Memristor-Based Crossbar. **2023**, 11, 33964-33978	0
2	Memristor-Based Signal Processing for Compressed Sensing. **2023**, 13, 1354	0
1	Integrated Memristor Network for Physiological Signal Processing.	0