

Integrating photonics with silicon nanoelectronics for t a chip

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Citation Report

#	ARTICLE	IF	CITATIONS
1	A Novel photodetector with the embedded field-induced p-n photodiode in the SOI Substrate. , 2018, , .		0
2	Strategy Design for Controlling On-Chip Photonic Circuits. , 2018, , .		0
3	A general strategy for printing colloidal nanomaterials into one-dimensional micro/nanowires. Nanoscale, 2018, 10, 22374-22380.	2.8	20
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6	Open-Access Silicon Photonics: Current Status and Emerging Initiatives. Proceedings of the IEEE, 2018, 106, 2313-2330.	16.4	164
7	Monolithic III-V nitride photonic circuit towards on-chip optical interconnection. Applied Physics Express, 2018, 11, 122201.	1.1	14
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9	1.5 μm quantum-dot diode lasers directly grown on CMOS-standard (001) silicon. Applied Physics Letters, 2018, 113, .	1.5	50
10	Meditation mathematical formalism and Lorentz factor calculation based-on Mindfulness foundation. Results in Physics, 2018, 11, 1034-1038.	2.0	2
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20	Towards heterogeneous integration of optical isolators and circulators with lasers on silicon [Invited]. <i>Optical Materials Express</i> , 2018, 8, 2471.	1.6	43
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26	Building blocks of silicon photonics. <i>Semiconductors and Semimetals</i> , 2019, 101, 1-41.	0.4	3
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