

# CITATION REPORT

List of articles citing

**Efficient arithmetic on ARM-NEON and its application for high-speed RSA implementation**

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**Security and Communication Networks, 2016, 9, 5401-5411.**

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#	Paper	IF	Citations
12	Enhancement of ElGamal signature scheme using multi-processing system. <b>2017</b> ,		
11	An Intra-Slice Security Solution for Emerging 5G Networks Based on Pseudo-Random Number Generators. <i>IEEE Access</i> , <b>2018</b> , 6, 16149-16164	3.5	18
10	Cryptanalysis of Compact-LWE and Related Lightweight Public Key Encryption. <i>Security and Communication Networks</i> , <b>2018</b> , 2018, 1-9	1.9	2
9	Parallel Implementations of CHAM. <i>Lecture Notes in Computer Science</i> , <b>2019</b> , 93-104	0.9	
8	Fast Number Theoretic Transform for Ring-LWE on 8-bit AVR Embedded Processor. <i>Sensors</i> , <b>2020</b> , 20,	3.8	2
7	Enhancing Cloud Security Using Secured Binary-DNA Approach with Impingement Resolution and Complex Key Generation. <i>Lecture Notes on Data Engineering and Communications Technologies</i> , <b>2021</b> , 159-171	0.4	0
6	SIKE in 32-bit ARM Processors Based on Redundant Number System for NIST Level-II. <i>Transactions on Embedded Computing Systems</i> , <b>2021</b> , 20, 1-23	1.8	1
5	SMCOS: Fast and Parallel Modular Multiplication on ARM NEON Architecture for ECC. <i>Lecture Notes in Computer Science</i> , <b>2021</b> , 531-550	0.9	
4	Secure Number Theoretic Transform and Speed Record for Ring-LWE Encryption on Embedded Processors. <i>Lecture Notes in Computer Science</i> , <b>2018</b> , 175-188	0.9	1
3	Ring-LWE on 8-Bit AVR Embedded Processor. <i>Lecture Notes in Computer Science</i> , <b>2020</b> , 315-327	0.9	
2	Fast and efficient parallel breadth-first search with power-law graph transformation. <i>Frontiers of Computer Science</i> , <b>2022</b> , 16, 1	2.2	
1	FLIA: Architecture of Collaborated Mobile GPU and FPGA Heterogeneous Computing. <b>2022</b> , 11, 3756		0