Speeding-up heuristic allocation, scheduling and binding with SAT-based abstraction/refinement techniques

Citation Report

#	ARTICLE	IF	CITATIONS
1	Design space exploration for an embedded processor with flexible datapath interconnect. , 2010, , .		9
2	Efficient SAT Solving under Assumptions. Lecture Notes in Computer Science, 2012, , 242-255.	1.3	32
3	SAT based low power binding to reduce switching activity. , 2013, , .		0
4	The Incremental Satisfiability Problem for a Two Conjunctive Normal Form. Electronic Notes in Theoretical Computer Science, 2016, 328, 31-45.	0.9	3
5	Speeding Up Assumption-Based SAT. Lecture Notes in Computer Science, 2019, , 164-182.	1.3	7
6	Ultimately Incremental SAT. Lecture Notes in Computer Science, 2014, , 206-218.	1.3	17