Flexible Interconnection Network for Dynamically and Architectures

Citation Report

#	ARTICLE	IF	CITATIONS
1	R2NoC: Dynamically Reconfigurable Routers for Flexible Networks on Chip. , 2010, , .		2
2	Communication service for hardware tasks executed on dynamic and partial reconfigurable resources. , 2011, , .		3
3	Re^{2}DA: Reliable and reconfigurable dynamic architecture. , 2011, , .		3
4	Survey of Dynamically Reconfigurable Network-on-Chip. , 2011, , .		5
5	Design Methodology of Dynamically Reconfigurable Network-on-Chip. Lecture Notes in Electrical Engineering, 2011, , 111-116.	0.4	0
6	Hardware OS Communication Service and Dynamic Memory Management for RSoCs. , 2011, , .		2
7	An area-efficient partially reconfigurable crossbar switch with low reconfiguration delay. , 2012, , .		10
8	OCEAN, a flexible adaptive Network-On-Chip for dynamic applications. Microprocessors and Microsystems, 2014, 38, 337-357.	2.8	0
9	A fast and accurate network-on-chip timing simulator with a flit propagation model. , 2015, , .		2
10	Exploring the performance of partially reconfigurable point-to-point interconnects. , 2017, , .		2
11	A Review of Research on Network-on-Chip Simulator. Lecture Notes in Electrical Engineering, 2011, , 103-110.	0.4	3