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Tree-Structured Linear Cellular Automata and Their Applications in BIST

DOI: 10.1023/a:1008349210156 Journal of Electronic Testing: Theory and Applications (JETTA), 1999, 15, 297-300.

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2	Speed-area optimized VLSI architecture of multi-bit cellular automaton cell based random number generator on FPGA with testable logic support. <i>Journal of Parallel and Distributed Computing</i> , 2021 , 151, 13-23	4.4	1
1	FPGA fabric conscious architecture design and automation of speed-area efficient Margolus neighborhood based cellular automata with variegated scan path insertion. <i>Journal of Parallel and Distributed Computing</i> , 2022 , 167, 50-63	4.4	1