Citation Report
List of articles citing

A theory of timed automata

DOI: 10.1016/0304-3975(94)90010-8
Theoretical Computer Science, 1994, 126, 183-235.

Source: https://exaly.com/paper-pdf/25096301/citation-report.pdf
Version: 2024-04-28

This report has been generated based on the citations recorded by exaly.com for the above article. For the latest version of this publication list, visit the link given above.
The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

#	Paper	IF	Citations
2272	. **1990**,		4
2271	Specifying timed state sequences in powerful decidable logics and timed automata. **1994**, 694-715		73
2270	The observational power of clocks. **1994**, 162-177		15
2269	Untiming timed languages. **1995**, 55, 129-135		12
2268	The algorithmic analysis of hybrid systems. *Theoretical Computer Science*, **1995**, 138, 3-34	1.1	1184
2267	Reachability analysis of dynamical systems having piecewise-constant derivatives. *Theoretical Computer Science*, **1995**, 138, 35-65	1.1	174
2266	Modeling and analysis of temporal constraints in communication protocols. **1995**, 50, 907-919		1
2265	Table of contents. **1995**, 10, c1-c1		
2264	Computing simulations on finite and infinite graphs.		142
2263	Model-checking for real-time systems. **1995**, 62-88		96
2262	On the synthesis of discrete controllers for timed systems. **1995**, 229-242		234
2261	What's decidable about hybrid automata?. **1995**,		228
2260	A user guide to HyTech. **1995**, 41-71		94
2259	HYTECH: the next generation.		82
2258	Compositional and symbolic model-checking of real-time systems.		66
2257	Two examples of verification of multirate timed automata with Kronos.		38
2256	Algorithmic analysis of nonlinear hybrid systems. **1995**, 225-238		54

2255	From timed automata to logic and back. **1995**, 529-539	47
2254	Strategic directions in concurrency research. **1996**, 28, 607-625	34
2253	The benefits of relaxing punctuality. **1996**, 43, 116-146	393
2252	Specification and verification of real-time properties using LOTOS and SQTL.	1
2251	The theory of hybrid automata.	545
2250	Timed Condition/Event systems: A framework for modular discrete models of chemical plants and verification of their real-time discrete control. **1996**, 225-240	5
2249	Automatic symbolic verification of embedded systems. **1996**, 22, 181-201	290
2248	Hardware timing verification using KRONOS.	13
2247	Verification of asynchronous circuits using time Petri net unfolding.	5
2246	Timing analysis in COSPAN. **1996**, 220-231	48
2245	Verifying clocked transition systems. **1996**, 13-40	18
2244	Deciding reachability for planar multi-polynomial systems. **1996**, 389-400	6
2243	Symbolic bisimulation for timed processes. **1996**, 321-335	2
2242	Logics vs. automata: The hybrid case. **1996**, 531-542	1
2241	Diagnostic model-checking for real-time systems. **1996**, 575-586	6
2240	Fischer's protocol revisited: A simple proof using modal constraints. **1996**, 604-615	14
2239	Checking linear duration invariants by linear programming. **1996**, 321-332	12
2238	Test sequence generation and timed testing. **1996**, 29, 107-131	5

2237	Action transducers and timed automata. **1996**, 8, 499-538	17
2236	Validation of protocols with temporal constraints. **1996**, 19, 1188-1199	3
2235	The tool Kronos. **1996**, 208-219	179
2234	Reducing the number of clock variables of timed automata.	49
2233	Minimizable timed automata. **1996**, 130-147	18
2232	Testing complex temporal relationships involving multiple granularities and its application to data mining (extended abstract). **1996**,	26
2231	Verification of asynchronous circuits using time Petri net unfolding. **1996**,	17
2230	Automatic effective verification method for distributed and concurrent systems using timed language inclusion.	
2229	A simplification of the untiming procedure for timed automata.	
2228	Data-structures for the verification of timed automata. **1997**, 346-360	41
2227	Some progress in the symbolic verification of timed automata. **1997**, 179-190	38
2226	HyTech: A model checker for hybrid systems. **1997**, 460-463	124
2225	Symbolic analysis of hybrid systems.	5
2224	The bounded retransmission protocol must be on time!. **1997**, 416-431	42
2223	Modellierung und Analyse hybrider dynamischer Systeme. **1997**, 45, 152-162	5
2222	Synthesising controllers from real-time specifications.	13
2221	Combining a computer science and control theory approach to the verification of hybrid systems.	
2220	A compositional proof of a real-time mutual exclusion protocol. **1997**, 565-579	12

2219 From finite automata toward hybrid systems (Extended abstract). **1997**, 411-422 5

2218 Removing □-transitions in timed automata. **1997**, 583-594 14

2217 Efficient scaling-invariant checking of timed bisimulation. **1997**, 177-188 16

2216 A computational analysis of the reachability problem for a class of hybrid dynamical systems. **1997**, 215-227

2215 Robust timed automata. **1997**, 331-345 70

2214 Accepting Zeno words without making time stand still. **1997**, 149-158 9

2213 Modelling a time-dependent protocol using the Circal process algebra. **1997**, 124-138 5

2212 The Railroad Crossing Problem: Towards semantics of timed algorithms and their model checking in high level languages. **1997**, 201-212 2

2211 Software design, specification, and verification: Lessons learned from the Rether case study. **1997**, 185-198 4

2210 State structures for verification and real-time control of hybrid automata.

2209 A Kleene theorem for timed automata. 26

2208 PLC-automata: A new class of implementable real-time automata. **1997**, 111-125 27

2207 On-the-fly symbolic model checking for real-time systems. 20

2206 VERDICT-a tool for model-based verification of real-time logic process controllers.

2205 A constraint-based approach for specification and verification of real-time systems. 26

2204 Timed automata and robust control: can we now control complex dynamical systems?. 6

2203 Comparing timed and hybrid automata as approximations of continuous systems. **1997**, 361-377 17

2202 State clock logic: A decidable real-time logic. **1997**, 33-47 35

2201	Decidability of hybrid systems with linear and nonlinear differential inclusions. **1997**, 77-92		
2200	Time-Abstracted Bisimulation: Implicit Specifications and Decidability. **1997**, 134, 75-101		34
2199	Computing Accumulated Delays in Real-time Systems. **1997**, 11, 137-155		17
2198	Continuous modeling of real-time and hybrid systems: from concepts to tools. **1997**, 1, 64-85		11
2197	Real-time system = discrete system + clock variables. **1997**, 1, 86-109		17
2196	HYTECH: a model checker for hybrid systems. **1997**, 1, 110-122		431
2195	KRONOS: a verification tool for real-time systems. **1997**, 1, 123-133		338
2194	Uppaal in a nutshell. **1997**, 1, 134-152		1143
2193	Formal methods integration for the specification of dependable distributed systems. **1997**, 43, 671-685		1
2192	On the rational behaviors of concurrent timers. *Theoretical Computer Science*, **1997**, 189, 229-237	1.1	1
2191	A method for the specification and verification of distributed systems by a timed automaton. **1997**, 28, 11-20		
2190	Timed automata and recognizability. **1998**, 65, 313-318		16
2189	Supervisor Synthesis for Real-Time Discrete Event Systems. **1998**, 8, 5-35		7
2188	Modelling and analysis of hybrid systems. **1998**, 46, 445-464		16
2187	What's Decidable about Hybrid Automata?. **1998**, 57, 94-124		408
2186	A LOTOS based tutorial on formal methods for object-oriented distributed systems. **1998**, 16, 343-372		1
2185	Partial-order Reduction Techniques for Real-time Model Checking. **1998**, 10, 469-482		19
2184	Automatic Verification of a Lip-Synchronisation Protocol Using Uppaal. **1998**, 10, 550-575		23

2183	Using model-checking for timed automata to parameterize logic control programs. **1998**, 22, S875-S878	1
2182	Specifying Real-Time Finite-State Systems in Linear Logic (Extended Abstract). **1998**, 16, 42-59	6
2181	Straightening out rectangular differential inclusions. **1998**, 35, 79-85	3
2180	Formal methods for broadband and multimedia systems. **1998**, 30, 865-899	4
2179	Petri net based modeling and analysis of switched systems.	
2178	Hidden time model for specification and verification of embedded systems.	0
2177	Verification of the fast reservation protocol with delayed transmission using the tool KRONOS.	3
2176	A geometric approach to bisimulation and verification of hybrid systems.	6
2175	On the fly test generation for real time protocols.	6
2174	A state graph manipulator tool for real-time system specification and verification.	8
2173	On checking timed automata for linear duration invariants.	3
2172	Membership questions for timed and hybrid automata.	7
2171	Timed test cases generation based on state characterization technique.	36
2170	The very idea of software development environments: a conceptual architecture for the arts' environment paradigm.	4
2169	Formal methods in real-time systems.	1
2168	Accounting for temporal evolutions in highly reactive decision-making.	
2167	Algorithmic analysis of nonlinear hybrid systems. **1998**, 43, 540-554	159
2166	Verified hybrid controllers for automated vehicles. **1998**, 43, 522-539	197

2165	Kronos: A model-checking tool for real-time systems. **1998**, 546-550	118
2164	The design and verification of a high-performance low-control-overhead asynchronous differential equation solver. **1998**, 6, 643-655	30
2163	Conflict resolution for air traffic management: a study in multiagent hybrid systems. **1998**, 43, 509-521	640
2162	On the Decidability of Continuous Time Specification Formalisms. **1998**, 8, 669-678	27
2161	Semantics and verification of action diagrams with linear timing. **1998**, 3, 21-50	9
2160	Compositional model-checking for real-time systems. **1998**, 23, 99	
2159	Pumping Lemmas for timed automata. **1998**, 81-94	6
2158	An equivalence theorem for the operational and temporal semantics of real-time, concurrent programs. **1998**, 8, 545-567	1
2157	Finitary fairness. **1998**, 20, 1171-1194	25
2156	Kronos: A model-checking tool for real-time systems. **1998**, 298-302	20
2155	MOCHA: Modularity in model checking. **1998**, 521-525	153
2154	Supervisory control design for real-time discrete event systems. **1998**,	
2153	. **1998**, 10, 222-237	74
2152	Modeling and analysis of time invariant linear hybrid systems.	1
2151	Timed Formal Languages - A Framework for Hybrid Systems. **1998**, 31, 249-252	
2150	Application of Cfsmcharts for Modelling Real-Time and Industrial Embedded Systems. **1998**, 31, 91-98	
2149	Supervisor Control Design of Hybrid Systems Using Timed Petri Nets Based on Invariant Properties. **1998**, 31, 689-694	
2148	Computing the Untimed Language of a Buechi Timed Automaton. **1998**, 31, 441-446	

2147 Controller Synthesis for Timed Automata. **1998**, 31, 447-452 63

2146 Model checking via reachability testing for timed automata. **1998**, 263-280 36

2145 On discretization of delays in timed automata and digital circuits. **1998**, 470-484 27

2144 Logic programming and model checking. **1998**, 1-20 16

2143 Formal design and analysis of a gear controller. **1998**, 281-297 28

2142 Partition refinement in real-time model checking. **1998**, 143-157 13

2141 Synchronizing clocked transition systems. **1998**, 173-185

2140 The regular real-time languages. **1998**, 580-591 54

2139 On the expressiveness of real and integer arithmetic automata. **1998**, 152-163 25

2138 Verifying systems with infinite but regular state spaces. **1998**, 88-97 81

2137 Operational and logical semantics for polling real-time systems. **1998**, 29-40 12

2136 Moby/PLC Graphical development of PLC-automata. **1998**, 311-314 8

2135 Controlled timed automata. **1998**, 455-469 11

2134 Timed bisimulation and open maps. **1998**, 378-387 6

2133 Discrete-time Promela and Spin. **1998**, 307-310 15

2132 Optikron: A tool suite for enhancing model-checking of real-time systems. **1998**, 542-545 2

2131 Supervisory control of timed automata. **1999**, 1

2130 Modeling and Interoperability Test Case Generation of a Real-Time QoS Monitoring Protocol. **1999**, 21, 52-64 0

2129	Analysis of the coupled linear hybrid automata dynamics. **1999**,	
2128	Hybrid automata models in continuous-linear hybrid systems analysis. **1999**,	
2127	Decidability of single-rate hybrid Petri nets.	
2126	Temporal coherence of timed automata product.	
2125	Model-Checking. **1999**, 330-354	56
2124	As Soon as Possible: Time Optimal Control for Timed Automata. **1999**, 19-30	65
2123	A symbolic model checker for testing ASTRAL real-time specifications.	
2122	Efficient Verification of Timed Automata Using Dense and Discrete Time Semantics. **1999**, 125-141	22
2121	Timed Sequence Diagrams and Tool-Based Analysis A Case Study. **1999**, 645-660	26
2120	On Hybrid Systems and the Modal -calculus. **1999**, 38-69	8
2119	Temporal Petri nets, a modeling and control tool of manufacturing lines. **1999**,	
2118	A modular tool for test generation for real-time systems. **1999**,	1
2117	Timed Models. **1999**, 275-316	
2116	Product Interval Automata: A Subclass of Timed Automata. **1999**, 60-71	5
2115	Timing coverification of concurrent embedded real-time systems.	1
2114	Towards the automatic generation of quality-of-service-preserving implementations from formal specifications. **1999**, 22, 211-223	
2113	A service creation environment based on scenarios. **1999**, 41, 697-713	11
2112	A framework for the specification of test cases for real-time distributed systems. **1999**, 41, 781-798	10

2111	Testing protocols modeled as FSMs with timing parameters. **1999**, 31, 1967-1988		11
2110	Test development for communication protocols: towards automation. **1999**, 31, 1835-1872		45
2109	Event-clock automata: a determinizable class of timed automata. *Theoretical Computer Science*, **1999**, 211, 253-273	1.1	128
2108	Symbolic reachability analysis of FIFO-channel systems with nonregular sets of configurations. *Theoretical Computer Science*, **1999**, 221, 211-250	1.1	48
2107	Discrete-time control for rectangular hybrid automata. *Theoretical Computer Science*, **1999**, 221, 369-392	1.1	70
2106	Automated Verification of Function Block Based Industrial Control Systems. **1999**, 25, 97-110		1
2105	Decidable Integration Graphs. **1999**, 150, 209-243		30
2104	Timed Automata. **1999**, 8-22		164
2103	Hybrid Verifications of Reactive Programs. **1999**, 11, 448-471		1
2102	Local model checking and protocol analysis. **1999**, 2, 219-241		13
2101	Partial model checking of modal equations: A survey. **1999**, 2, 242		9
2100	From discrete-event systems to hybrid systems.		1
2099	Experiments with parametric verification of real-time systems.		
2098	A realistic architecture for timed testing.		4
2097	Fault coverage in testing real-time systems.		15
2096	Interval diagrams: increasing efficiency of symbolic real-time verification.		4
2095	Formal description of real-time systems using SDL.		0
2094	Specification of real-time systems using a timed automata model with shared variables and verification of partial-deadlock freeness.		0

2093	An efficiently checkable subset of TCTL for formal verification of transition systems with delays. **1999**,	1
2092	Computer Safety, Reliability and Security. **1999**,	2
2091	Timed automaton models for simple programmable logic controllers.	35
2090	A framework for scheduler synthesis.	30
2089	Hardware-software coverification of concurrent embedded real-time systems.	0
2088	Handling contingency in temporal constraint networks: from consistency to controllabilities. **1999**, 11, 23-45	105
2087	Modular verification of a fault-tolerant active structure controller: an example.	
2086	An environment for model-checking of logic control systems with hybrid dynamics.	4
2085	Bounding average time separations of events in stochastic timed Petri nets with choice.	11
2084	Specification and analysis of soft real-time systems: quantity and quality.	4
2083	Synthesizing controllers from real-time specifications. **1999**, 18, 33-43	8
2082	Scheduling a steel plant with timed automata.	26
2081	Automatic Verification of Real-Time Systems with Discrete Probability Distributions. **1999**, 75-95	11
2080	Supervisory hybrid systems. **1999**, 19, 42-55	50
2079	Temporal Logic. **1999**,	1
2078	Modular Verification of Function Block Based Industrial Control Systems. **1999**, 32, 159-164	2
2077	From Timed Automata to Testable Untimed Automata. **1999**, 32, 189-194	4
2076	Optimal control for timed automata 1. **1999**, 32, 2131-2136	

2075 Reachability computation for linear hybrid systems. **1999**, 32, 2137-2142 8

2074 Timed approximations of hybrid processes for controller verification. **1999**, 32, 4782-4787

2073 Parametric analysis for linear hybrid systems. **1999**, 32, 4805-4809

2072 Computation of controllers for nonlinear hybrid systems 1. **1999**, 32, 2149-2154 1

2071 An open tool architecture for the formal verification of logic controllers in processing systems. **1999**, 32, 4828-4833

2070 Characteristic Formulae for Timed Automata. **2000**, 34, 565-584 10

2069 Modular synthesis of efficient schedules in a timed discrete event plant. 3

2068 Timed automata and additive clock constraints. **2000**, 75, 1-7 18

2067 Embedded software verification in hardware software codesign. **2000**, 46, 1435-1450 22

2066 Automatic Synthesis of Schedulers in Timed Systems . **2000**, 31, 118-131 1

2065 Distributed Timed Automata. **2000**, 28, 5-21 8

2064 Real-Time Verification Techniques for Untimed Systems. **2000**, 39, 258-272 2

2063 Hardware-software timing coverification of concurrent embedded real-time systems. **2000**, 147, 83 8

2062 Proving the Correctness of the Standardized Algorithm for ABR Conformance. **2000**, 17, 221-243

2061 Accepting Zeno words: a way toward timed refinements. **2000**, 37, 45-81 7

2060 Conformance Tests for Real-Time Systems with Timed Automata Specifications. **2000**, 12, 350-371 48

2059 Modelling and Verifying of a Cash-Point Service Using MOBY/PLC. **2000**, 12, 220-221 2

2058 Composing and Refining Dense Temporal Logic Specifications. **2000**, 12, 52-70 1

2057	O-Minimal Hybrid Systems. **2000**, 13, 1-21	165
2056	Temporal validation of multimedia protocols. **2000**, 55, 31-44	
2055	Analysis of real-time code by model checking.	
2054	Fluid models and hybrid automata in manufacturing.	1
2053	Supervisory control for rectangular hybrid automata.	3
2052	Abstraction from counters. **2000**,	2
2051	Testing timed systems with timed purposes.	3
2050	Abstraction from counters: an application on real-time systems.	
2049	Characterization of well-posedness of piecewise-linear systems. **2000**, 45, 1600-1619	131
2048	Foundations of Software Science and Computation Structures. **2000**,	
2047	Graphical animation of behavior models. **2000**,	26
2046	The Theory of Hybrid Automata. **2000**, 265-292	163
2045	An efficient state space generation for the analysis of real-time systems. **2000**, 26, 453-477	11
2044	Control synthesis of timed discrete event systems based on predicate invariance. **2000**, 30, 713-24	9
2043	Testing embedded real-time systems.	4
2042	STACS 2000. **2000**,	
2041	Performance Analysis of Asynchronous Circuits and Systems Using Stochastic Timed Petri Nets. **2000**, 239-268	9
2040	Communication protocols for a fault-tolerant automated highway system. **2000**, 8, 787-800	20

2039	Duration properties over real time system designs.	
2038	Parametric verification of the IEEE 1394a Root Contention protocol using LPMC.	1
2037	Interval diagrams for efficient symbolic verification of process networks. **2000**, 19, 939-956	12
2036	Hybrid Systems: Computation and Control. **2000**,	14
2035	Approximate Reachability Analysis of Piecewise-Linear Dynamical Systems. **2000**, 20-31	110
2034	Discrete Event Systems. **2000**,	3
2033	Hybrid model visualization in requirements and design: a preliminary investigation.	0
2032	A new method for testing real time systems.	9
2031	Concurrent embedded real-time software verification.	
2030	Verifying temporal constraints on data in multi-rate transactions using timed automata.	2
2029	Tabled resolution + constraints: a recipe for model checking real-time systems.	
2028	.	12
2027	Adaptative timed tests for temporal interoperability verification.	
2026	Formal verification of safety properties in timed circuits.	7
2025	Model-checking real-time control programs: verifying LEGO MINDSTORMSTM systems using UPPAAL. **2000**,	18
2024	On the Generation of Timed Discrete Approximations for Continuous Systems. **2000**, 6, 51-70	25
2023	. **2000**, 88, 926-948	58
2022	A game theoretic approach to controller design for hybrid systems. **2000**, 88, 949-970	301

2021	Discrete abstractions of hybrid systems. **2000**, 88, 971-984	465
2020	Logics for hybrid systems. **2000**, 88, 985-1010	87
2019	Effective synthesis of switching controllers for linear systems. **2000**, 88, 1011-1025	120
2018	Continuous-discrete interactions in chemical processing plants. **2000**, 88, 1050-1068	100
2017	Tools and Algorithms for the Construction and Analysis of Systems. **2001**,	1
2016	Reduction methods for real-time systems using Delay Time Petri Nets. **2001**, 27, 422-448	24
2015	Verifying Untimed and Timed Aspects of the Experimental Batch Plant. **2001**, 7, 400-415	5
2014	Computing Efficient Operation Schemes for Chemical Plants in Multi-batch Mode. **2001**, 7, 440-453	5
2013	Verifiable Embedded Real-Time Application Framework.	4
2012	A rigorous method for testing real-time reactive systems.	2
2011	Using 'parallel automaton' as a single notation to specify, design and control small computer based systems.	1
2010	Multiple enabledness of transitions in Petri nets with time.	10
2009	Validating real-time constraints in embedded systems.	1
2008	Real-time computation: a formal definition and its applications.	0
2007	Embedded Software. **2001**,	6
2006	Modeling and Verification of Parallel Processes. **2001**,	2
2005	Computer Aided Verification. **2001**,	4
2004	Fundamentals of Computation Theory. **2001**,	

2003	Concurrency in Timed Automata. **2001**, 240-251		
2002	Composition and refinement of behavioral specifications.		8
2001	Specifying and verifying a railroad crossing with cafeOBJ.		1
2000	.		6
1999	Decentralized failure diagnosis with asynchronous communication between supervisors. **2001**,		9
1998	. **2001**,		79
1997	Distributed autonomous control of concurrent combat tasks. **2001**,		1
1996	Hybrid system model of production work-cells and its optimal control subject to logical constraints.		
1995	An application of discrete-event theory to truck dispatching. **2001**,		1
1994	Analysis and Control of Manufacturing Systems: A Hybrid Approach. **2001**, 34, 487-492		
1993	Discrete Transition Enabling Properties Applied to a Hybrid Automation Model. **2001**, 34, 499-504		
1992	Closed Interval Process Algebra versus Interval Process Algebra **2001**, 37, 467-509		8
1991	Pursuit and Evasion on a Ring: An Infinite Hierarchy for Parallel Real-Time Systems. **2001**, 34, 565-576		2
1990	Timed tree automata with an application to temporal logic. **2001**, 38, 89-116		9
1989	Formal design and analysis of a gear controller. **2001**, 3, 353-368		34
1988	Mechanical verification of the IEEE 1394a root contention protocol using Uppaal2k. **2001**, 3, 469-485		27
1987	A first order logic for specification of timed algorithms: basic properties and a decidable class. **2001** , 113, 13-52		8
1986	Refinement of time. *Theoretical Computer Science*, **2001**, 253, 3-26	1.1	21

1985	Deductive verification of real-time systems using STeP. *Theoretical Computer Science*, **2001**, 253, 27-60	1.1	17
1984	PLC-automata: a new class of implementable real-time automata. *Theoretical Computer Science*, **2001**, 253, 61-93	1.1	33
1983	Well-structured transition systems everywhere!. *Theoretical Computer Science*, **2001**, 256, 63-92	1.1	430
1982	A polynomial-time algorithm for checking equivalence under certain semiring congruences motivated by the state-space isomorphism problem for hybrid systems. *Theoretical Computer Science*, **2001**, 262, 161-189	1.1	3
1981	Testing timed automata. *Theoretical Computer Science*, **2001**, 254, 225-257	1.1	166
1980	Locally finite languages. *Theoretical Computer Science*, **2001**, 255, 223-261	1.1	9
1979	Fast asynchronous systems in dense time. *Theoretical Computer Science*, **2001**, 254, 379-422	1.1	14
1978	Metric semantics for true concurrent real time. *Theoretical Computer Science*, **2001**, 254, 501-542	1.1	14
1977	Model-Generation of a Fictitious Clock Real-Time Logic Using Sharing Trees. **2001**, 23, 108-126		
1976	Zeno hybrid systems. **2001**, 11, 435-451		155
1975	Symbolic Reachability Computation for Families of Linear Vector Fields. **2001**, 32, 231-253		144
1974	Automatic Real-Time Analysis of Reactive Systems with the PARTS Toolset. **2001**, 8, 227-273		1
1973	Decidability results in First□Order Hybrid Petri Nets. **2001**, 11, 41-57		13
1972	Analysis of Scheduling Behaviour using Generic Timed Automata. **2001**, 42, 143-157		5
1971	A Model of Finite Automata on Timed □Trees. **2001**, 42, 158-173		
1970	Analysis of three hybrid systems in timed □RL. **2001**, 39, 215-247		5
1969	Visual temporal logic as a rapid prototyping tool. **2001**, 27, 93-113		2
1968	Asynchronous communication in timed discrete-event systems. **2001**,		0

1967	MOBY/PLC: Eine graphische Entwicklungsumgebung f SPS-Programme (MOBY/PLC: A Graphical Design Environment for PLC Programs). **2001**, 49, 38	
1966	Approaches to the Formal Verification of Hybrid Systems. **2001**, 49, 66	2
1965	Formal verification of embedded real-time software in component-based application frameworks.	2
1964	Testing distributed real time systems using a distributed test architecture.	2
1963	Application of parametric model checking - the Root Contention protocol.	4
1962	An approach for the analysis and control of hybrid systems.	1
1961	ON REACHABILITY AND SAFETY IN INFINITE-STATE SYSTEMS. **2001**, 12, 821-836	5
1960	Practical Aspects of Declarative Languages. **2001**,	2
1959	Real-time protocol analysis for detecting link-state routing protocol attacks. **2001**, 4, 1-36	26
1958	STACS 2001. **2001**,	2
1957	Verification of hybrid systems: formalization and proof rules in PVS.	5
1956	Software Reliability Methods. **2001**,	84
1955	Some parameters for timed system testability.	1
1954	An Internet multicast system for the stock market. **2001**, 19, 384-412	19
1953	Perspectives of System Informatics. **2001**,	1
1952	Measuring behavioral correspondence to a timed concurrent model.	10
1951	Test generation for time critical systems: Tool and case study.	5
1950	Static Analysis. **2001**,	3

1949	A method for verifying real-time properties of Ada programs.	0
1948	Dense real-time games.	11
1947	Timed discrete-event method for diagnosis of industrial actuators.	
1946	Introduction to Constraint Databases. **2002**,	
1945	Formal methods for specification and analysis of communication protocols. **2002**, 4, 2-20	23
1944	Equivalence of timed state machines and safe TPN.	2
1943	Model checking UML specifications of real time software.	4
1942	Splitting trees and partition refinement in real-time model checking.	1
1941	Model Checking Timed UML State Machines and Collaborations. **2002**, 395-414	96
1940	Extending synchronous languages for generating abstract real-time models.	2
1939	Specification, Implementation, and Verification of Domain Specific Languages: A Logic Programming-Based Approach. **2002**, 211-239	
1938	THE EXISTENCE OF □CHAINS FOR TRANSITIVE MIXED LINEAR RELATIONS AND ITS APPLICATIONS. **2002**, 13, 911-936	4
1937	VERIFICATION IN QUEUE-CONNECTED MULTICOUNTER MACHINES. **2002**, 13, 115-127	2
1936	Tools and Algorithms for the Construction and Analysis of Systems. **2002**,	1
1935	Design and Analysis of Distributed Embedded Systems. **2002**,	
1934	Timed regular expressions. **2002**, 49, 172-206	132
1933	A generic operator over discrete time intervals. **2002**,	
1932	Lazy abstraction. **2002**,	531

1931	Lazy abstraction. **2002**, 37, 58-70	164
1930	Submodule construction for timed systems.	
1929	A case study for the verification of complex timed circuits: IPCMOS.	
1928	Analysis of Event-Driven Real-Time Systems with Time Petri Nets. **2002**, 31-40	3
1927	The d/dt Tool for Verification of Hybrid Systems. **2002**, 365-370	77
1926	Supervisory control of dense real-time discrete-event systems with partial observation.	3
1925	Symbolic simulation of hybrid systems.	1
1924	Computing reachability relations in timed automata.	4
1923	Model checking embedded system designs.	2
1922	Embedded Software. **2002**,	1
1921	Preemptive Job-Shop Scheduling Using Stopwatch Automata. **2002**, 113-126	24
1920	Relative timing based verification of timed circuits and systems.	13
1919	Hybrid Dynamical Systems. **2002**,	124
1918	VERTAF: an object-oriented application framework for embedded real-time systems.	5
1917	Timed Wp-method: testing real-time systems. **2002**, 28, 1023-1038	80
1916	. **2002**,	20
1915	Formal Synthesis and Control of Soft Embedded Real-Time Systems. **2001**, 35-50	1
1914	Compiling Real-time Scenarios into a Timed Automaton. **2001**, 135-150	2

1913	Deriving Parameter Conditions for Periodic Timed Automata Satisfying Real-time Temporal Logic Formulas. **2001**, 151-166	1
1912	A Method to Generate Conformance Test Sequences for FSM with Timer System Call. **2001**, 301-316	
1911	Verification of Dense Time Properties Using Theories of Untimed Process Algebra. **2001**, 353-368	
1910	Is your model checker on time? On the complexity of model checking for timed modal logics. **2002**, 52-53, 7-51	20
1909	Positive loop-closed automata: a decidable class of hybrid systems. **2002**, 52-53, 79-108	4
1908	Guest editors□ntroduction: Model checking in a nutshell. **2002**, 52-53, 1-5	
1907	Linear parametric model checking of timed automata. **2002**, 52-53, 183-220	88
1906	Product interval automata. **2002**, 27, 181-208	2
1905	Checking Temporal Duration Properties of timed automata. **2002**, 17, 689-698	6
1904	Axiomatising timed automata. **2002**, 38, 277-305	7
1903	Temporal fault trees. **2002**, 44, 137-150	36
1902	Extending Timed Automata for Compositional Modeling Healthy Timed Systems. **2002**, 52, 227-245	1
1901	Privacy in Real-Time Systems. **2002**, 52, 295-305	4
1900	Characterizing Non-Zenoness on Real-Time Processes. **2002**, 52, 306-322	
1899	Automatic Verification of the IEEE-1394 Root Contention Protocol with KRONOS and PRISM . **2002**, 66, 104-119	4
1898	Translation from timed Petri nets with intervals on transitions to intervals on places (with urgency). **2002**, 65, 16-46	2
1897	Verification of Asynchronous Circuits using Timed Automata. **2002**, 65, 47-59	13
1896	TPAP: An algebra of preemptive processes for verifying real-time systems with shared resources. **2002**, 65, 68-85	2

1895	Predicate Abstraction for Dense Real-Time Systems . **2002**, 65, 218-237		0
1894	Classes of Timed Automata and the Undecidability of Universality. **2002**, 65, 238-253		
1893	Rule Formats for Timed Processes. **2002**, 68, 12-31		3
1892	Timed CSP = Closed Timed Automata1. **2002**, 68, 142-159		9
1891	A Performance Study of Distributed Timed Automata Reachability Analysis. **2002**, 68, 486-502		6
1890	Zeus: A Distributed Timed Model-Checker Based on Kronos. **2002**, 68, 503-522		5
1889	Refinement of Actions in a Real-Time Process Algebra with a True Concurrency Model. **2002**, 70, 260-280		6
1888	Process algebra for performance evaluation. *Theoretical Computer Science*, **2002**, 274, 43-87	1.1	137
1887	Automatic verification of real-time systems with discrete probability distributions. *Theoretical Computer Science*, **2002**, 282, 101-150	1.1	210
1886	Bisimulation indexes and their applications. *Theoretical Computer Science*, **2002**, 275, 1-68	1.1	38
1885	Decidable verification for reducible timed automata specified in a first order logic with time. *Theoretical Computer Science*, **2002**, 275, 347-388	1.1	5
1884	Counter Machines and Verification Problems. *Theoretical Computer Science*, **2002**, 289, 165-189	1.1	29
1883	Efficiency of asynchronous systems, read arcs, and the MUTEX-problem. *Theoretical Computer Science*, **2002**, 275, 589-631	1.1	19
1882	Specification of real-time and hybrid systems in rewriting logic. *Theoretical Computer Science*, **2002**, 285, 359-405	1.1	72
1881	Conformance testing to real-time communications systems. **2002**, 25, 32-45		4
1880	Formal Models of Timing Attacks on Web Privacy . **2002**, 62, 229-243		10
1879	On Improving Backwards Verification of Timed Automata (Extended Abstract). **2002**, 65, 60-67		1
1878	Timed Semantics of Message Sequence Charts Based on Timed Automata. **2002**, 65, 160-179		2

1877	Modular Synthesis of Timed Circuits using Partial Orders on LPNs. **2002**, 65, 180-201		1
1876	A note on the verification of automata specifications of probabilistic real-time systems. **2002**, 82, 223-228		3
1875	Bisimulation for Labelled Markov Processes. **2002**, 179, 163-193		189
1874	Applying Learning by Examples for Digital Design Automation. **2002**, 16, 205-221		1
1873	TimeC: A Time Constraint Language for ILP Processor Compilation. **2002**, 7, 75-115		2
1872	Scheduler Modeling Based on the Controller Synthesis Paradigm. **2002**, 23, 55-84		62
1871	Modular and Visual Specification of Hybrid Systems: An Introduction to HyCharts. **2002**, 21, 5-38		4
1870	Pushdown timed automata: a binary reachability characterization and safety verification. *Theoretical Computer Science*, **2003**, 302, 93-121	1.1	43
1869	How to Verify a Safe Real-Time System: The Application of Model Checking and Timed Automata to the Production Cell Case Study*. **2003**, 24, 135-151		18
1868	The ASM Refinement Method. **2003**, 15, 237-257		101
1867	Probabilistic Model Checking of Deadline Properties in the IEEE 1394 FireWire Root Contention Protocol. **2003**, 14, 295-318		57
1866	False Loop Detection in the IEEE 1394 Tree Identify Phase. **2003**, 14, 319-327		2
1865	Fun with FireWire: A Comparative Study of Formal Verification Methods Applied to the IEEE 1394 Root Contention Protocol. **2003**, 14, 328-337		17
1864	Automated test generation from timed automata. **2003**, 5, 59-77		27
1863	An algebraic approach to data languages and timed languages. **2003**, 182, 137-162		45
1862	Approximating labelled Markov processes. **2003**, 184, 160-200		63
1861	Hybrid I/O automata. **2003**, 185, 105-157		242
1860	On probabilistic timed automata. *Theoretical Computer Science*, **2003**, 292, 65-84	1.1	59

1859	Verification in loosely synchronous queue-connected discrete timed automata. *Theoretical Computer Science*, **2003**, 290, 1713-1735	1.1	5
1858	Automata over continuous time. *Theoretical Computer Science*, **2003**, 300, 331-363	1.1	14
1857	The power of reachability testing for timed automata. *Theoretical Computer Science*, **2003**, 300, 411-475	1.1	46
1856	Generalized discrete timed automata: decidable approximations for safety verification. *Theoretical Computer Science*, **2003**, 296, 59-74	1.1	8
1855	Presburger liveness verification of discrete timed automata. *Theoretical Computer Science*, **2003**, 299, 413-438	1.1	13
1854	Eliminating the storage tape in reachability constructions. *Theoretical Computer Science*, **2003**, 299, 687-706	1.1	3
1853	Finite automata on timed □trees. *Theoretical Computer Science*, **2003**, 293, 479-505	1.1	4
1852	Automatic synthesis of a subclass of schedulers in timed systems. *Theoretical Computer Science*, **2003**, 298, 347-363	1.1	
1851	Concurrency in timed automata. *Theoretical Computer Science*, **2003**, 309, 503-527	1.1	4
1850	Bounded Model Checking for Timed Automata1 1This research was supported by the National Science Foundation under grants CCR-00-82560 and CCR-00-86096.. **2003**, 68, 116-134		15
1849	Testing distributed real-time systems in the presence of inaccurate clock synchronizations. **2003**, 45, 853-864		7
1848	Implicit integration of scenarios into a reduced timed automaton. **2003**, 45, 715-725		3
1847	Systematic evaluation of fault trees using real-time model checker UPPAAL. **2003**, 82, 11-20		9
1846	Balanced timed regular expressions1 1Partially supported by the European community project IST-2001-35304 AMETIST. **2003**, 68, 16-33		2
1845	Testing and Monitoring Model-based Generated Program. **2003**, 89, 128-148		10
1844	A Guided Method for Testing Timed Input Output Automata. **2003**, 211-225		17
1843	Real-time information flow analysis. **2003**, 21, 20-35		26
1842	Making software timing properties easier to inspect and verify. **2003**, 20, 34-41		1

1841	A Solvable Class of Quadratic Diophantine Equations with Applications to Verification of Infinite-State Systems. **2003**, 668-680	10
1840	Supervisory target control for hybrid systems. **2003**, 76, 1142-1158	8
1839	Weak bisimulation for probabilistic timed automata and applications to security. **2003**,	7
1838	Branching within Time: An Expressively Complete and Elementarily Decidable Temporal Logic for Time Granularity. **2003**, 1, 229-263	6
1837	Hierarchical modeling and analysis of embedded systems. **2003**, 91, 11-28	93
1836	. **2003**, 91, 29-39	28
1835	Building models of real-time systems from application software. **2003**, 91, 100-111	22
1834	Computational techniques for the verification of hybrid systems. **2003**, 91, 986-1001	221
1833	Modular verification of timed circuits using automatic abstraction. **2003**, 22, 1138-1153	12
1832	Model-checking algorithms for continuous-time Markov chains. **2003**, 29, 524-541	456
1831	. **2003**, 29, 705-720	14
1830	Programming Methodology. **2003**,	0
1829	Parallel Computing Technologies. **2003**,	
1828	Hybrid Modelling and Control of Power Electronics. **2003**, 450-465	42
1827	Generic Transforms on Incomplete Specifications of Asynchronous Interfaces. **2003**, 83, 187-200	1
1826	Logic Programming. **2003**,	
1825	Formal modeling and verifying of FMS.	
1824	.	28

1823 BDIGGY: a cognitive architecture for cooperative problem solving. 0

1822 Model checking for probability and time: from theory to practice. 31

1821 Static Guard Analysis in Timed Automata Verification. **2003**, 254-270 40

1820 Formal Techniques for Networked and Distributed Systems - FORTE 2003. **2003**,

1819 Testing of Communicating Systems. **2003**,

1818 Tools and Algorithms for the Construction and Analysis of Systems. **2003**, 2

1817 Formal design and analysis of FMS controller.

1816 A QoS-Oriented Extension of UML Statecharts. **2003**, 76-91 16

1815 The Gurkh project: a framework for verification and execution of mission critical applications. **2003**,

1814 Real-time property preservation in approximations of timed systems. **2003**, 6

1813 Formal probabilistic refinement verification of embedded real-time systems.

1812 The Rosetta meta-model framework. 8

1811 Scheduling analysis integration for heterogeneous multiprocessor SoC. 25

1810 Automating formal modular verification of asynchronous real-time embedded systems. 6

1809 Relative timing [asynchronous design]. **2003**, 11, 129-140 44

1808 Multi-agent planning as a coordination model for self-organized systems. 1

1807 Modeling complex systems by a set of interacting finite-state models. 1

1806 Verification and Improvement of the Sliding Window Protocol. **2003**, 113-127 19

1805	On a simplified untiming procedure for supervisory control of timed automata when the time increases strictly monotonically. **2003**, 76, 1739-1748	1
1804	Modular Programming Languages. **2003**,	
1803	Rabbit: A Tool for BDD-Based Verification of Real-Time Systems. **2003**, 122-125	30
1802	A technique to generate feasible tests for communications systems with multiple timers. **2003**, 11, 796-809	22
1801	Computing and Combinatorics. **2003**,	0
1800	Hybrid control synthesis for eventuality specifications using level set methods. **2003**, 76, 1599-1627	12
1799	Revisiting digitization, robustness, and decidability for timed automata.	12
1798	A Logical Characterisation of Event Clock Automata. **2003**, 14, 625-639	17
1797	Task graph scheduling using timed automata.	14
1796	Developing a formal specification for the mission system of a maritime surveillance aircraft.	2
1795	Deadline analysis of interrupt-driven software. **2003**,	2
1794	Language equations for timed alternating finite automata. **2003**, 80, 1075-1091	2
1793	Finite bisimulations of controllable linear systems.	7
1792	Functional reactive programming as a hybrid system framework.	
1791	Model and algorithm for efficient verification of high-assurance properties of real-time systems. **2003**, 15, 405-422	3
1790	Formal design and analysis of FMS controller.	
1789	Hybrid systems modeling of cooperative robots.	7
1788	Model checking stochastic automata. **2003**, 4, 452-492	24

1787	Abstract State Machines. **2003**,	506
1786	Combining Real-Time Model-Checking and Fault Tree Analysis. **2003**, 522-541	7
1785	Translating a Continuous-Time Temporal Logic into Timed Automata. **2003**, 322-338	2
1784	On Clock Difference Constraints and Termination in Reachability Analysis of Timed Automata. **2003**, 491-503	17
1783	Temporal verification of communicating real-time state machines using Uppaal.	2
1782	Simulating Security Systems Based on Logigrams. **2003**, 498-499	
1781	Making Timed Automata Communicate. **2003**, 337-351	
1780	A Tool Architecture for the Next Generation of Uppaal. **2003**, 352-366	11
1779	An integrated approach to modeling and analysis of embedded real-time systems based on Timed Petri Nets.	
1778	Deadline analysis of interrupt-driven software. **2003**, 28, 198-207	4
1777	From timed automata to DEVS models.	3
1776	Stability Analysis for Hybrid Automata Using Conservative Gains. **2003**, 36, 337-342	2
1775	Time Extension of Hierarchical State Machines. **2003**, 36, 217-222	
1774	Relations of Timed Event Graphs and Timed Automata in Fault Diagnosis. **2003**, 36, 747-752	2
1773	Formal Analysis of Scientific-Computation Methods. **2003**, 36, 295-300	
1772	Application of Timed Automata to the Diagnosis of the DAMADICS Benchmark Problem. **2003**, 36, 1083-1088	2
1771	Modelisation of a distributed hardware system for accurate simulation of real-time applications. **2003**, 36, 31-38	1
1770	Real-Time Computation: A Formal Definition And Its Applications. **2003**, 25, 247-257	1

1769	Verification of Timeliness QoS Properties in Multimedia Systems. **2003**, 523-540	3
1768	Efficient Path Finding with the Sweep-Line Method Using External Storage. **2003**, 319-337	13
1767	Analyzing the Redesign of a Distributed Lift System in UPPAAL. **2003**, 504-522	1
1766	Diagnosis of timed automata with an application to industrial actuators. **2004**, 11, 25-36	
1765	Bounded Model Checking for Region Automata. **2004**, 246-262	2
1764	Specification and Analysis of Real-Time Systems Using Real-Time Maude. **2004**, 354-358	9
1763	LTL over Integer Periodicity Constraints. **2004**, 121-135	6
1762	Incremental Search Methods for Reachability Analysis of Continuous and Hybrid Systems. **2004**, 142-156	55
1761	Modeling and Verification of a Fault-Tolerant Real-Time Startup Protocol Using Calendar Automata. **2004**, 199-214	33
1760	Analysing the PGM protocol with UPPAAL. **2004**, 42, 2773-2791	9
1759	Dependability checking with StoCharts: is train radio reliable enough for trains?. **2004**,	4
1758	Synthesis and stochastic assessment of schedules for lacquer production. **2004**,	8
1757	Visual timed event scenarios.	18
1756	Multi-robot planning : a timed automata approach. **2004**,	54
1755	Formal Modeling and Analysis of Timed Systems. **2004**,	3
1754	AN AUTOMATA-BASED APPROACH TO CSCW VERIFICATION. **2004**, 13, 183-209	1
1753	Decision Problems for Timed Automata: A Survey. **2004**, 1-24	53
1752	Timed Automata: Semantics, Algorithms and Tools. **2004**, 87-124	296

1751	UML models for dependability analysis of real-time systems.	14
1750	ReTiS: a real-time simulation tool for the analysis of distributed real-time applications.	1
1749	Mutation-based testing criteria for timeliness.	9
1748	Models and temporal logics for timed component connectors.	14
1747	Formal timing model for hardware components.	4
1746	An approach to hardware/software partitioning for multiple hardware devices model.	0
1745	Temporal Verification of Real-Time Multitasking Application Properties Based on Communicating Timed Automata.	1
1744	Black-Box Conformance Testing for Real-Time Systems. **2004**, 109-126	85
1743	Timed model checking of security protocols. **2004**,	23
1742	Information flow in hybrid systems. **2004**, 3, 760-799	
1741	Formal verification of real-time embedded software in an object-oriented application framework. **2004**, 151, 417	2
1740	Bisimulation on speed: worst-case efficiency. **2004**, 191, 105-144	14
1739	Polyhedral Flows in Hybrid Automata. **2004**, 24, 261-280	1
1738	Implementing Coloured Petri Nets Using a Functional Programming Language. **2004**, 17, 207-243	9
1737	Algebraic Recognizability of Languages. **2004**, 149-175	9
1736	Automatic verification of the IEEE 1394 root contention protocol with KRONOS and PRISM. **2004**, 5, 221-236	20
1735	Automated analysis of timed security: a case study on web privacy. **2004**, 2, 168-186	9
1734	Automated formal verification of visual modeling languages by model checking. **2004**, 3, 85-113	56

1733	Model checking real-time value-passing systems. **2004**, 19, 459-471		
1732	Model-checking dense-time Duration Calculus. **2004**, 16, 121-139		16
1731	Comparing model checking and logical reasoning for real-time systems. **2004**, 16, 104-120		21
1730	Timed automata with urgent transitions. **2004**, 40, 317-347		16
1729	Analysing failure behaviours in component interaction. **2004**, 71, 97-115		6
1728	QoS management specification support for multimedia middleware. **2004**, 72, 1-23		5
1727	System theory for system identification. **2004**, 118, 313-339		11
1726	Specification and Verification of Protocols With Time Constraints. **2004**, 99, 205-227		10
1725	On Equivalences for a Class of Timed Regular Expressions. **2004**, 106, 315-333		1
1724	Past pushdown timed automata and safety verification. *Theoretical Computer Science*, **2004**, 313, 57-71	1.1	5
1723	Scheduling hard sporadic tasks with regular languages and generating functions. *Theoretical Computer Science*, **2004**, 313, 119-132	1.1	2
1722	Optimal paths in weighted timed automata. *Theoretical Computer Science*, **2004**, 318, 297-322	1.1	45
1721	Updatable timed automata. *Theoretical Computer Science*, **2004**, 321, 291-345	1.1	60
1720	Linear reachability problems and minimal solutions to linear Diophantine equation systems. *Theoretical Computer Science*, **2004**, 328, 203-219	1.1	3
1719	Model-checking the Secure Release of a Time-locked Secret over a Network. **2004**, 99, 229-243		
1718	Interfaces: A Game-Theoretic Framework for Reasoning About Component-Based Systems. **2004**, 97, 3-23		8
1717	Timing in the Cospan-Span Model. **2004**, 104, 81-97		0
1716	□-transitions in Concurrent Timed Automata. **2004**, 89, 1-7		

1715 A Kleene theorem for splitable signals. **2004**, 89, 237-245 3

1714 Integration of an object formalism within a hybrid dynamic simulation environment. **2004**, 12, 1211-1223 15

1713 Testing times: on model-driven test generation for non-deterministic real-time systems.

1712 Multi-clock timed networks. **2004**, 8

1711 A translation based method for the timed analysis of scheduling extended time Petri nets. 18

1710 An integral cycle for building feasible multi-agent plans.

1709 Optimal hybrid control for switched affine systems under safety and liveness constraints. 2

1708 Deductive verification of probabilistic real-time systems. **2004**,

1707 A test case generation algorithm for real-time systems. 7

1706 . 10

1705 Timed automata approach to real time distributed system verification. 9

1704 Scheduling acyclic branching programs on parallel machines. 1

1703 . **2004**, 7

1702 . **2004**, 37

1701 . 2

1700 A dense time semantics for Web services specifications languages. 3

1699 Automatic verification of component-based real-time CORBA applications. 11

1698 Real-Time Testing with Timed Automata Testers and Coverage Criteria. **2004**, 134-151 9

1697	Teaching Coloured Petri Nets: Examples of Courses and Lessons Learned. **2004**, 402-412	1
1696	Using Zone Graph Method for Computing the State Space of a Time Petri Net. **2004**, 246-259	17
1695	The IF Toolset. **2004**, 237-267	61
1694	Validation of Stochastic Systems. **2004**,	9
1693	Automated Technology for Verification and Analysis. **2004**,	
1692	On Probabilistic Computation Tree Logic. **2004**, 147-188	56
1691	Decompositional verification of component-based systems-a hybrid approach.	
1690	Formal Methods for the Design of Real-Time Systems. **2004**,	2
1689	CONCUR 2004 - Concurrency Theory. **2004**,	2
1688	Model-Checking for Weighted Timed Automata. **2004**, 277-292	26
1687	Robust Model-based Execution of Critical Spacecraft Sequences. **2004**,	1
1686	Formal verification of timed systems: a survey and perspective. **2004**, 92, 1283-1305	48
1685	. **2004**, 30, 634-655	13
1684	VERTAF: an application framework for the design and verification of embedded real-time software. **2004**, 30, 656-674	25
1683	A CLP proof method for timed automata.	16
1682	Diagnose zeitbewerteter ereignisdiskreter Systeme (Diagnosis of Timed Discrete Event Systems). **2004**, 52, 164-173	2
1681	Towards interoperability test generation of time dependent protocols: a case study.	2
1680	Constructing decidable hybrid systems with velocity bounds. **2004**,	32

1679 Toward Unbounded Model Checking for Region Automata. **2004**, 20-33

1678 A Partial Order Semantics Approach to the Clock Explosion Problem of Timed Automata. **2004**, 296-311 8

1677 Open Maps, Alternating Simulations and Control Synthesis. **2004**, 466-480 8

1676 Automatic Synthesis and Verification of Real-Time Embedded Software. **2004**, 12-21

1675 Job-shop scheduling by combining reachability analysis with linear programming. **2004**, 37, 195-200 6

1674 Motion planning in multi-robot systems using timed automata. **2004**, 37, 597-602 1

1673 Analysis of OSEK/VDX Based Automotive Applications. **2004**, 37, 197-202

1672 Supervisory control for the conformance of real-time discrete event systems,. **2004**, 37, 39-44 1

1671 A formal analysis of a car periphery supervison system 1. **2004**, 37, 423-428

1670 Timed Automata Approach to CAN Verification. **2004**, 37, 147-152 3

1669 Abstraction of continuous system trajectories into timed automata. **2004**, 37, 309-314 4

1668 Deductive Probabilistic Verification Methods for Embedded and Ubiquitous Computing. **2004**, 183-195

1667 The Discrete Time Behavior of Lazy Linear Hybrid Automata. **2005**, 55-69 15

1666 Generating Polynomial Invariants for Hybrid Systems. **2005**, 590-605 33

1665 TIMED AUTOMATA MODEL OF PREEMPTIVE MULTITASKING APPLICATIONS. **2005**, 38, 147-154

1664 SCHEDULING LACQUER PRODUCTION BY REACHABILITY ANALYSIS A CASE STUDY. **2005**, 38, 50-55 4

1663 TWO PHASE TECHNIQUE FOR ASSEMBLY LINE BALANCING. **2005**, 38, 127-132

1662 INTRODUCING A NEW MATHEMATICAL ABSTRACTION FOR MODELING REAL TIME SYSTEMS. **2005**, 38, 141-146

1661	FROM FAULT TREE ANALYSIS TO MODEL CHECKING OF LOGIC CONTROLLERS. **2005**, 38, 86-91		3
1660	Systematic Implementation of Real-Time Models. **2005**, 139-156		10
1659	Perturbed Timed Automata. **2005**, 70-85		34
1658	Timed Testing with TorX. **2005**, 173-188		24
1657	Timed-Arc Petri Nets vs. Networks of Timed Automata. **2005**, 385-402		14
1656	Model checking hierarchical communicating real-time state machines.		1
1655	Computational Techniques for the Verification and Control of Hybrid Systems. **2005**, 151-175		7
1654	Safe diagnosability for fault-tolerant supervision of discrete-event systems. **2005**, 41, 1335-1347		82
1653	On the expressiveness and decidability of o-minimal hybrid systems. **2005**, 21, 447-478		36
1652	Monotonic hybrid systems. **2005**, 71, 34-69		
1651	Semantics and Runtime Monitoring of TLCharts: Statechart Automata with Temporal Logic Conditioned Transitions. **2005**, 113, 3-21		11
1650	Testing Conformance of Real-Time Applications by Automatic Generation of Observers. **2005**, 113, 23-43		18
1649	Team Automata for Security. **2005**, 128, 105-119		4
1648	Structural Translation from Time Petri Nets to Timed Automata. **2005**, 128, 145-160		16
1647	Process algebra for hybrid systems. *Theoretical Computer Science*, **2005**, 335, 215-280	1.1	49
1646	On the completeness and decidability of duration calculus with iteration. *Theoretical Computer Science*, **2005**, 337, 278-304	1.1	7
1645	On composition and lookahead delegation of . *Theoretical Computer Science*, **2005**, 341, 344-363	1.1	4
1644	A partial order semantics approach to the clock explosion problem of timed automata. *Theoretical Computer Science*, **2005**, 345, 27-59	1.1	34

1643	A hybrid systems framework for cellular processes. **2005**, 80, 273-82	18
1642	On-the-fly Workload Prediction and Redistribution in the Distributed Timed Model Checker Zeus. **2005**, 128, 3-18	2
1641	PVS Strategies for Proving Abstraction Properties of Automata. **2005**, 125, 45-65	10
1640	Complete Test Graph Synthesis For Symbolic Real-time Systems. **2005**, 130, 79-100	4
1639	A New Method for Transforming Timed Automata. **2005**, 130, 101-128	6
1638	Closed, Open, and Robust Timed Networks. **2005**, 138, 117-151	1
1637	Games for Counting Abstractions. **2005**, 128, 69-85	16
1636	Probabilistic Model Checking of the CSMA/CD Protocol Using PRISM and APMC. **2005**, 128, 195-214	21
1635	A theory of stochastic systems part I: Stochastic automata. **2005**, 203, 1-38	47
1634	A theory of Stochastic systems. Part II: Process algebra. **2005**, 203, 39-74	24
1633	A symbolic decision procedure for cryptographic protocols with time stamps. **2005**, 65, 1-35	5
1632	. **2005**, 54, 1684-1696	3
1631	A refinement-based process algebra for timed automata. **2005**, 17, 138-159	8
1630	Almost ASAP semantics: from timed models to timed implementations. **2005**, 17, 319-341	41
1629	Exploring optimal solution to hardware/software partitioning for synchronous model. **2005**, 17, 443-460	
1628	An Internet-based integrated product design environment. Part I: a hybrid agent and network architecture. **2005**, 27, 211-224	4
1627	Abstract State Machines: a unifying view of models of computation and of system design frameworks. **2005**, 133, 149-171	16
1626	Real-Time Maude 2.1. **2005**, 117, 285-314	12

1625	Improving Software Quality in Safety-Critical Applications by Model-Driven Verification. **2005**, 133, 101-117	2
1624	Inference of Timed Transition Systems. **2005**, 138, 87-99	9
1623	Model Checking Functional and Performability Properties of Stochastic Fluid Models. **2005**, 128, 295-310	1
1622	A quadratic-time DBM-based successor algorithm for checking timed automata. **2005**, 96, 101-105	5
1621	Distributed reachability analysis in timed automata. **2005**, 7, 19-30	12
1620	Issues in distributed timed model checking. **2005**, 7, 4-18	5
1619	Communication Benchmarking and Performance Modelling of MPI Programs on Cluster Computers. **2005**, 34, 201-217	10
1618	A Supervisory Control Method for Ensuring the Conformance of Real-Time Discrete Event Systems. **2005**, 15, 397-431	9
1617	Checking Timed Büchi Automata Emptiness Efficiently. **2005**, 26, 267-292	46
1616	Specification and testing of autonomous agents in e-commerce systems. **2005**, 15, 211-233	16
1615	A SAT-Based Decision Procedure for Mixed Logical/Integer Linear Problems. **2005**, 320-335	6
1614	Robustness of Composed Timed Systems. **2005**, 157-166	2
1613	State Identification Problems for Timed Automata. **2005**, 175-191	4
1612	Taylor Approximation for Hybrid Systems. **2005**, 402-416	9
1611	Information Flow Analysis for Probabilistic Timed Automata. **2004**, 13-26	1
1610	On Timed Automata with Discrete Time Structural and Language Theoretical Characterization. **2005**, 272-283	2
1609	Introduction to Hybrid Systems. **2005**, 91-116	48
1608	Model-based run-time monitoring of end-to-end deadlines. **2005**,	18

1607 A classification of symbolic transition systems. **2005**, 6, 1-32 39

1606 Passive mid-stream monitoring of real-time properties. **2005**, 3

1605 Generating optimal distinguishing sequences with a model checker. **2005**, 30, 1-7 1

1604 SPECIFICATION-BASED TESTING OF REAL-TIME EMBEDDED SYSTEMS. **2005**, 115-124

1603 An Introduction to Hybrid Automata. **2005**, 491-517 22

1602 Modular Verification of Reconfigurable Components. **2005**, 59-81 3

1601 Fault Diagnosis Using Timed Automata. **2005**, 219-233 33

1600 Formal Methods and Software Engineering. **2005**, 3

1599 Parallel Computing Technologies. **2005**,

1598 Towards efficient use of shared communication media in the timed model. 1

1597 From StoCharts to MoDeST. **2005**, 17

1596 Abstracting functionality for modular performance analysis of hard real-time systems. **2005**, 7

1595 Generating optimal distinguishing sequences with a model checker. **2005**, 6

1594 Testing real-time embedded software using UPPAAL-TRON. **2005**, 73

1593 Online Testing of Real-time Systems Using Uppaal. **2005**, 79-94 70

1592 Fine-Grained Composition of Distributed Sensor System Infrastructure. **2005**,

1591 Control of Linear Min-plus Systems under Temporal Constraints. 10

1590 Discrete Geometry for Computer Imagery. **2005**, 1

1589	Efficient Solution of Optimal Control Problems Using Hybrid Systems. **2005**, 43, 1923-1952	12
1588	Control of temporal constraints based on dioid algebra for timed event graphs.	2
1587	Verification of concurrent systems with parametric delays using octahedra.	4
1586	Integrating Object-Z with Timed Automata.	3
1585	Timed automata with data structures for distributed systems design and analysis. **2005**,	1
1584	Optimal Control of Discrete-Time Hybrid Automata under Safety and Liveness Constraints.	
1583	FORMAL DESIGNS FOR EMBEDDED AND HYBRID SYSTEMS. **2005**, 15, 373-378	2
1582	Formal Approaches to Software Testing. **2005**,	1
1581	Over-approximate model of multitasking application based on timed automata using only one clock.	3
1580	.	1
1579	Timing analysis of TCP servers for surviving denial-of-service attacks.	2
1578	Multi-Agent Planning Under Constraints Application to Tactical Aircraft Simulation. **2005**,	
1577	Compatibility between shared variable valuations in timed automaton network model-checking.	2
1576	Thunderstriking constraints with JUPITER.	0
1575	Applications and Theory of Petri Nets 2005. **2005**,	1
1574	Characterizing workload correlations in multi processor hard real-time systems. **2005**,	8
1573	Application of Real-Time DEVS to Analysis of Safety-Critical Embedded Control Systems: Railroad Crossing Control Example. **2005**, 81, 119-136	21
1572	Periodic Linear Programming with applications to real-time scheduling. **2005**, 15, 383-406	

1571	An Expressive and Implementable Formal Framework for Testing Real-Time Systems. **2005**, 209-225	10
1570	Translation validation for model-based code-generators for PLCs.	5
1569	Automata Composition for Modeling Large Industrial Systems.	0
1568	An overview of SystemC/sup FL/.	
1567	A consistency-based approach to efficient test case generation from timed automata specifications.	
1566	A scenario-matching approach to the description and model checking of real-time properties. **2005**, 31, 1028-1041	16
1565	SOFSEM 2005: Theory and Practice of Computer Science. **2005**,	0
1564	QoS modelling and analysis with UML-statecharts. **2005**, 32, 28-33	7
1563	Monitoring, Security, and Rescue Techniques in Multiagent Systems. **2005**,	
1562	Synchronization verification in system-level design with ILP solvers.	1
1561	Model checking timed systems with priorities.	3
1560	Model checking real-time component based systems with blackbox testing.	2
1559	Efficient deadlock-freeness detection in real-time systems. **2005**,	
1558	On the decidability of metric temporal logic.	54
1557	An architecture for automated QoS resolution in wireless systems.	3
1556	Task scheduling observation and stack safety analysis in real time distributed systems using a simulation tool.	2
1555	.	4
1554	Constraint specification of the control logic of automated manufacturing systems.	1

1553	On the freeze quantifier in constraint LTL: decidability and complexity.	11
1552	Specification and execution of gateways in integrated architectures.	4
1551	Axiomatisation and decidability of multi-dimensional duration calculus.	1
1550	A parametric model checking approach for real-time systems design. **2005**,	1
1549	From requirements to specifications.	1
1548	A cognitive model of interaction for software agents.	0
1547	Test case generation for flexible real-time control systems.	1
1546	Formal communication semantics of SystemC/sup FL/.	1
1545	Abstracting functionality for modular performance analysis of hard real-time systems.	4
1544	Specifying urgency in timed I/O automata. **2005**,	10
1543	Modeling decentralized real-time control by state space partition of timed automata. **2005**,	
1542	Realtime Hybrid Task-Based Control for Robots and Machine Tools.	18
1541	Boolean Web-service automata: a parallel model for distributed Web service operations.	1
1540	A pragmatic approach for testing robustness on real-time component based systems.	3
1539	Modeling and verifying circuits using generalized relative timing.	6
1538	Verification of a timed multitask system with UPPAAL.	2
1537	Combining Extended UML Models and Formal Methods to Analyze Real-Time Systems. **2005**, 24-36	3
1536	Refining the Undecidability Frontier of Hybrid Automata. **2005**, 261-272	9

1535	Integration of AI and OR Techniques in Constraint Programming for Combinatorial Optimization Problems. **2005**,	3
1534	Computer Network Security. **2005**,	1
1533	Model-Based Testing of Reactive Systems. **2005**,	172
1532	Testing of Communicating Systems. **2005**,	
1531	Embedded Software and Systems. **2005**,	
1530	.	18
1529	Programming Multi-Agent Systems. **2005**,	7
1528	Super-Recursive Algorithms. **2005**,	
1527	Reachability Analysis of Multi-affine Systems. **2006**, 348-362	32
1526	Formal Techniques for Networked and Distributed Systems - FORTE 2006. **2006**,	1
1525	Automata Based Modeling for Batching and Mixing Plants for Concrete. **2006**,	
1524	Diagnosis of a Class of Timed Discrete Event Systems.	3
1523	An approach to protocol modeling and validation.	1
1522	Strengthening Property Preservation in Concurrent Real-Time Systems. **2006**,	2
1521	.	10
1520	Automated Verification of Continuous Time Systems by Discrete Temporal Induction. **2006**,	3
1519	Verifying Autonomic Fault Mitigation Strategies in Large Scale Real-Time Systems.	4
1518	Extracting Simulation Models from Complex Embedded Real-Time Systems. **2006**,	4

1517	Controlling a Class of Nonlinear Systems on Rectangles. **2006**, 51, 1749-1759	141
1516	A Three-Layer Virtual Director Model for Supporting Automated Multi-Site Distributed Education. **2006**,	2
1515	FSTTCS 2006: Foundations of Software Technology and Theoretical Computer Science. **2006**,	5
1514	A Hybrid Heuristic Algorithm for HW-SW Partitioning Within Timed Automata. **2006**, 459-466	4
1513	A real-time profile for UML. **2006**,	2
1512	Quality of Software Architectures. **2006**,	1
1511	.	
1510	Timed-MPSG: A Formal Model for Real-Time Shop Floor Controller. **2006**,	3
1509	Specification languages as front-ends towards the DEVS formalism. **2006**,	0
1508	Optimizing Quality of Control in Networked Automation Systems using Probabilistic Models. **2006**,	8
1507	. **2006**,	2
1506	Correctness-preserving synthesis for real-time control software. **2006**,	
1505	Specifying and proving properties of timed I/O automata in the TIOA toolkit.	10
1504	An automaton-based approach to the verification of timed workflow schemas. **2006**,	7
1503	Using Model-Driven Engineering to generate QoS Monitors from a formal specification. **2006**,	1
1502	LTL with the Freeze Quantifier and Register Automata.	25
1501	. **2006**, 42, 1420-1436	9
1500	Model based testing of real-time embedded components. **2006**,	

1499	Detecting Design Errors in Composite Events for Event Triggered Real-Time Systems Using Timed Automata. **2006**,	3
1498	Integrating timed automata into tabu algorithm for HW-SW partitioning.	
1497	. **2006**, 51, 1862-1877	190
1496	Representation, Verification, and Computation of Timed Properties in Web. **2006**,	33
1495	Timed automata based analysis of embedded system architectures. **2006**,	33
1494	III-Phase Verification and Validation of IEC Standard Programmable Logic Controller. **2006**,	10
1493	A Distributed Approach for Testing Timed Systems. **2006**,	1
1492	Safety Control Synthesis for Time Petri Nets.	8
1491	Symbolic control of linear systems based on symbolic subsystems. **2006**, 51, 1003-1013	14
1490	In this work, we propose two translations: one from extended.	3
1489	MODEST: A Compositional Modeling Formalism for Hard and Softly Timed Systems. **2006**, 32, 812-830	100
1488	Control in o-minimal Hybrid Systems.	2
1487	An Equivalence Result between Linear Hybrid Automata and Piecewise Affine Systems. **2006**,	5
1486	A Fully Automated Framework for Control of Linear Systems from LTL Specifications. **2006**, 333-347	34
1485	Linear-time temporal logics with Presburger constraints: an overview ?. **2006**, 16, 311-347	12
1484	Application Development for Safety Critical Distributed Embedded Systems with Model Verification. **2006**,	
1483	Sideslip angle, lateral tire force and road friction estimation in simulations and experiments. **2006**,	23
1482	Solving production scheduling problems using advanced model checking tools. **2006**, 26, 37	1

1481	Hybrid Modelling and Dynamical Analysis of Gene Regulatory Networks with Delays. **2006**, 3, 231-251	40
1480	Fault diagnosis applied on the heat exchanger. **2006**, 39, 422-426	
1479	OBTAINING TEMPORAL AND TIMED PROPERTIES OF LOGIC CONTROLLERS FROM FAULT TREE ANALYSIS. **2006**, 39, 243-248	3
1478	FAULT DETECTION FOR HDS BY MEANS OF NEURAL NETWORKS: APPLICATION TO TWO TANKS HYDRAULIC SYSTEM. **2006**, 39, 980-985	4
1477	PROBABILISTIC HYBRID AUTOMATA WITH VARIABLE STEP WIDTH APPLIED TO THE ANAYLSIS OF NETWORKED AUTOMATION SYSTEMS. **2006**, 39, 283-288	3
1476	Systematic design of logic controllers for processing plants starting from informal specifications. **2006**, 21, 1317-1322	2
1475	MODELING AN IMPACT CONTROL STRATEGY USING HYPA. **2006**, 39, 56-63	
1474	COMPARATIVE STUDY BETWEEN THE TIMED AUTOMATA AND THE RECURRENT RADIAL BASIS FUNCTION FOR DISCRETE EVENT SYSTEM DIAGNOSIS. **2006**, 39, 1455-1460	2
1473	TIMED DISCRETE EVENT CONTROL OF A PARALLEL PRODUCTION LINE WITH CONTINUOUS OUTPUT. **2006**, 39, 205-210	3
1472	State space computation and analysis of Time Petri Nets. **2006**, 6, 301-320	19
1471	A survey of formal verification methods and tools for embedded and real-time systems. **2006**, 2, 184	
1470	Model-centric software architecture reconstruction. **2006**, 36, 333-363	8
1469	Towards the Semantics and Verification of BPEL4WS. **2006**, 151, 33-52	31
1468	A Classification of Time and/or Probability Dependent Security Properties. **2006**, 153, 177-193	4
1467	On the Contribution of a □simulation in the Incremental Modeling of Timed Systems. **2006**, 160, 97-111	
1466	Extended state identification and verification using a model checker. **2006**, 48, 981-992	5
1465	Periodicity based decidable classes in a first order timed logic. **2006**, 139, 43-73	1
1464	Modeling web applications reacting to user behaviors. **2006**, 50, 1533-1546	7

1463	Efficient synthesis of production schedules by optimization of timed automata. **2006**, 14, 1183-1197		25
1462	Predicate Diagrams for the Verification of Real-Time Systems. **2006**, 145, 151-165		
1461	Liveness Checking as Safety Checking for Infinite State Spaces. **2006**, 149, 79-96		24
1460	Optimal control of discrete-time hybrid automata under safety and liveness constraints. **2006**, 65, 1188-1210		8
1459	Structural translation from Time Petri Nets to Timed Automata. **2006**, 79, 1456-1468		68
1458	Verification of rectangular hybrid automata models. **2006**, 79, 1433-1443		3
1457	A control synthesis approach for time discrete event systems. **2006**, 70, 250-265		3
1456	MinMax-DEVS modeling and simulation. **2006**, 14, 909-929		12
1455	Expand, Enlarge and Check: New algorithms for the coverability problem of WSTS. **2006**, 72, 180-203		51
1454	Scheduling with timed automata. *Theoretical Computer Science*, **2006**, 354, 272-300	1.1	111
1453	Counterexample-guided predicate abstraction of hybrid systems. *Theoretical Computer Science*, **2006**, 354, 250-271	1.1	49
1452	Model checking restricted sets of timed paths. *Theoretical Computer Science*, **2006**, 358, 273-292	1.1	7
1451	LTL over integer periodicity constraints. *Theoretical Computer Science*, **2006**, 360, 96-123	1.1	14
1450	Methodology for UML Modeling and Formal Verification of Real-Time Systems. **2006**,		12
1449	Performance analysis of probabilistic timed automata using digital clocks. **2006**, 29, 33-78		111
1448	Dealing with practical limitations of distributed timed model checking for timed automata. **2006**, 29, 197-214		2
1447	Modular Design of Real-Time Systems Using Hierarchical Communicating Real-time State Machines. **2006**, 32, 105-123		4
1446	Verifying distributed real-time properties of embedded systems via graph transformations and model checking. **2006**, 33, 77-100		20

1445	Lower and upper bounds in zone-based abstractions of timed automata. **2006**, 8, 204-215		55
1444	Validating timed UML models by simulation and verification. **2006**, 8, 128-145		49
1443	A semantics of communicating reactive objects with timing. **2006**, 8, 97-112		6
1442	A real-time profile for UML. **2006**, 8, 113-127		30
1441	Preface of Specification and Validation of Real Time and Embedded systems in UML **2006**, 8, 93-96		1
1440	A formal framework for verifying distributed embedded systems based on abstraction methods. **2006**, 8, 471-484		7
1439	Model checker aided design of a controller for a wafer scanner. **2006**, 8, 633-647		11
1438	Analysis of a biphase mark protocol with Uppaal and PVS. **2006**, 18, 433-458		21
1437	How to stop time stopping. **2006**, 18, 459-493		19
1436	Refinement of actions for real-time concurrent systems with causal ambiguity. **2006**, 42, 389-418		2
1435	On synthesizing test cases in symbolic real-time testing. **2006**, 12, 31-48		1
1434	Schedulability analysis of fixed-priority systems using timed automata. *Theoretical Computer Science*, **2006**, 354, 301-317	1.1	58
1433	Efficient timed model checking for discrete-time systems. *Theoretical Computer Science*, **2006**, 353, 249-271	1.1	23
1432	Diagnosis of timed automata: Theory and application to the DAMADICS actuator benchmark problem. **2006**, 14, 609-619		26
1431	Grid automata and supervisory control of dense real-time discrete event systems. **2006**, 70, 408-418		4
1430	Timed CSP: A Retrospective. **2006**, 162, 273-276		13
1429	Weighted Timed Automata: Model-Checking and Games. **2006**, 158, 3-17		16
1428	SaveCCM: An Analysable Component Model for Real-Time Systems. **2006**, 160, 127-140		12

1427	Formal Analysis of Streaming Downloading Protocol for System Upgrading. **2006**, 164, 205-224	2
1426	Test Case Generation for Mutation-based Testing of Timeliness. **2006**, 164, 97-114	40
1425	Improved undecidability results on weighted timed automata. **2006**, 98, 188-194	41
1424	Folk theorems on the determinization and minimization of timed automata. **2006**, 99, 222-226	22
1423	Remove Irrelevant Atomic Formulas for Timed Automaton Model Checking. **2006**, 21, 41-51	
1422	A survey of temporal data mining. **2006**, 31, 173-198	166
1421	On model-checking timed automata with stopwatch observers. **2006**, 204, 408-433	11
1420	Statistical probabilistic model checking with a focus on time-bounded properties. **2006**, 204, 1368-1409	140
1419	Continuity controlled hybrid automata. **2006**, 68, 5-53	2
1418	A note on the undecidability of the reachability problem for o-minimal dynamical systems. **2006**, 52, 165-170	10
1417	Scheduling and planning with timed automata. **2006**, 21, 1973-1978	2
1416	Fault diagnosis based on timed automata: Diagnoser verification. **2006**,	2
1415	HighSpec. **2006**,	3
1414	From semantic web to expressive software specifications. **2006**,	1
1413	An analysis framework for network-code programs. **2006**,	8
1412	Predicate abstraction for reachability analysis of hybrid systems. **2006**, 5, 152-199	75
1411	Reusable models for timing and liveness analysis of middleware for distributed real-time and embedded systems. **2006**,	11
1410	Verification of a scheduler in B through a timed automata specification. **2006**,	2

1409	A toolsuite for the verification of real-time systems in Eclipse. **2006**,	
1408	Applying the CBSE paradigm in the real time specification for Java. **2006**,	4
1407	Achieving communication coverage in testing. **2006**, 31, 1-10	7
1406	Experiments in the use of □simulations for the components-verification of real-time systems. **2006**,	
1405	Scheduling for multi-threaded real-time programs via path planning. **2006**,	
1404	Formal design and analysis of a hybrid supervisory control structure for Virtual Production Systems. **2006**, 44, 2479-2497	1
1403	Logical Approaches to Computational Barriers. **2006**,	1
1402	B 2007: Formal Specification and Development in B. **2006**,	3
1401	SOLVING PRODUCTION SCHEDULING PROBLEMS USING ADVANCED MODEL CHECKING TOOLS. **2006**, 05, 209-232	2
1400	Formal Approaches to Software Testing. **2006**,	1
1399	Model checking procedures for infinite state systems. **2006**,	
1398	Automatic Generation and Validation of Models of Legacy Software. **2006**,	5
1397	Testing of Communicating Systems. **2006**,	
1396	Monitoring of Real-Time Properties. **2006**, 260-272	82
1395	Multiple Fault Models for Timed FSMs. **2006**,	1
1394	Methods for bounding end-to-end delays on an AFDX network.	73
1393	Stabilization, Safety, and Security of Distributed Systems. **2006**,	1
1392	Modelling and hierarchical diagnosis of timed discrete-event systems. **2006**, 12, 519-542	7

1391 Two architectures for testing distributed real-time systems.

1390 Design of verified logic control programs. **2006**,

1389 Diagnosis of discrete event system by stochastic timed automata. **2006**, 1

1388 Algebraic Methodology and Software Technology. **2006**, 2

1387 Testing of Hybrid Real-time Systems Using FPGA Platform. **2006**, 1

1386 Formal modeling and analysis of the AFDX frame management design. 4

1385 A Semantic Unit for Timed Automata Based Modeling Languages. 1

1384 Conformance Testing Relations for Timed Systems. **2006**, 103-117 12

1383 Robustness evaluation of real-Time Protocols. **2006**, 1

1382 Modeling internal combustion engines using a hyper-class of hybrid automata: A case study. **2006**,

1381 Improve Model Checking Efficiency Using Specific Knowledge about the System. **2006**,

1380 Probabilistic Model Checking of Contention Resolution in the IEEE 802.15.4 Low-Rate Wireless Personal Area Network Protocol. **2006**, 30

1379 A diagnostic framework for integrated time-triggered architectures. 5

1378 Use of Timed Automata Models in the Design of Real-time Control Network Elements. **2006**,

1377 Timed modelling and analysis in Web service compositions. **2006**, 28

1376 Formal Methods for Open Object-Based Distributed Systems. **2006**, 2

1375 SAT-BASED MODEL CHECKING FOR REGION AUTOMATA. **2006**, 17, 775-795

1374 Embedded Computer Systems: Architectures, Modeling, and Simulation. **2006**, 4

1373	Finite bisimulation of reactive untimed infinite state systems modeled as automata with variables. **2006**,	5
1372	Scheduling algorithms for optimal robot cell coordination - a comparison. **2006**,	7
1371	Due Date Achievable Verification in Job Shop with Alternative Processes by Timed Automata. **2006**,	
1370	Towards an Integration of Standard Component-Based Safety Evaluation Techniques with SaveCCM. **2006**, 199-213	14
1369	Time Constraint Patterns for Event B Development. **2006**, 140-154	31
1368	Formal Methods and Software Engineering. **2007**,	2
1367	A Symbolic Decision Procedure for Robust Safety of Timed Systems. **2007**,	7
1366	Advances in Computer Science - ASIAN 2006. Secure Software and Related Issues. **2007**,	
1365	Formal Modeling and Analysis of Timed Systems. **2007**,	1
1364	Evaluating the Quality of Models Extracted from Embedded Real-Time Software. **2007**,	0
1363	Symbolic model checking for temporal-epistemic logics. **2007**, 38, 77-99	7
1362	Aspects of availability. **2007**,	1
1361	A verification system for transient response of analog circuits. **2007**, 12, 1-39	11
1360	Seamless formal verification of complex event processing applications. **2007**,	7
1359	Influence of different system abstractions on the performance analysis of distributed real-time systems. **2007**,	31
1358	Wahrscheinlichkeitsbasierte Modellverifikation Netzbasierter Automatisierungssysteme (Probabilistic Model Checking of Networked Automation Systems). **2007**, 55, 624-633	
1357	Quantitative verification. **2007**,	20
1356	A computational model of human interaction and planning for heterogeneous multi-agent systems. **2007**,	0

1355 REX, the rule and event eXplorer. **2007**, 9

1354 Worst-case lifetime computation of a wireless sensor network by model-checking. **2007**, 9

1353 Performance estimation of distributed real-time embedded systems by discrete event simulations. **2007**, 10

1352 On timed components and their abstraction. **2007**, 3

1351 Quantitative verification. **2007**, 44

1350 42. **2007**, 13

1349 Simulation for end-to-end delays distribution on a switched Ethernet. **2007**, 9

1348 Approximate timed abstractions of hybrid automata. **2007**, 10

1347 Perspectives of Systems Informatics. **2007**,

1346 A Formal Model for Real-Time Automated Manufacturing System Control with Timed-MPSG. **2007**, 1

1345 Diagnosability verification for hybrid automata and durational graphs. **2007**, 5

1344 Integration of Model-Checking Tools: from Discrete to Hybrid Models. **2007**, 1

1343 On the Equivalence of Automaton-Based Representations of Time Granularities. **2007**, 3

1342 A Typed Compositional Language for Real-Time Systems. **2007**,

1341 The Cost of Punctuality. **2007**, 24

1340 Validation of biological models with temporal logic and Timed Hybrid Petri Nets. **2007**, 2007, 4603-8 1

1339 Verification of temporal properties on hybrid automata by simulation relations. **2007**, 7

1338 Real-Time Stable Event Structures and Marked Scott Domains: An Adjunction. **2006**, 443-450

1337	Modelling and Analysis of Hybrid Supervisory Systems. **2007**,	
1336	Formal Methods for Components and Objects. **2007**,	3
1335	AUTOMATIC FAILURE ANALYSIS USING SAFECHARTS. **2007**, 17, 57-78	1
1334	Comparative Study Between the Timed Automata and the Recurrent Radial Basis Function for Discrete Event System Diagnosis. **2007**, 1455-1460	
1333	Malicious node detection in ad hoc networks using timed automata. **2007**, 253	
1332	PROBABILISTIC TIMED AUTOMATA FOR MODELING NETWORKED AUTOMATION SYSTEMS. **2007**, 40, 1-6	3
1331	TIMED ANALYSIS OF EMBEDDED NETWORKS USING TIMED AUTOMATA. **2007**, 40, 255-260	
1330	5.4.3 Usability of formal verification on EFFBD models: Applying Petri nets to Systems Engineering issues. **2007**, 17, 848-859	2
1329	GENERIC DETERMINATION OF FAULT MODELS FOR FDI PURPOSES. **2007**, 40, 61-66	
1328	PROSPECTS FOR MODEL-BASED TESTING OF DISCRETE SAFETY SYSTEMS. **2007**, 40, 133-138	
1327	ROBUST FAULT DETECTION FOR THE HYBRID DYNAMIC SYSTEMS. **2007**, 40, 505-510	
1326	A GENERIC SIMULATION MODEL FOR END-TO-END DELAYS EVALUATION ON AN AVIONICS SWITCHED ETHERNET. **2007**, 40, 159-166	0
1325	STOCHASTIC NETWORK CALCULUS FOR END-TO-END DELAY EVALUATION OF AVIONICS MULTI-HOP VIRTUAL LINKS. **2007**, 40, 383-390	1
1324	Test selection criteria for real-time systems modeled as timed input-output automata. **2007**, 3, 279-292	2
1323	A Semantic Anchoring Infrastructure for the Design of Embedded Systems. **2007**,	
1322	Stochastic network calculus for end-to-end delays distribution evaluation on an avionics switched Ethernet. **2007**,	4
1321	On Synthesizing Controllers from Bounded-Response Properties. **2007**, 95-107	24
1320	RFID Data Management: Challenges and Opportunities. **2007**,	72

1319 Embedded Computer Systems: Architectures, Modeling, and Simulation. **2007**, 3

1318 Multi-Agent Systems and Applications V. **2007**, 0

1317 Formal Verification of Use Case Maps with Real Time Extensions. **2007**, 225-241 7

1316 SDL 2007: Design for Dependable Systems. **2007**, 2

1315 Web Service Composition Approaches: From Industrial Standards to Formal Methods. **2007**, 52

1314 Formal Methods for Open Object-Based Distributed Systems. **2007**, 1

1313 A framework for the translation of extended workflow processes to Petri Net with Clocks. **2007**,

1312 Generation of optimal finite test suites for timed systems. **2007**, 4

1311 Composing Functional and State-Based Performance Models for Analyzing Heterogeneous Real-Time Systems. **2007**, 13

1310 A Formal Approach to Test the Robustness of Embedded Systems using Behaviour Analysis. **2007**, 7

1309 CSL^TA: an Expressive Logic for Continuous-Time Markov Chains. **2007**, 8

1308 A survey on automata-based methods for modelling and simulation of industrial systems. **2007**, 3

1307 Constraint Verification for Concurrent System Management Workflows Sharing Resources. **2007**, 1

1306 Formal Methods and Stochastic Models for Performance Evaluation. **2007**, 3

1305 Timed Behavior Trees and Their Application to Verifying Real-Time Systems. **2007**, 14

1304 Introducing the modeling and verification process in SysML. **2007**, 24

1303 Formal Specification using Interaction Diagrams. **2007**, 4

1302 Timed Modeling of Web Services Composition for Automatic Testing. **2007**, 16

1301	Modeling and verification of control logics in safety instrumented system for chemical industrial processes. **2007**, 1265-1270	1
1300	Timed-Automata Semantics and Analysis of UML/SPT Models with Concurrency. **2007**,	2
1299	A Model-Driven Framework for the Generation of Gateways in Distributed Real-Time Systems. **2007** ,	2
1298	Algebraic Biology. **2007**,	1
1297	Automated Testing of Timeliness : A Case Study. **2007**,	7
1296	On Verification of Probabilistic Timed Automata against Probabilistic Duration Properties. **2007**,	4
1295	Periodic Finite-State Machines. **2007**,	4
1294	Testing Duration Systems using an approximation method. **2007**,	1
1293	An Automatic Real-Time Analysis of the Time to Reach Consensus. **2007**,	2
1292	Model-Driven Engineering for Development-Time QoS Validation of Component-Based Software Systems. **2007**,	5
1291	On the automatic generation of timed automata models from ISA 5.2 diagrams. **2007**,	4
1290	Minimization of Expected Cycle Time in Manufacturing Cells with Uncontrollable Behavior. **2007**,	2
1289	Modeling and Verification of TTCAN Startup Protocol Using Synchronous Calendar. **2007**,	4
1288	Incorporating Time in the Modeling of Hardware and Software Systems: Concepts, Paradigms, and Paradoxes. **2007**,	
1287	A Programming Model for Time-Synchronized Distributed Real-Time Systems. **2007**,	71
1286	Detection of Out-of-Norm Behaviors in Event-Triggered Virtual Networks. **2007**,	
1285	. **2007**,	
1284	Computer Safety, Reliability, and Security. **2007**,	2

1283	WCET free time analysis of hard real-time systems on multiprocessors: A regular language-based model. *Theoretical Computer Science*, **2007**, 388, 26-52	1.1	9
1282	Algorithmic analysis of polygonal hybrid systems, part I: Reachability. *Theoretical Computer Science*, **2007**, 379, 231-265	1.1	24
1281	Model checking mobile stochastic logic. *Theoretical Computer Science*, **2007**, 382, 42-70	1.1	51
1280	Non-Interference Control Synthesis for Security Timed Automata. **2007**, 180, 35-53		11
1279	Extending a Component Specification Language with Time. **2007**, 176, 47-67		
1278	Proving Approximate Implementations for Probabilistic I/O Automata. **2007**, 174, 71-93		8
1277	Q-Automata: Modelling the Resource Usage of Concurrent Components. **2007**, 175, 153-167		22
1276	A Global Algorithm for Model-Based Test Suite Generation. **2007**, 190, 47-59		21
1275	Robust parametric reachability for timed automata. **2007**, 102, 208-213		29
1274	State explosion in almost-sure probabilistic reachability. **2007**, 102, 236-241		13
1273	The SAVE approach to component-based development of vehicular systems. **2007**, 80, 655-667		52
1272	A Zero-Space algorithm for Negative Cost Cycle Detection in networks. **2007**, 5, 408-421		8
1271	Modeling and verification of real-time systems based on equations. **2007**, 66, 162-180		6
1270	Engineering and theoretical underpinnings of retrenchment. **2007**, 67, 301-329		40
1269	Workload correlations in multi-processor hard real-time systems. **2007**, 73, 207-224		4
1268	On the freeze quantifier in Constraint LTL: Decidability and complexity. **2007**, 205, 2-24		242
1267	Axiomatisation and decidability of multi-dimensional Duration Calculus. **2007**, 205, 25-64		5
1266	An automata-theoretic approach to constraint LTL. **2007**, 205, 380-415		53

1265	Symbolic model checking for probabilistic timed automata. **2007**, 205, 1027-1077	79
1264	Task automata: Schedulability, decidability and undecidability. **2007**, 205, 1149-1172	100
1263	Taylor approximation for hybrid systems. **2007**, 205, 1575-1607	6
1262	Timed verification of hierarchical communicating real-time state machines. **2007**, 29, 635-646	0
1261	Modelling coral reef habitat trajectories: Evaluation of an integrated timed automata and remote sensing approach. **2007**, 205, 59-80	18
1260	Modelling and verification of program logic controllers using timed automata. **2007**, 1, 127-131	12
1259	Hybridization methods for the analysis of nonlinear systems. **2007**, 43, 451-476	120
1258	Symbolic models for control systems. **2007**, 43, 477-500	13
1257	Parametric probabilistic transition systems for system design and analysis. **2007**, 19, 93-109	44
1256	Predicate diagrams for the verification of real-time systems. **2007**, 19, 401-413	
1255	Formal Specification and Verification of Real-Time Systems using Graph Grammars. **2007**, 13, 51-68	4
1254	Semantics and pragmatics of Real-Time Maude. **2007**, 20, 161-196	150
1253	A study on multimedia documents behavior: a notion of equivalence. **2007**, 33, 301-324	2
1252	Predictable real-time software synthesis. **2007**, 36, 159-198	6
1251	Cost enforcement in the real-time specification for Java. **2007**, 37, 139-179	2
1250	Formal verification of component-based designs. **2007**, 11, 49-90	11
1249	Timed substitutions for regular signal-event languages. **2007**, 31, 101-134	2
1248	On the optimal reachability problem of weighted timed automata. **2007**, 31, 135-175	40

1247	Models and temporal logical specifications for timed component connectors. **2007**, 6, 59-82	42
1246	On a decision procedure for quantified linear programs. **2007**, 51, 55-77	14
1245	On the expressiveness of MTL in the pointwise and continuous semantics. **2007**, 9, 1-4	26
1244	Towards UML-based formal specifications of component-based real-time software. **2007**, 9, 179-192	2
1243	Special section on coloured Petri nets. **2007**, 9, 209-212	3
1242	Towards a verifiable real-time, autonomic, fault mitigation framework for large scale real-time systems. **2007**, 3, 33-52	8
1241	Recent Advances in Real-Time Maude. **2007**, 174, 65-81	3
1240	Keeping Secrets in Resource Aware Components. **2007**, 190, 79-94	1
1239	The octahedron abstract domain. **2007**, 64, 115-139	34
1238	Timed Behavior Trees for Failure Mode and Effects Analysis of time-critical systems. **2008**, 81, 2163-2182	22
1237	Towards Deriving Test Sequences by Model Checking. **2008**, 195, 21-40	3
1236	Parametric Analysis of an Improved Fault Tolerant System. **2008**, 207, 121-136	
1235	A Game-theoretic Approach to Behavioural Visualisation. **2008**, 208, 113-129	2
1234	A scalable method for testing real-time systems. **2008**, 16, 3-22	16
1233	Using communication coverage criteria and partial model generation to assist software integration testing. **2008**, 16, 185-211	8
1232	Formal verification of multitasking applications based on timed automata model. **2008**, 38, 39-65	38
1231	Applying infinite state model checking and other analysis techniques to tabular requirements specifications of safety-critical systems. **2008**, 12, 97-137	3
1230	Specifying and proving properties of timed I/O automata using Tempo. **2008**, 12, 139-170	7

1229	Timed Discrete Event Control of Parallel Production Lines with Continuous Outputs. **2008**, 18, 241-262	7
1228	Optimal infinite scheduling for multi-priced timed automata. **2008**, 32, 3-23	41
1227	Robust safety of timed automata. **2008**, 33, 45-84	35
1226	Model checking Duration Calculus: a practical approach. **2008**, 20, 481-505	25
1225	Network invariants for real-time systems. **2008**, 20, 619-635	5
1224	M-nets: a survey. **2008**, 45, 537-564	4
1223	A formal method to real-time protocol interoperability testing. **2008**, 51, 1723-1744	2
1222	Timed automata for metric interval temporal logic formulae in prototype verification system. **2008**, 12, 339-346	2
1221	. **2008**, 12, 159-172	3
1220	A multiple model approach for predictive control of nonlinear hybrid systems. **2008**, 18, 131-148	82
1219	Analyzing safety properties of hybrid processing systems: A case study on an industrial evaporator. **2008**, 18, 885-895	4
1218	Automated generation of test suites from formal specifications of real-time reactive systems. **2008**, 81, 286-304	10
1217	Design and verification of long-running transactions in a timed framework. **2008**, 73, 76-94	15
1216	Approximately bisimilar symbolic models for nonlinear control systems. **2008**, 44, 2508-2516	178
1215	Formal testing from timed finite state machines. **2008**, 52, 432-460	50
1214	Scheduling of multi-product batch plants based upon timed automata models. **2008**, 32, 275-291	34
1213	Time Separation of Events: An Inverse Method. **2008**, 209, 135-148	
1212	Model Checking Quantitative Linear Time Logic. **2008**, 220, 61-77	20

1211	An Inverse Method for Parametric Timed Automata. **2008**, 223, 29-46		6
1210	Integration of a failure monitoring within a hybrid dynamic simulation environment. **2008**, 47, 1942-1952		7
1209	Automatic synthesis and verification of real-time embedded software for mobile and ubiquitous systems. **2008**, 34, 153-169		7
1208	Algorithmic analysis of polygonal hybrid systems, Part II: Phase portrait and tools. *Theoretical Computer Science*, **2008**, 390, 1-26	1.1	2
1207	Automatic generation of path conditions for concurrent timed systems. *Theoretical Computer Science*, **2008**, 404, 275-292	1.1	1
1206	When are Timed Automata weakly timed bisimilar to Time Petri Nets?. *Theoretical Computer Science* , **2008**, 403, 202-220	1.1	14
1205	Verification of qualitative . *Theoretical Computer Science*, **2008**, 409, 24-40	1.1	11
1204	Automatic Verification of Combined Specifications: An Overview. **2008**, 207, 3-16		2
1203	Timed Specification For Web Services Compatibility Analysis. **2008**, 200, 155-170		13
1202	Cooperative Testing of Timed Systems. **2008**, 220, 79-92		9
1201	Approximated Reachability on Hybrid Automata: Falsification meets Certification. **2008**, 223, 47-60		
1200	Post and pre-initialized stopwatch Petri nets: Formal semantics and state space computation. **2008**, 2, 1175-1186		3
1199	FPGA based tester tool for hybrid real-time systems. **2008**, 32, 447-459		3
1198	Timed Component-Interaction Automata for Specification and Verification of Real-Time Reactive Systems. **2008**,		1
1197	A Specification and Verification Method on Component Composition of Real-Time Reactive Systems. **2008**,		
1196	A Method on Specification and Verification of Component Interaction in Real-Time Reactive Systems. **2008**,		
1195	On Verification of Linear Occurrence Properties of Real-Time Systems. **2008**, 207, 107-120		
1194	Semantic domains of timed event structures. **2008**, 34, 125-137		2

1193	Fault Modeling and Detection Capabilities for EFSM Models. **2008**, 57, 1102-1111	1
1192	Analysis of dynamic policies. **2008**, 206, 185-212	9
1191	Timed Petri nets and timed automata: On the discriminating power of zeno sequences. **2008**, 206, 73-107	25
1190	Inclusion dynamics hybrid automata. **2008**, 206, 1394-1424	10
1189	Using timed automata and model-checking to simulate material flow in agricultural production systemsApplication to animal waste management. **2008**, 63, 183-192	10
1188	Systematic Logic Controller Design as Sequential Function Chart Starting from Informal Specifications. **2008**, 16, 43-47	3
1187	Automatic Verification of Biochemical Network Using Model Checking Method. **2008**, 16, 90-94	
1186	NON-BLOCKING SUPERVISORY CONTROL FOR INITIALISED RECTANGULAR AUTOMATA. **2008**, 6, 293-301	
1185	Models and formal verification of multiprocessor system-on-chips. **2008**, 77, 1-19	23
1184	Comparing the Expressiveness of Timed Automata and Timed Extensions of Petri Nets. **2008**, 15-32	17
1183	Hybrid Systems and Biology. **2008**, 424-448	20
1182	A Survey on Continuous Time Computations. **2008**, 383-423	30
1181	Modeling and Testing Secure Web-Based Systems: Application to an Industrial Case Study. **2008**,	3
1180	Embedded Systems Specification and Design Languages. **2008**,	1
1179	Service-Oriented Computing ICSOC 2008. **2008**,	0
1178	Enterprise Information Systems. **2008**,	
1177	UCaS: A Schedulability Analysis Tool for AADL Models. **2008**,	1
1176	New Computational Paradigms. **2008**,	6

1175 Hardware Runtime Monitoring for Dependable COTS-Based Real-Time Embedded Systems. **2008**, 46

1174 Hierarchical, Hybrid Framework for Collision Avoidance Algorithms in the National Airspace. **2008**, 2

1173 Schedulability Analysis of Time-Constrained Cluster Tools With Bounded Time Variation by an Extended Petri Net. **2008**, 5, 490-503 88

1172 Information and Communications Security. **2008**, 2

1171 Formale erg□zen traditionelle Methoden in der Softwareentwicklung. **2008**, 3, 24-27

1170 Model validation for embedded systems using formal method-aided simulation. **2008**, 2, 413 4

1169 Algorithms for Modeling a Class of Single Timing Faults in Communication Protocols. **2008**, 57, 274-288 11

1168 Extending EFSMs to Specify and Test Timed Systems with Action Durations and Time-Outs. **2008**, 57, 835-844 31

1167 Timed Automata Patterns. **2008**, 34, 844-859 43

1166 A Decidable Class of Planar Linear Hybrid Systems. **2008**, 401-414 6

1165 STORMED Hybrid Systems. **2008**, 136-147 21

1164 . **2008**, 19, 851-859 19

1163 Almost-Sure Model Checking of Infinite Paths in One-Clock Timed Automata. **2008**, 16

1162 Description and Discovery of Complex Events in Video Surveillance. **2008**, 3

1161 Expressiveness of verifiable hierarchical clock systems. **2008**, 37, 391-413 1

1160 . **2008**, 1

1159 Real-Time Model Checking: Algorithms and Complexity. **2008**,

1158 Conflict-Tolerant Real-Time Features. **2008**, 1

1157	Intuitive event modeling for personalized behavior monitoring. **2008**,	1
1156	Behavioral Compatibility of Active Components. **2008**,	1
1155	Decomposition of Decidable First-Order Logics over Integers and Reals. **2008**,	3
1154	Software Architecture Behavior Meta-model for Real-Time Systems. **2008**,	
1153	Modeling System Security Rules with Time Constraints Using Timed Extended Finite State Machines. **2008**,	1
1152	. **2008**, 4, 280-291	16
1151	Timed and Hybrid Automata in SAL. **2008**,	
1150	A Game-Theoretic Approach to Real-Time System Testing. **2008**,	5
1149	MIRELA: A Language for Modeling and Analyzing Mixed Reality Applications Using Timed Automata. **2008**,	2
1148	Extending Stream X-Machines to Specify and Test Systems with Timeouts. **2008**,	1
1147	Formal add to traditional methods in software engineering. **2008**, 3, 14-17	
1146	Save-IDE: An Integrated Development Environment for Building Predictable Component-Based Embedded Systems. **2008**,	6
1145	A Fully Automated Framework for Control of Linear Systems from Temporal Logic Specifications. **2008**, 53, 287-297	301
1144	Symbolic Computation of Schedulability Regions Using Parametric Timed Automata. **2008**,	30
1143	Computing Expected Absorption Times for Parametric Determinate Probabilistic Timed Automata. **2008**,	4
1142	PAFSV: A process algebraic framework for SystemVerilog. **2008**,	2
1141	Time-Abstracting Bisimulation for Probabilistic Timed Automata. **2008**,	7
1140	. **2008**,	8

1139	Validating Real Time Specifications using Real Time Event Queue Modeling. **2008**,	1
1138	On the automatic generation of timed automata models from Function Block Diagrams for safety instrumented systems. **2008**,	13
1137	Design of A QoS-Aware Service Composition and Management System in Peer-to-Peer Network Aided by DEVS. **2008**,	1
1136	. **2008**,	15
1135	. **2008**,	3
1134	. **2008**,	2
1133	A Method to Generate Embedded Real-Time System Test Suites Based on Software Architecture Specifications. **2008**,	0
1132	On-line Monitoring of Real Time Applications for Early Error Detection. **2008**,	3
1131	Timed and Hybrid Models. **2008**, 269-325	
1130	Tools for analyzing intelligent agent systems. **2008**, 6, 355-371	7
1129	On modeling decisions for representation of service orchestrators. **2008**,	0
1128	Testing Component-Based Real Time Systems. **2008**,	1
1127	A Boundary Checking Technique for Testing Real-Time Systems Modeled as Timed Input Output Automata (Short Paper). **2008**,	8
1126	Automatic Verification of Wireless Control in a Mining Ventilation System. **2008**,	2
1125	Modeling Online User Behavior. **2008**,	1
1124	Model-driven specification of component-based distributed real-time and embedded systems for verification of systemic QoS properties. **2008**,	2
1123	Timed Properties-Aware Asynchronous Web Service Composition. **2008**, 44-61	7
1122	ON COUNTER MACHINES, REACHABILITY PROBLEMS, AND DIOPHANTINE EQUATIONS. **2008**, 19, 919-934	2

1121	25 Years of Model Checking. **2008**,	37
1120	Algebraic Methodology and Software Technology. **2008**,	
1119	Formal specification of DSP gateway for data transmission between processor cores of OMAP platform. **2008**,	
1118	Towards verifiable deeply embedded systems. **2008**, 5, 1-2	
1117	Durations and parametric model-checking in timed automata. **2008**, 9, 1-23	7
1116	Modeling and analyzing mixed reality applications using timed automata. **2008**,	1
1115	Automatic Generation of Observers for the Dala Robot with TTG. **2008**,	
1114	Produktionssteuerung auf der Grundlage von Echtzeitautomaten (Production Scheduling Using Timed Automata). **2008**, 56, 171-180	2
1113	DEVS Approach to Real-time RTI Design for Large-scale Distributed Simulation Systems. **2008**, 84, 231-238	3
1112	Model checking based analysis of end-to-end latency in embedded, real-time systems with clock drifts. **2008**,	12
1111	Alternating timed automata. **2008**, 9, 1-27	27
1110	A game-theoretic approach to real-time system testing. **2008**,	4
1109	Efficient online monitoring of web-service SLAs. **2008**,	84
1108	Model-based fault detection in context-aware adaptive applications. **2008**,	37
1107	Designing, Modelling and Verifying a Container Terminal System Using UPPAAL. **2008**,	
1106	A Timed Extension of Property Sequence Chart. **2008**,	6
1105	Algorithms for efficient symbolic detection of faults in context-aware applications. **2008**,	2
1104	Software monitoring with bounded overhead. **2008**,	3

1103 Efficient Model-Checking of Dense-Time Systems with Time-Convexity Analysis. **2008**, 3

1102 libFAUDES An open source C++ library for discrete event systems. **2008**, 15

1101 Control of (max,+) automata: Logical and timing aspects. **2008**, 3

1100 Using context descriptions and property definition patterns for software formal verification. **2008**, 4

1099 Soundness test cases generation for duration systems. **2008**, 1

1098 An incremental testing method based on timed Message Sequence Charts. **2008**, 1

1097 Decomposition of timed automata for solving scheduling problems. **2008**, 1

1096 Soft User Behavior Modeling in Virtual Environments. **2008**, 1

1095 Verification of networks of timed automata using mCRL2. **2008**,

1094 Modelling interaction-aware services from an orchestration viewpoint. **2008**, 1

1093 Model checking SystemC designs using timed automata. **2008**, 47

1092 XAL: A Web Oriented Programming Language Based on Timed-Automata. **2008**, 1

1091 Global automata: a new approach on modelling industrial systems. **2008**, 3, 383 3

1090 Formal modelling and discrete-time analysis of BPEL web services. **2008**, 4, 183 7

1089 Event order abstraction for parametric real-time system verification. **2008**, 2

1088 Chapter 12 Temporal Representation and Reasoning. **2008**, 513-550 7

1087 Automatically transforming and relating Uppaal models of embedded systems. **2008**, 5

1086 Testing of Control Programs in Distant Education. **2008**, 41, 11648-11653 1

1085	Verification and Implementation of Dependable Controllers. **2008**,	7
1084	Fault diagnosis in a wireless network. **2008**, 41, 7271-7275	
1083	A modelling approach to formally represent service orchestration. **2008**, 1, 1	3
1082	Bibliography. 304-312	
1081	Uma abordagem intervalar para a caracterizaðo de intervalos de disparo em redes de Petri temporais. **2008**, 19, 379-394	1
1080	Stressing Symbolic Scheduling Techniques within Aircraft Maintenance Optimization. **2008**, 5, 83-110	2
1079	Activity Recognition Using Probabilistic Timed Automata. **2008**,	2
1078	Control of (max,+) automata: A single step approach. **2009**,	1
1077	Time Petri Nets. 123-161	12
1076	Specification Formalisms and Models. **2009**, 2775	1
1075	An Integrated Framework for Web Services Orchestration. **2009**, 6, 1-29	7
1074	On the Complexities of Selected Satisfiability and Equivalence Queries over Boolean Formulas and Inclusion Queries over Hulls. **2009**, 2009, 1-18	4
1073	Component-Based Abstraction Refinement for Timed Controller Synthesis. **2009**,	7
1072	Design methodology internal sub state observer using CPLD. **2009**,	
1071	Modeling and Verification for Timing Satisfaction of Fault-Tolerant Systems with Finiteness. **2009**,	
1070	Timed Model Checking Based Approach for Web Services Analysis. **2009**,	34
1069	REMES: A Resource Model for Embedded Systems. **2009**,	25
1068	Representations and algorithms for finite-state bisimulations of linear discrete-time control systems. **2009**,	

1067 Test Purpose Generation for Timed Protocol Testing. **2009**,

1066 A note on fault diagnosis algorithms. **2009**, 7

1065 Testing of Software and Communication Systems. **2009**, 1

1064 AN INVERSE METHOD FOR PARAMETRIC TIMED AUTOMATA. **2009**, 20, 819-836 47

1063 A Formal Syntax for Probabilistic Timed Property Sequence Charts. **2009**, 8

1062 On identification of input/output extended automata with finite bisimilar quotients. **2009**, 2

1061 Future design challenges for electric energy supply. **2009**, 12

1060 A contractivity approach for probabilistic bisimulations of diffusion processes. **2009**, 14

1059 Model Checking and Artificial Intelligence. **2009**, 1

1058 Survey on Directed Model Checking. **2009**, 65-89 24

1057 Architectures for Adaptive Software Systems. **2009**,

1056 Nano-Net. **2009**,

1055 Save-IDE - A tool for design, analysis and implementation of component-based embedded systems. **2009**, 13

1054 The Effects of Bounding Syntactic Resources on Presburger LTL. **2009**, 19, 1541-1575 11

1053 Formal Modeling and Analysis of Timed Systems. **2009**,

1052 Checking Timed Büchi Automata Emptiness Using LU-Abstractions. **2009**, 228-242 13

1051 Formal Modeling and Analysis of RBC Subsystem in CTCS Level 3 Using UPPAAL. **2009**, 1

1050 Verifying real-time properties by intelligent parsing. **2009**,

1049	Translation-Based Model Checking for PLC Programs. **2009**,	8
1048	Integrating real-time analysis with the dendritic cell algorithm through segmentation. **2009**,	13
1047	Using MDE for the formal verification of embedded systems modeled by UML sequence diagrams. **2009**,	
1046	Checking timed Büchi automata emptiness on simulation graphs. **2009**, 10, 1-19	28
1045	Verification and performance evaluation of a congestion control approach within vehicular ad hoc networks. **2009**,	1
1044	Timed P Automata. **2009**, 94, 1-19	1
1043	A Decidable Probability Logic for Timed Probabilistic Systems. **2009**, 96, 127-151	
1042	Critical Infrastructures Security Modeling, Enforcement and Runtime Checking. **2009**, 95-108	2
1041	Using improved resource interfaces to formally describe adaptability in embedded systems. **2009**, 6, 1-5	
1040	Scenario-based timing verification of multiprocessor embedded applications. **2009**, 14, 1-58	3
1039	Complexity results in revising UNITY programs. **2009**, 4, 1-28	16
1038	A natural, tiered and executable UIDL for 3D user interfaces based on Concept-Oriented Design. **2009**, 16, 1-36	7
1037	Undecidability Results for Timed Automata with Silent Transitions. **2009**, 92, 1-25	5
1036	Heuristic and exact techniques for aircraft maintenance scheduling. **2009**, 223, 989-999	3
1035	TCTL Model Checking of Time Petri Nets. **2009**, 19, 1509-1540	43
1034	Reliability and Risk Issues in Large Scale Safety-critical Digital Control Systems. **2009**,	9
1033	An algebra of hybrid systems. **2009**, 78, 74-97	23
1032	POETS: Process-oriented event-driven transaction systems. **2009**, 78, 381-401	10

1031 A Petri net approach for the design and analysis of Web Services Choreographies. **2009**, 78, 359-380 18

1030 Schedulability of asynchronous real-time concurrent objects. **2009**, 78, 402-416 29

1029 Monitoring embedded software timing properties with an SoC-resident monitor. **2009**, 3, 140 5

1028 Dynamic state reconciliation and model-based fault detection for chemical processes. **2009**, 4, 929-941 1

1027 Use Case Maps as a property specification language. **2009**, 8, 205-220 11

1026 Automata and logics over finitely varying functions. **2009**, 161, 324-336

1025 Weighted o-minimal hybrid systems. **2009**, 161, 268-288 1

1024 Model Checking Biological Oscillators. **2009**, 229, 41-58 12

1023 Hybrid Automata in Systems Biology: How Far Can We Go?. **2009**, 229, 93-108 4

1022 Model-checking Timed Temporal Logics. **2009**, 231, 323-341 29

1021 An Automata-based Approach for CTL? With Constraints. **2009**, 239, 193-211 7

1020 Influence of different abstractions on the performance analysis of distributed hard real-time systems. **2009**, 13, 27-49 27

1019 Discrete Semantics for Hybrid Automata. **2009**, 19, 471-493 9

1018 Supervisory Control of (max,+) Automata: A Behavioral Approach. **2009**, 19, 525-549 23

1017 Timed verification of the generic architecture of a memory circuit using parametric timed automata. **2009**, 34, 59-81 18

1016 Conformance testing for real-time systems. **2009**, 34, 238-304 100

1015 Decision problems for lower/upper bound parametric timed automata. **2009**, 35, 121-151 41

1014 Formal verification of real-time systems with preemptive scheduling. **2009**, 41, 118-151 29

1013	A development methodology for embedded systems based on RT-DEVS. **2009**, 5, 117-127		15
1012	Verification of real-time systems with preemption: negative and positive results. **2009**, 5, 163-179		
1011	Runtime Monitoring CompositeWeb Services Through Stateful Aspect Extension. **2009**, 24, 294-308		9
1010	Categories of Timed Stochastic Relations. **2009**, 249, 193-217		
1009	Analyzing a Pattern-Based Model of a Real-Time Turntable System. **2009**, 253, 161-178		4
1008	SAT-based Verification for Timed Component Connectors. **2009**, 255, 103-118		15
1007	Experimenting statecharts for multiple experts knowledge elicitation in agriculture. **2009**, 36, 11296-11303		20
1006	PolyOrBAC: A security framework for Critical Infrastructures. **2009**, 2, 154-169		30
1005	Towards a real-time distributed computing model. *Theoretical Computer Science*, **2009**, 410, 629-659	1.1	7
1004	On minimal elements of upward-closed sets. *Theoretical Computer Science*, **2009**, 410, 2442-2452	1.1	5
1003	Robustness of temporal logic specifications for continuous-time signals. *Theoretical Computer Science*, **2009**, 410, 4262-4291	1.1	294
1002	Automatic verification of control logics in safety instrumented system design for chemical process industry. **2009**, 22, 975-980		8
1001	Modeling and verification of real-time embedded systems with urgency. **2009**, 82, 1627-1641		3
1000	Fault masking by multiple timing faults in timed EFSM models. **2009**, 53, 596-612		7
999	Linear coupled component automata for MILP modeling of hybrid systems. **2009**, 33, 162-175		5
998	Model-based fault diagnosis for hybrid systems: Application on chemical processes. **2009**, 33, 1617-1630		20
997	Multi-product batch scheduling with intermediate due dates using priced timed automata models. **2009**, 33, 1661-1676		13
996	Timed P Automata. **2009**, 227, 21-36		3

995 Reachability in Timed Counter Systems. **2009**, 239, 167-178 5

994 Discounting in Time. **2009**, 253, 25-31 3

993 Temporal constraints of a gene regulatory network: Refining a qualitative simulation. **2009**, 98, 149-59 13

992 Switched discrete-time systems with time-varying delays: A generalized . **2009**, 57, 79-95 23

991 Formal specification and simulation of the robot path planner. **2009**, 0

990 A Computationally Efficient Scheme for Model Predictive Control of Nonlinear Hybrid Systems Using Generalized Outer Approximation **2009**, 48, 5767-5778 5

989 Formal Specification and Code Generation of Programable Logic Controllers. **2009**, 1

988 Modeling and Verifying Physical Properties of Security Protocols for Wireless Networks. **2009**, 21

987 Towards a Unified Behavioral Model for Component-Based and Service-Oriented Systems. **2009**, 1

986 An edit-distance model for the approximate matching of timed strings. **2009**, 31, 736-41 7

985 A cooperative and fully-distributed congestion control approach within VANETs. **2009**, 6

984 Specifying and Monitoring Temporal Properties in Web Services Compositions. **2009**, 22

983 Testing Timed Finite State Machines with Guaranteed Fault Coverage. **2009**, 66-80 9

982 Testing Web Services Composition Using the TGSE Tool. **2009**, 8

981 Parallel Computing Technologies. **2009**, 1

980 Towards Adaptable SOA: Model Driven Development, Context and Aspect. **2009**, 175-189 3

979 Privacy Time-Related Analysis in Business Protocols. **2009**, 0

978 Minimal Cost Reachability/Coverability in Priced Timed Petri Nets. **2009**, 348-363 15

977	Integrated Modeling and Verification of Real-Time Systems through Multiple Paradigms. **2009**,	4
976	Computing needs time. **2009**, 52, 70-79	126
975	Advanced Information Systems Engineering. **2009**,	1
974	Using formal specifications to support testing. **2009**, 41, 1-76	207
973	Coloured Petri Nets. **2009**,	520
972	Modeling and Analysis of Probabilistic Timed Systems. **2009**,	
971	Applying Formal Passive Testing to Study Temporal Properties of the Stream Control Transmission Protocol. **2009**,	3
970	Supervisory control for real-time systems based on conflict-tolerant controllers. **2009**,	
969	. **2009**,	10
968	Supporting the Extraction of Timed Properties for Passive Testing by Using Probabilistic User Models. **2009**,	2
967	Efficient Model-Checking for Real-Time Task Networks. **2009**,	7
966	A Modest Approach to Checking Probabilistic Timed Automata. **2009**,	34
965	A Conservative Approximation Method for the Verification of Preemptive Scheduling Using Timed Automata. **2009**,	6
964	Formal verification of UML-modeled machine controls. **2009**,	10
963	Applying REMES behavioral modeling to PLC systems. **2009**,	3
962	An extension of the Workflow Process Definition Language to validate time restrictions in SUBFLOW Activities and XOR SPLIT Attributes. **2009**,	
961	Coordination of multiple non-holonomic agents with input constraints. **2009**,	7
960	Testing real-time task networks with functional extensions using model-checking. **2009**,	6

959	Formal verification of PLC controlled systems using Sensor Graphs. **2009**,	3
958	Passive Testing of Stochastic Timed Systems. **2009**,	6
957	Positive and Negative Results on the Decidability of the Model-Checking Problem for an Epistemic Extension of Timed CTL. **2009**,	3
956	Time-Optimal Real-Time Test Case Generation Using Prioritized Time Petri Nets. **2009**,	
955	. **2009**,	
954	. **2009**,	4
953	Profinet IO IRT Message Scheduling. **2009**,	20
952	Quantitative Model Checking of Continuous-Time Markov Chains Against Timed Automata Specifications. **2009**,	31
951	HieroMate: A Graphical Tool for Specification and Verification of Hierarchical Hybrid Automata. **2009**, 695-702	1
950	Using Runtime Verification to Design a Reliable Execution Framework for Scientific Workflows. **2009**,	
949	Verification of Real-Time Systems: Application to the Transportation Domain. **2009**,	
948	Formal Semantics of the ProCom Real-Time Component Model. **2009**,	8
947	MDE approach to the co-synthesis of embedded systems using a MOF-based internal design representation. **2009**,	
946	. **2009**,	11
945	Environmental Simulation of Real-Time Systems with Nested Interrupts. **2009**,	3
944	Model Checking Timed and Stochastic Properties with CSL^{TA}. **2009**, 35, 224-240	61
943	Building Meaningful Timed Plant Models for Verification Purposes. **2009**, 42, 966-971	1
942	Finding the bounds of response time of networked automation systems by iterative proofs. **2009**, 42, 1382-1387	1

941	TMM: Temporal Moore Machines. **2009**, 42, 1388-1393	
940	Online Diagnosis of Systems with Rectangular Hybrid Automata Models. **2009**, 42, 954-959	2
939	Reachability Analysis of Variants of Communication-Free Petri Nets. **2009**, E92-D, 377-388	1
938	Timed Delay Bisimulation is an Equivalence Relation for Timed Transition Systems. **2009**, 93, 127-142	4
937	Monitoring of Temporal Requirements with Stocharts Observation Patterns, Application to a Level Crossing Control System. **2009**, 42, 63-68	
936	Verification of functional constraints for safe product driven control. **2009**, 42, 275-280	1
935	Diagnosis of Faults in Real-Time Discrete Event System. **2009**, 42, 1557-1562	2
934	Timed Automata Models for Batch Scheduling with Sequence-Dependent Changeovers. **2009**, 27, 1515-1520	3
933	Tight WCRT analysis of synchronous C programs. **2009**,	22
932	Fault detection and diagnosis based on a hybrid dynamic simulator. **2009**, 42, 223-228	1
931	Playing Games with Timed Games. **2009**, 42, 238-243	5
930	Towards formal security analysis of GTRBAC using timed automata. **2009**,	14
929	Towards a Resource-Aware Component Model for Embedded Systems. **2009**,	1
928	An Extension of Mazukiewicz Traces and their Applications in Specification of Real-Time Systems. **2010**,	1
927	Compatibility verification of real-time components' behavior. **2010**,	
926	Backward Time Analysis for the diagnosis of discrete event systems. **2010**, 43, 128-133	
925	An efficient and user-friendly optimization framework for batch process scheduling. **2010**, 43, 28-33	
924	Synchronous composition of interval weighted automata. **2010**, 43, 318-323	2

923 Using Model Checking for Analyzing Distributed Power Control Problems. **2010**, 2010,

922 Verification of Flexray Start-Up Mechanism by Timed Automata. **2010**, 17, 461-480 2

921 An Identification Technique for Timed Event Systems. **2010**, 43, 181-186

920 Well (and Better) Quasi-Ordered Transition Systems. **2010**, 16, 457-515 35

919 Formal language properties of hybrid systems with strong resets. **2010**, 44, 79-111 1

918 Unifying behavioral equivalences of timed transition systems. **2010**, 36, 138-148

917 Synthesis of distinguishing test cases for timed finite state machines. **2010**, 36, 216-224 2

916 Component architecture of model-based testing environment. **2010**, 36, 289-305

915 Timing analysis of scenario-based specifications using linear programming. **2010**, 22, n/a-n/a

914 Material Flow Abstraction of Manufacturing Systems. **2010**, 153-167

913 Reachability results for timed automata with unbounded data structures. **2010**, 47, 279-311 5

912 A process algebraic framework for specification and validation of real-time systems. **2010**, 22, 153-191 39

911 Detecting synchronisation of biological oscillators by model checking. *Theoretical Computer Science*, **2010**, 411, 1999-2018 1.1 18

910 Proved development of the real-time properties of the IEEE 1394 Root Contention Protocol with the event-B method. **2010**, 12, 39-51 8

909 The clock constraint specification language for building timed causality models. **2010**, 6, 99-106 22

908 Event-driven modeling and testing of real-time web services. **2010**, 4, 3-15 10

907 A new model for model checking: cycle-weighted Kripke structure. **2010**, 4, 78-88

906 Constraint-based modeling of discrete event dynamic systems. **2010**, 21, 31-47 5

905	SetExp: a method of transformation of timed automata into finite state automata. **2010**, 46, 189-250		5
904	Real-Time Embedded Software Design for Mobile and Ubiquitous Systems. **2010**, 59, 13-32		2
903	Model Checking for a Class of Weighted Automata. **2010**, 20, 103-137		9
902	Scenario-based verification of real-time systems using Uppaal. **2010**, 37, 200-264		12
901	Synthesising certificates in networks of timed automata. **2010**, 4, 222		1
900	A pervasive visual-haptic framework for virtual delivery training. **2010**, 14, 326-34		13
899	. **2010**, 98, 562-583		19
898	Quantitative analysis of weighted transition systems. **2010**, 79, 689-703		36
897	On the expressiveness of TPTL and MTL. **2010**, 208, 97-116		24
896	Fault diagnosis for discrete event systems: Modelling and verification. **2010**, 95, 369-378		28
895	Aspects of availability. **2010**, 75, 516-542		3
894	Timed Property Sequence Chart. **2010**, 83, 371-390		20
893	An evaluation of timed scenario notations. **2010**, 83, 326-350		7
892	Cost Analysis for Embedded Systems: Experiments with Priced Timed Automata. **2010**, 238, 81-95		6
891	Coinduction in Concurrent Timed Systems. **2010**, 264, 177-197		1
890	Hybrid automata, reachability, and Systems Biology. *Theoretical Computer Science*, **2010**, 411, 2037-2051	1.1	1
889	Model checking memoryful linear-time logics over one-counter automata. *Theoretical Computer Science*, **2010**, 411, 2298-2316	1.1	8
888	Learning of event-recording automata. *Theoretical Computer Science*, **2010**, 411, 4029-4054	1.1	30

887 Weak bisimulation for Probabilistic Timed Automata. *Theoretical Computer Science*, **2010**, 411, 4291-4322 1.1 9

886 Verification of real-time systems design. **2010**, 20, 3-37 4

885 Wireless ventilation control for large-scale systems: The mining industrial case. **2010**, 20, 226-251 18

884 Robustness testing of composed real-time systems. **2010**, 10, S135-S148

883 Multi-Robot Systems: Modeling, Specification, and Model Checking. **2010**, 6

882 Formal Verification of Hybrid Automotive Systems. **2010**,

881 Extended Petri net based formal modeling and verification of WTB-TCN device. **2010**,

880 Towards a Component-Based Requirements Modeling for Automotive Systems. **2010**, 40-41, 955-960

879 The Theory of Timed I/O Automata, Second Edition. **2010**, 1, 1-137 17

878 Formal Modeling of Automotive Software Requirements by Correctness. **2010**, 40-41, 961-967

877 Reachability analysis of multi-affine systems. **2010**, 32, 445-467 4

876 Short-Term Scheduling of Multi-Product Batch Plants with Sequence-Dependent Changeovers Using Timed Automata Models. **2010**, 1201-1206 10

875 Stability analysis and controller synthesis for hybrid dynamical systems. **2010**, 368, 4937-60 31

874 Advances and challenges of probabilistic model checking. **2010**, 27

873 A Model Checking Approach for Multi-core Time Partitions Design. **2010**,

872 OMEGA2: A New Version of the Profile and the Tools. **2010**, 6

871 Conflict-Tolerant Real-Time Specifications in Metric Temporal Logic. **2010**, 4

870 Abstraction of continuous dynamical systems utilizing Lyapunov functions. **2010**, 4

869	An integrated approach to the symbolic control design of nonlinear systems with infinite states specifications. **2010**,	
868	Enhancing Transparent Fuzzy Controllers Through Temporal Concepts: An Application to Computer Games. **2010**,	5
867	Comparison of the Expressiveness of Time Petri Nets and Timed Automata. **2010**,	1
866	. **2010**,	
865	Schedulability analysis of multi-processor real-time systems using Uppaal. **2010**,	
864	Time-Bounded Reachability Probabilities in Continuous-Time Markov Decision Processes. **2010**,	17
863	Fault Coverage Measurement of a Timed Test Case Generation Approach. **2010**,	3
862	Logical Analysis of Hybrid Systems. **2010**,	160
861	A Model-Based Framework for Adapting Interaction Mismatches of Time-Aware Web Services. **2010** ,	0
860	Watson-Crick online tessellation automaton and timed Watson-Crick automaton. **2010**,	
859	Hybridizing fuzzy control and timed automata for modeling variable structure fuzzy systems. **2010**,	7
858	Scenario-based analysis and synthesis of real-time systems using uppaal. **2010**,	8
857	. **2010**,	1
856	CONTINUOUS PETRI NETS: EXPRESSIVE POWER AND DECIDABILITY ISSUES. **2010**, 21, 235-256	15
855	Formal Methods for Components and Objects. **2010**,	5
854	Research on the Mobility Management Scheme in Heterogeneous Network. **2010**,	1
853	Predicting Events for the Simulation of Hybrid Systems. **2010**,	1
852	Modelling and analysis of real-time systems on multi-core processors. **2010**,	

851 Modeling Automotive Software Requirements with Aspectual Models. **2010**,

850 . **2010**, 11

849 Modeling time in computing. **2010**, 42, 1-59 35

848 A Cooperative Congestion Control Approach within VANETs: Formal Verification and Performance Evaluation. **2010**, 2010, 17

847 Differential-algebraic Dynamic Logic for Differential-algebraic Programs. **2010**, 20, 309-352 85

846 Hierarchische Modellierung und Regelung hybrider Systeme auf zwei Ebenen. **2010**, 58, 458-466

845 Application of RT-DEVS in military. **2010**, 2

844 Analysis and applications of timed service protocols. **2010**, 19, 1-38 21

843 . **2010**,

842 Notice of Retraction: Innovative large Tank Farm oil terminal monitoring. **2010**,

841 Implementing global automata in PLCs based on the IEC 61131-3 programming norm. **2010**, 6, 163 5

840 Rational time-advance DEVS (RTA-DEVS). **2010**, 7

839 Temporal verification of RT-DEVS models with implementation aspects. **2010**, 1

838 A pragmatic testing approach for wireless sensor networks. **2010**, 2

837 Temperature- and energy-constrained scheduling in multitasking systems. **2010**, 6

836 Reducing the state space of incompletely specified timed Moore machines. **2010**,

835 Schedulability and end-to-end latency in distributed ECU networks. **2010**, 17

834 Time and alternation. **2010**, 1

833	Model-based implementation of real-time applications. **2010**,	39
832	REMES tool-chain. **2010**,	6
831	Approximate Model Checking of Stochastic Hybrid Systems. **2010**, 16, 624-641	109
830	Robust reach-avoid controller synthesis for switched nonlinear systems. **2010**,	17
829	Conformance Testing of Temporal Role-Based Access Control Systems. **2010**, 7, 144-158	29
828	Timed Automata Model for Component-Based Real-Time Systems. **2010**,	3
827	Context-Enhanced Directed Model Checking. **2010**, 88-105	5
826	Utilization of Timed Automata as a Verification Tool for Security Protocols. **2010**,	1
825	Safraless Procedures for Timed Specifications. **2010**, 2-22	4
824	Reconciling Urgency and Variable Abstraction in a Hybrid Compositional Setting. **2010**, 47-61	1
823	Computing Equilibria in Two-Player Timed Games via Turn-Based Finite Games. **2010**, 62-76	3
822	Robust Satisfaction of Temporal Logic over Real-Valued Signals. **2010**, 92-106	231
821	Combining Symbolic Representations for Solving Timed Games. **2010**, 107-121	8
820	Expected Reachability-Time Games. **2010**, 122-136	6
819	From Mtl to Deterministic Timed Automata. **2010**, 152-167	17
818	Unambiguity in Timed Regular Languages: Automata and Logics. **2010**, 168-182	3
817	Memory Event Clocks. **2010**, 198-212	3
816	Simulation and Bisimulation for Probabilistic Timed Automata. **2010**, 213-227	3

815	Layered Composition for Timed Automata. **2010**, 228-242	8
814	A Conformance Testing Relation for Symbolic Timed Automata. **2010**, 243-255	9
813	Fuzzy-Timed Automata. **2010**, 140-154	3
812	Alternating Timed Automata over Bounded Time. **2010**,	5
811	Energy Efficient Scheduling for Cluster-Tree Wireless Sensor Networks With Time-Bounded Data Flows: Application to IEEE 802.15.4/ZigBee. **2010**, 6, 438-450	78
810	Exploiting Timed Automata-based Fuzzy Controllers and data mining to detect computer network intrusions. **2010**,	2
809	4 Semantics of UML Models for Dynamic Behavior. **2010**, 77-103	3
808	Testing Software and Systems. **2010**,	
807	Leveraging Applications of Formal Methods, Verification, and Validation. **2010**,	
806	Conformance testing of variable driven automata. **2010**,	3
805	Verification of a Timed Multitask System With Uppaal. **2010**, 7, 921-932	34
804	Abstract State Machines, Alloy, B and Z. **2010**,	1
803	UPPAAL-based real-time testing: New coverage criteria and length-optimal test generation. **2010**,	
802	WSOTF: An Automatic Testing Tool for Web Services Composition. **2010**,	6
801	On the Toyota's Throttle Control Problem. **2010**,	1
800	A novel analytical framework for qualitative Model-Based Fault Diagnosis. **2010**,	
799	Event scheduling of timed automata using Dioid Algebra. **2010**,	0
798	Tank Gauging control system monitoring based on innovative DCS. **2010**,	

797	Synthesis and stochastic assessment of cost-optimal schedules. **2010**, 12, 305-318	9
796	Generating Reliable Code from Hybrid-Systems Models. **2010**, 59, 1281-1294	7
795	Time and Probability-Based Information Flow Analysis. **2010**, 36, 719-734	10
794	Guest Editors' Introduction: 2008 Conference on the Foundations of Software Engineering. **2010**,	
793	A Novel SAT-Based Approach to the Task Graph Cost-Optimal Scheduling Problem. **2010**, 29, 2027-2040	1
792	Combining Control and Data Abstraction in the Verification of Hybrid Systems. **2010**, 29, 1481-1494	3
791	The concept of postponed event in timed discrete event systems and its PLC implementation. **2010** ,	1
790	Parametric analysis of distributed firm real-time systems: A case study. **2010**,	2
789	Introduction to Databases. **2010**,	20
788	An Approach to Verifying Security and Timing Properties in UML Models. **2010**,	6
787	. **2010**,	2
786	Consistency's checking of chronicles' set using Time Petri Nets. **2010**,	0
785	Reflections on the Work of C.A.R. Hoare. **2010**,	9
784	Computer Performance Engineering. **2010**,	
783	Specification and verification of security properties of e-contracts. **2010**,	1
782	Black-Box System Testing of Real-Time Embedded Systems Using Random and Search-Based Testing. **2010**, 95-110	42
781	Kleene, Rabin, and Scott Are Available. **2010**, 462-477	5
780	Computer Safety, Reliability, and Security. **2010**,	3

779 Fully Symbolic Timed Model Checking Using Constraint Matrix Diagrams. **2010**, 9

778 Programming Multi-Agent Systems. **2010**, 1

777 Principles of Distributed Systems. **2010**, 2

776 Automatic timed automata extraction from ladder programs for model-based analysis of control systems. **2010**, 3

775 Synthesizing bots emotional behaviors through fuzzy cognitive processes. **2010**, 2

774 Composition challenges and approaches for cyber physical systems. **2010**, 31

773 Prototyping Hierarchically Scheduled Systems Using Task Automata and TIMES. **2010**,

772 A domain-specific language for modeling and analyzing solution spaces for technology roadmapping. **2021**, 111094 1

771 Online Strategy Synthesis for Safe and Optimized Control of Steerable Needles. 348, 128-135 1

770 From Semantics to Spatial Distribution. **2000**, 427-436

769 Real-Time Automata and the Kleene Algebra of Sets of Real Numbers. **2000**, 279-289 4

768 A Complete Axiomatisation for Timed Automata. **2000**, 277-289

767 On the Representation of Timed Polyhedra. **2000**, 793-807 3

766 Timed Automata. **2000**, 233-264 1

765 Specification and Analysis of Automata-Based Designs. **2000**, 176-193 1

764 Uniform Reachability Algorithms. **2000**, 215-228

763 Verification of IL Programs with an Explicit Model of their PLC Execution. **2000**, 283-290

762 Using Cylindrical Algebraic Decomposition for the Analysis of Slope Parametric Hybrid Automata. **2000**, 252-263 2

761	Assertion-Based Analysis of Hybrid Systems with PVS. **2001**, 94-109	2
760	On the Category of Event Structures with Dense Time. **2001**, 287-298	
759	Analyzing Fair Parametric Extended Automata. **2001**, 335-355	5
758	Accurate Widenings and Boundedness Properties of Timed Systems. **2001**, 79-94	2
757	A Real-Time Animator for Hybrid Systems. **2001**, 134-145	2
756	Constraint Database Models Characterizing Timed Bisimilarity. **2001**, 245-258	1
755	Decidable Approximations on Generalized and Parameterized Discrete Timed Automata. **2001**, 529-539	3
754	Hybrid Abstractions that Preserve Timed Languages. **2001**, 501-514	4
753	Observational Semantics for Timed Event Structures. **2001**, 214-224	3
752	On Presburger Liveness of Discrete Timed Automata. **2001**, 132-143	1
751	Characterizing Timed Net Processes Categorically. **2001**, 128-141	
750	Analysis of Reactive Systems with n Timers. **2001**, 27-38	
749	Decidability of Safety Properties of Timed Multiset Rewriting. **2002**, 165-183	1
748	Formal Requirements Engineering Using Observer Models. **2002**, 264-278	
747	Compositional Termination Analysis of Symbolic Forward Analysis. **2002**, 109-125	
746	A Decidable Fixpoint Logic for Time-Outs*. **2002**, 255-272	1
745	Conformance Testing Techniques for Timed Systems. **2002**, 1-19	
744	A Method for Functional Testing of Media Synchronization Protocols. **2002**, 539-550	1

743 Distributed Real-Time Automata. **2003**, 131-140

742 On the Correctness of an Intrusion-Tolerant Group Communication Protocol. **2003**, 231-246 3

741 Discrete Timed Automata and MONA: Description, Specification and Verification of a Multimedia Stream. **2003**, 177-192 3

740 Real-Time Systems. **2003**, 45-54

739 Testing Protocol Robustness. **2003**, 201-215

738 A retrospective on constraint databases. **2003**, 0

737 Automated verification using deduction, exploration, and abstraction. **2003**, 333-351 0

736 Towards UML-Based Formal Specifications of Component-Based Real-Time Software. **2003**, 118-134

735 Interval Approach to Parallel Timed Systems Verification. **2003**, 100-116

734 Abstractions from time. **2003**, 95-107 1

733 New Complexity Results for Some Linear Counting Problems Using Minimal Solutions to Linear Diophantine Equations. **2003**, 163-175

732 Robotics, Hybrid Systems for. **2003**, 283-292

731 Real-Time Systems Development Using Hierarchical State Machines. **2003**, 110-121 2

730 Temporale Spezifikationslogiken (Temporal Logics for Specifications). **2003**, 51, A1-A4

729 Modeling Discretely Timed Systems Using Different Magnitudes of Non-standard Reals. **2004**, 218-233

728 On the Urgency Expressiveness. **2004**, 71-83

727 Theorem Proving Languages for Verification. **2004**, 11-14

726 On Timing Analysis of Combinational Circuits. **2004**, 204-218 6

725	Model Checking Polygonal Differential Inclusions Using Invariance Kernels. **2004**, 110-121	3
724	Model Checking for Probabilistic Timed Systems. **2004**, 189-229	3
723	Real-Counter Automata and Their Decision Problems. **2004**, 198-210	1
722	Model Checking Restricted Sets of Timed Paths. **2004**, 432-447	3
721	Connectivity Testing Through Model-Checking. **2004**, 167-184	2
720	On the Verification of Cooperating Traffic Agents. **2004**, 77-110	7
719	Sound Code Generation from Communicating Hybrid Models. **2004**, 432-447	6
718	Closed-Loop Modeling and Related Problems of Embedded Control Systems in Engineering. **2004**, 6-19	6
717	ELSE: A New Symbolic State Generator for Timed Automata. **2004**, 273-280	2
716	Towards Timed Automata and Multi-agent Systems. **2004**, 161-172	3
715	Diagnostic Tests for Real Time Systems. **2004**, 1128-1133	
714	Open Maps and Trace Semantics for Timed Partial Order Models. **2004**, 248-259	
713	Causality and Scheduling Constraints in Heterogeneous Reactive Systems Modeling. **2004**, 1-16	2
712	Formal Design and Verification of Real-Time Embedded Software. **2004**, 382-397	1
711	Computing Schedules for Multithreaded Real-Time Programs Using Geometry. **2004**, 325-342	1
710	Analysis of Real Time Operating System Based Applications. **2004**, 219-233	4
709	Model Checking Prioritized Timed Automata. **2005**, 370-384	8
708	Generalized Abstract Non-interference: Abstract Secure Information-Flow Analysis for Automata. **2005**, 221-234	1

707 Verification of Parameterized Timed Systems. **2005**, 95-97

706 Conformance Testing of Real-Time Component Based Systems. **2005**, 167-181

705 Automata-Theoretic Performance Analysis Method of Soft Real-Time Systems. **2005**, 1211-1224

704 Verification in Predicate Logic with Time: Algorithmic Questions. **2005**, 3-17

703 A New Modality for Almost Everywhere Properties in Timed Automata. **2005**, 110-124 6

702 Timed Weak Simulation Verification and Its Application to Stepwise Refinement of Real-Time Software. **2005**, 381-394 2

701 Integrated Tool for Testing Timed Systems. **2005**, 153-166 0

700 Average Reward Timed Games. **2005**, 65-80 7

699 Testing Deadlock-Freeness in Real-Time Systems: A Formal Approach. **2005**, 95-109 2

698 Quantitative and Qualitative Analysis of Temporal Aspects of Complex Activities. **2005**, 129-143

697 Modal Logics for Timed Control. **2005**, 81-94 3

696 Testing Communicating Systems: a Model, a Methodology, and a Tool. **2005**, 111-128 4

695 SVERTS Specification and Validation of Real-Time and Embedded Systems. **2005**, 33-42

694 Verification of Qualitative Z Constraints. **2005**, 518-532 3

693 Polyhedra-Based Approach for Incremental Validation of Real-Time Systems. **2005**, 184-193 2

692 A Calculus for Shapes in Time and Space. **2005**, 463-477 15

691 Deductive Probabilistic Verification Methods of Safety, Liveness and Nonzenoness for Distributed Real-Time Systems. **2005**, 332-345

690 Symbolic Model Checking of Finite Precision Timed Automata. **2005**, 272-287 1

689	Behavioral Types for Embedded Software A Survey. **2005**, 82-106	1
688	Computation Platform for Automatic Analysis of Embedded Software Systems Using Model Based Approach. **2005**, 114-128	4
687	How to Fix It: Using Fixpoints in Different Contexts. **2005**, 184-193	
686	Discrete Geometry Applied in Hard Real-Time Systems Validation. **2005**, 23-33	4
685	SYMTC: An Efficient Symbolic Model Checker for Embedded Systems. **2005**, 5, 144-148	
684	Verification of the Generic Architecture of a Memory Circuit Using Parametric Timed Automata. **2006**, 113-127	2
683	On Continuous Timed Automata with Input-Determined Guards. **2006**, 369-380	2
682	Model-Based Tool-Chain Infrastructure for Automated Analysis of Embedded Systems. **2006**, 523-537	1
681	A Compositional Algorithm for Parallel Model Checking of Polygonal Hybrid Systems. **2006**, 168-182	5
680	Timed Temporal Logics for Abstracting Transient States. **2006**, 337-351	4
679	Symbolic and on the Fly Testing with Real-Time Observers. **2006**, 306-323	
678	Literaturverzeichnis. **2006**, 535-637	
677	Time Aware Modelling and Analysis of Multiclocked VLSI Systems. **2006**, 737-756	
676	Branching-Time Property Preservation Between Real-Time Systems. **2006**, 260-275	1
675	Spatio-temporal Model Checking for Mobile Real-Time Systems. **2006**, 347-361	1
674	Test Case Minimization for Real-Time Systems Using Timed Bound Traces. **2006**, 289-305	1
673	Managing Ad-Hoc Networks Through the Formal Specification of Service Requirements. **2006**, 164-178	1
672	Static Analysis for State-Space Reduction of Polygonal Hybrid Systems. **2006**, 306-321	3

671 Refinements and Abstractions of Signal-Event (Timed) Languages. **2006**, 67-81 1

670 Model Checking Timed Systems with Urgencies. **2006**, 67-81 3

669 Interesting Properties of the Real-Time Conformance Relation tioco. **2006**, 317-331 4

668 Decreasing Maintenance Costs by Introducing Formal Analysis of Real-Time Behavior in Industrial Settings. **2006**, 130-145

667 Scenario-Based Timing Consistency Checking for Time Petri Nets. **2006**, 388-403

666 Modeling an Impact Control Strategy Using Hypa. **2006**, 56-63

665 Analyzing Chain Programs over Difference Constraints. **2006**, 171-180

664 Bounded Model Checking for GSMP Models of Stochastic Real-Time Systems. **2006**, 19-33 9

663 Assessing the Expressivity of Formal Specification Languages. **2006**, 220-234

662 Probabilistic Modelling and Evaluation of Soft Real-Time Embedded Systems. **2006**, 206-215 3

661 Extending EFSMs to Specify and Test Timed Systems with Action Durations and Timeouts. **2006**, 372-387 5

660 On the Computational Power of Timed Differentiable Petri Nets. **2006**, 230-244 1

659 Intersection of Regular Signal-Event (Timed) Languages. **2006**, 52-66 3

658 Timed Discrete Event Control of a Parallel Production Line with Continuous Output 11Work partially done in the framework of the HYCON Network of Excellence, contract number FP6-IST-511368. **2006**, 205-210

657 Specification of the Multimedia Documents Scenari by the Petri Networks and the Timed Automata. **2006**, 6, 2168-2176

656 Simulating a Human Cooperative Problem Solving. **2007**, 225-235

655 Decision Problems for Lower/Upper Bound Parametric Timed Automata. **2007**, 925-936 2

654 Symbolic Analysis for GSMP Models with One Stateful Clock. **2007**, 90-103 6

653	Improvements for the Symbolic Verification of Timed Automata. **2007**, 196-210	
652	Games, Time, and Probability: Graph Models for System Design and Analysis. **2007**, 103-110	1
651	VeSTA: A Tool to Verify the Correct Integration of a Component in a Composite Timed System. **2007**, 116-135	
650	Adapting the UPPAAL Model of a Distributed Lift System. **2007**, 81-97	
649	Fault Detection for HDS by Means of Neural Networks: Application to Two Tanks Hydraulic System. **2007**, 980-985	
648	Modeling Urgency in Component-Based Real-Time Systems. **2007**, 248-255	1
647	Modeling and Automatic Failure Analysis of Safety-Critical Systems Using Extended Safecharts. **2007**, 451-464	
646	Reachability Analysis of Cost-Reward Timed Automata for Energy Efficiency Scheduling. **2007**,	
645	On Resource-Sensitive Timed Component Connectors. **2007**, 301-316	9
644	Schedule Verification and Synthesis for Embedded Real-Time Components. **2007**, 137-153	0
643	Timed Automata-Based Rehabilitation Training Game Design for the Affected Lower Extremity of Hemiparetic Patient. **2008**, 17-27	2
642	Gesture Based Interface for Crime Scene Analysis: A Proposal. **2008**, 143-154	1
641	Transactor-Based Formal Verification of Real-Time Embedded Systems. **2008**, 255-270	
640	Model Checking on Higher-Level Design Descriptions. **2008**, 163-186	
639	Deep Random Search for Efficient Model Checking of Timed Automata. **2008**, 111-124	1
638	A Partial Order Reduction Technique for Parallel Timed Automaton Model Checking. **2008**, 262-276	1
637	Average-Price-per-Reward Games on Hybrid Automata with Strong Resets. **2008**, 167-181	
636	Exploration of the Dendritic Cell Algorithm with the Duration Calculus.	

635 Synthesis of Non-Interferent Timed Systems. **2009**, 28-42 2

634 A Compositional Translation of Timed Automata with Deadlines to Uppaal Timed Automata. **2009**, 179-194 4

633 Verifying Real-Time Systems against Scenario-Based Requirements. **2009**, 676-691 1

632 A Formal Methodology for Semantics and Time Consistency Checking of UML Dynamic Diagrams. **2009**, 78-85

631 Automata-Theoretic Verification based on Counterexample Specifications. **2009**, 217-225

630 Modeling and Analysis of Thread-Pools in an Industrial Communication Platform. **2009**, 367-386 4

629 On the Supports of Recognizable Timed Series. **2009**, 243-257

628 Model-Based Analysis of Contract-Based Real-Time Scheduling. **2009**, 227-239 1

627 On the Complexity of Synthesizing Relaxed and Graceful Bounded-Time 2-Phase Recovery. **2009**, 660-675 5

626 Compositional verification of fault-tolerant real-time programs. **2009**, 5

625 Model Checking Logic WCTL with Multi Constrained Modalities on One Clock Priced Timed Automata. **2009**, 88-102

624 Removing All Silent Transitions from Timed Automata. **2009**, 118-132 1

623 Volume and Entropy of Regular Timed Languages: Analytic Approach. **2009**, 13-27 12

622 Decomposition and Coordination Method for Flowshop Scheduling Problems Represented by Timed Automata. **2009**, 45, 369-378

621 SystemCtlm FL: the Successor of SystemCFL. **2009**, 434-440 0

620 Event-Clock Visibly Pushdown Automata. **2009**, 558-569 7

619 Towards Parametric Verification of Prioritized Time Petri Nets. **2009**, 19-25

618 Property Driven Three-Valued Model Checking on Hybrid Automata. **2009**, 218-229 1

617	Formal Verification for Embedded Systems Design Based on MDE. **2009**, 159-170	3
616	Message Scheduling within Vehicular Networks. **2009**, 221-246	
615	Ensuring Sound Numerical Simulation of Hybrid Automata. **2009**, 3, 73-87	1
614	Schedulability verification of AADL model based on UPPAAL. **2009**, 29, 1820-1824	1
613	An Algebraic Approach for TAL-Freeness Detection. **2009**, 31, 516-521	
612	Verifying Real-Time Systems using Explicit-time Description Methods. 13, 67-78	4
611	Hardness of Preorder Checking for Basic Formalisms. **2010**, 119-135	1
610	Forgetting the Time in Timed Process Algebra. **2010**, 110-124	1
609	Intelligent Control Theory and Technologies. **2010**, 214-244	
608	On Process-Algebraic Extensions of Metric Temporal Logic. **2010**, 283-300	1
607	Solution-Oriented Requirements. **2010**, 211-287	
606	A Framework for Relating Timed Transition Systems and Preserving TCTL Model Checking. **2010**, 83-98	4
605	Making the Right Cut in Model Checking Data-Intensive Timed Systems. **2010**, 565-580	1
604	Compositional and Quantitative Model Checking (Extended Abstract). **2010**, 35-42	
603	Nash Equilibria for Reachability Objectives in Multi-player Timed Games. **2010**, 192-206	11
602	Introduction. **2010**, 1-29	
601	Modeling and Reasoning about Service Behaviors and Their Compositions. **2010**, 82-96	4
600	On the Automated Implementation of Time-Based Paxos Using the IOA Compiler. **2010**, 235-252	1

599 Timed Automata Semantics for Analyzing Creol. 30, 108-122 5

598 An Individual-based Probabilistic Model for Fish Stock Simulation. 33, 37-55 3

597 Sampled Semantics of Timed Automata. **2010**, 6, 3

596 Diagnose de falhas em sistemas a eventos discretos modelados por aut matos finitos. **2010**, 21, 510-533

595 Protecting Critical Infrastructures While Preserving Each Organization Autonomy. **2011**, 15-34

594 Behavior Specification of a Red-Light Violation Warning Application An Approach for Specifying Reactive Vehicle-2-X Communication Applications. **2011**, 106-118

593 Compositional timing analysis. **2011**,

592 Research on Modeling and Analysis of CPS. **2011**, 92-105 4

591 Fully Symbolic Model Checking for Timed Automata. **2011**, 616-632 10

590 An Automated Semantic-Based Approach for Creating Tasks from Matlab Simulink Models. **2011**, 149-164 4

589 Behavioral Subtyping Relations for Timed Components. **2011**, 26-35

588 Snapshot Reachability Graphs for Alvis Models. **2011**, 190-199

587 Probabilistic Real-Time Rewrite Theories and Their Expressive Power. **2011**, 60-79 3

586 A Framework for Specification and Verification of Timeout Models of Real-Time Systems. **2011**, 146-157

585 Formal Verification of DEV&DESS Formalism Using Symbolic Model Checker HyTech. **2011**, 112-121 1

584 Observing Continuous-Time MDPs by 1-Clock Timed Automata. **2011**, 2-25 7

583 Performance Evaluation of Schedulers in a Probabilistic Setting. **2011**, 1-17 3

582 Segmented State Space Traversal for Conformance Testing of Cyber-Physical Systems. **2011**, 193-208 3

581	Multi-Agent Reconfigurable Embedded Systems. **2011**, 1-30	
580	A Formal Framework for Testing duration Systems. **2011**, 155-168	
579	Semantics of DFCharts. **2011**, 61-92	
578	Timed Specification Patterns for System Validation: A Railway Case Study. **2011**, 121-134	1
577	Access Nets: Modeling Access to Physical Spaces. **2011**, 184-198	5
576	Emptiness and Universality Problems in Timed Automata with Positive Frequency. **2011**, 246-257	1
575	Distributed Event Clock Automata. **2011**, 250-263	3
574	A Game-Theoretic approach to Fault Diagnosis of Hybrid Systems. 54, 237-249	
573	Deciding Reachability for 3-Dimensional Multi-Linear Systems. 54, 250-262	
572	When Parameterized Model Driven Development Supports Aspect Based SOA. **2011**, 7, 44-62	1
571	Two-Player Reachability-Price Games on Single-Clock Timed Automata. 57, 31-46	4
570	On the reaction time of some synchronous systems. 59, 69-83	
569	A Probabilistic Temporal Logic with Frequency Operators and Its Model Checking. 73, 79-93	6
568	An Analyzing Framework for the Requirements of Automotive Software. **2012**, 1319-1326	
567	Checking Correctness of Services Modeled as Priced Timed Automata. **2012**, 308-322	3
566	Model Checking Inconsistency Recovery Costs. **2012**, 195-200	0
565	Controllers with Minimal Observation Power (Application to Timed Systems). **2012**, 223-237	
564	Dual-Language Approaches. **2012**, 365-407	

563 Synchronous Abstract Machines. **2012**, 153-207

562 Frequencies in Forgetful Timed Automata. **2012**, 236-251 2

561 On the Relationship between Reachability Problems in Timed and Counter Automata. **2012**, 54-65 5

560 Reducing Timed Automata: A New Approach.

559 Nash Equilibrium in Weighted Concurrent Timed Games with Reachability Objectives. **2012**, 117-128

558 Heuristics for Probabilistic Timed Automata with Abstraction Refinement. **2012**, 151-165

557 Playing Optimally on Timed Automata with Random Delays. **2012**, 43-58 3

556 Robust Reachability in Timed Automata: A Game-Based Approach. **2012**, 128-140 9

555 On Decidability of Prebisimulation for Timed Automata. **2012**, 444-461 6

554 Timed Conformance Testing for Orchestrated Service Discovery. **2012**, 133-150 2

553 Bibliography. **2012**, 793-823

552 Prototype Design of Hierarchical Cluster Multiprocessor on Platform FPGA. **2012**, 687-692

551 Measuring Information in Timed Languages. **2012**, 1-2

550 Study and Simulation of Scheduling Strategies on Vehicle Operating Safety State Monitoring System. **2012**, 117-123

549 The Minimal Cost Reachability Problem in Priced Timed Pushdown Systems. **2012**, 58-69 8

548 Avoiding Shared Clocks in Networks of Timed Automata. **2012**, 100-114 4

547 Modeling Quality Aspects: Real-Time. **2012**, 119-128 1

546 Timed Transition Systems with Independence and Marked Scott Domains: An Adjunction. **2012**, 86-94

545	mctau: Bridging the Gap between Modest and UPPAAL. **2012**, 227-233	6
544	Symbolic Model-Checking of Stateful Timed CSP Using BDD and Digitization. **2012**, 398-413	3
543	Robustness of Time Petri Nets under Architectural Constraints. **2012**, 11-26	2
542	Combining Static and Dynamic Models for Boosting Forward Planning. **2012**, 322-338	
541	Counter Automata for Parameterised Timing Analysis of Box-Based Systems. **2012**, 126-141	
540	Symbolically Bounding the Drift in Time-Constrained MSC Graphs. **2012**, 1-15	
539	The Complexity of Bounded Synthesis for Timed Control with Partial Observability. **2012**, 204-219	
538	Formalized Approach for the Design of Real-Time Distributed Computer Systems. **2012**, 35-62	
537	Conformance Verification of Normative Specifications using C-O Diagrams. 94, 1-10	1
536	Automatic Synthesis and Verification of Real-Time Embedded Software for Mobile and Ubiquitous Systems. **2012**, 395-416	
535	A Formal Model For Real-Time Parallel Computation. 105, 39-55	3
534	Adding Time to Pushdown Automata. 103, 1-16	
533	Unifying Operational Semantics with Algebraic Semantics for Instantaneous Reactions. **2013**, 186-203	
532	Execution Information Rate for Some Classes of Automata. **2013**, 226-237	2
531	A Faster Algorithm for Solving One-Clock Priced Timed Games. **2013**, 531-545	3
530	A Maximal Entropy Stochastic Process for a Timed Automaton,. **2013**, 61-73	3
529	Spectral Gap in Timed Automata. **2013**, 16-30	0
528	An Overtime-Detection Model-Checking Technique for Interrupt Processing Systems. **2013**, 482-489	

527 Verifying Temporal Properties in Real Models. **2013**, 309-323 1

526 On Generating Soft Real-Time Programs for Non-Real-Time Environments. **2013**, 1-12 1

525 Automated Addition of Fault-Tolerance under Synchronous Semantics. **2013**, 266-280 1

524 On characteristic formulae for Event-Recording Automata. **2013**, 47, 69-96

523 Improving Model Checking Stateful Timed CSP with non-Zenoness through Clock-Symmetry Reduction. **2013**, 182-198 3

522 Pruning Search Spaces of RATA Model for the Job-Shop Scheduling. **2013**, 387-401

521 Zone-Based Synthesis of Strict 2-Phase Fault Recovery. **2013**, 357-359

520 Exploiting Functional Models to Assess the Security Aspect in Embedded System Design. **2013**, 90-97 1

519 A Modular Approach for Reusing Formalisms in Verification Tools of Concurrent Systems. **2013**, 199-214 1

518 Boundness Issues in CCSL Specifications. **2013**, 20-35 5

517 Contract-Based Compositional Scheduling Analysis for Evolving Systems. **2013**, 272-282

516 Improving the Efficiency of Synchronized Product with Infinite Transition Systems. **2013**, 285-307

515 Shrinktech: A Tool for the Robustness Analysis of Timed Automata. **2013**, 1006-1012 2

514 Model Checking Metric Temporal Logic over Automata with One Counter. **2013**, 468-479 3

513 Synthesis of Bounded Integer Parameters for Parametric Timed Reachability Games. **2013**, 87-101 3

512 Generalized Quantitative Analysis of Metric Transition Systems. **2013**, 192-208 3

511 Bibliography. 137-148

510 Timed Model-Checking. 39-66

509	Control of Timed Systems. 67-105	
508	Fault Diagnosis of Timed Systems. 107-138	0
507	Models for Real-Time Embedded Systems. 1-37	
506	Verification of Timed Systems. 271-306	
505	Runtime Verification Based on Executable Models: On-the-Fly Matching of Timed Traces. 111, 67-81	5
504	Verification of Infinite-State Systems. 221-269	
503	Modeling Time. 63-96	
502	Model Integration for Formal Qualification of Timing-Aware Software Data Acquisition Components. 167-200	
501	Model-Based Design Using Simulink - Modeling, Code Generation, Verification, and Validation. 159-181	2
500	Modal Specifications for Probabilistic Timed Systems. 117, 66-80	2
499	Introduction: Modeling, Analysis and Synthesis of Embedded Software and Systems. **2014**, 1-16	
498	A Unifying Approach to Decide Relations for Timed Automata and their Game Characterization. 120, 47-62	1
497	Completeness of Lyapunov Abstraction. 124, 26-42	0
496	Falsifying Oscillation Properties of Parametric Biological Models. 125, 53-67	5
495	References. 395-399	
494	Model-Driven Approaches to Service Composition. **2013**, 4, 18-35	
493	Towards a Safe Compositional Real-Time Scheduling Theory for Cyber-Physical Systems. **2013**,	
492	A Clock Based Approach to the Formal Specification of Cyber Physical Systems. **2014**, 401-409	

491 A Clock-Based Specification of Cyber-Physical Systems. **2014**, 1357-1363

490 Timed Automata with Action Durations From Theory to Implementation. **2014**, 94-109 3

489 Distributed Energy Management Case Study: A Formal Approach to Analyzing Utility Functions. **2014**, 74-87 3

488 The Power of Proofs: New Algorithms for Timed Automata Model Checking. **2014**, 115-129 1

487 A Framework for Verification of SystemC Designs Using SystemC Waiting State Automata. **2014**, 77-104

486 Transformations for Compositional Verification of Assumption-Commitment Properties. **2014**, 216-229

485 Combined Bounded and Symbolic Model Checking for Incomplete Timed Systems. **2014**, 30-47

484 Verification for Timed Automata Extended with Unbounded Discrete Data Structures. **2014**, 512-526

483 Inference of Markov Chain Models by Using k-Testable Language: Application on Aging People. **2014**, 89-106

482 Probabilistic Robust Timed Games. **2014**, 203-217 3

481 Analyzing Oscillatory Behavior with Formal Methods. **2014**, 1-25 1

480 On the Expressiveness of Metric Temporal Logic over Bounded Timed Words. **2014**, 138-150 2

479 Towards Formal Modelling and Verification of Pervasive Computing Systems. **2014**, 62-91

478 Methods for the Design and Development. **2014**, 183-350

477 Lossiness of Communication Channels Modeled by Transducers. **2014**, 224-233 2

476 Information Rate of Some Classes of Non-regular Languages: An Automata-Theoretic Approach. **2014**, 232-243 3

475 Adding Negative Prices to Priced Timed Games. **2014**, 560-575 2

474 A Logical Encoding of Timed $$pi $$ -Calculus. **2014**, 164-182

473	Communication Security Prognosis Realized as the Parallel Dynamic Auditing Intelligent System. **2014**, 139-149	
472	Towards a DSL-Based Approach for Specifying and Monitoring Home Care Plans. **2014**, 342-354	
471	Time-Bounded Reachability for Initialized Hybrid Automata with Linear Differential Inclusions and Rectangular Constraints. **2014**, 191-205	
470	Weak Singular Hybrid Automata. **2014**, 161-175	
469	Interoperability of Security-Aware Web Service Business Processes: Case Studies and Empirical Evaluation. **2014**, 285-299	
468	Interval Abstraction Refinement for Model Checking of Timed-Arc Petri Nets. **2014**, 237-251	2
467	Safety Verification of Multiple Autonomous Systems by Formal Approach. **2014**, 11-18	
466	Well-Structured Pushdown System: Case of Dense Timed Pushdown Automata. **2014**, 336-352	4
465	Zenoness for Timed Pushdown Automata. 140, 35-47	
464	Predicting logical and temporal properties of real-time systems using Synchronized Elementary Nets. **1994**, 211-227	
463	Automatic synthesis of real time systems. **1995**, 535-546	1
462	Symbolic timing devices. **1995**, 504-517	
461	A calculus for timed automata. **1996**, 110-129	8
460	Timed systems behaviour and conformance testing a mathematical framework. **1996**, 89-104	
459	Specification of Stochastic Properties in Real-Time Systems. **1996**, 202-216	1
458	Fast asynchronous systems in dense time. **1996**, 75-86	4
457	Regular processes and timed automata. **1997**, 141-155	
456	Formal specification and verification method of concurrent and distributed systems by restricted timed automata. **1997**, 169-183	

455 Verification of clocked and hybrid systems. **1998**, 4-73 2

454 Abstract games for infinite state processes. **1998**, 147-162 1

453 Extracting Stable Timed Automata from Switched Model Reference Systems. **1999**, 204-217

452 From Timed Scenarios to SDL: Specification, Implemention and Testing of Real-Time Systems**This work was supported by NSERC Operating Grant (Dssouli) and IGLOO-NSERC Project.. **1999**, 67-80 1

451 Equivalences of discrete-event systems and of hybrid systems. **1999**, 251-257

450 Compilation and Synthesis for Real-Time Embedded Controllers. **1999**, 256-287

449 Is Your Model Checker on Time?. **1999**, 125-136 7

448 Flexibilit durch kombinierte Design Pattern. **1999**, 364-374

447 Correctness Proof of the Standardized Algorithm for ABR Conformance. **1999**, 662-681 3

446 Polyhedral Flows in Hybrid Automata. **1999**, 5-18 3

445 Timed Alternating Tree Automata: The Automata-Theoretic Solution to the TCTL Model Checking Problem. **1999**, 281-290 11

444 Formalizing Timed BPEL by D-LOTOS. **2014**, 5, 1-21 1

443 True Concurrency Semantics. **2014**, 5, 22-42 1

442 On the Expressiveness of TPTL and MTL over Data Words. 151, 174-187 1

441 Formal and Informal Methods for Multi-Core Design Space Exploration. 154, 78-92

440 Symbolic Minimum Expected Time Controller Synthesis for Probabilistic Timed Automata. **2015**, 140-155 4

439 Interpretation of DD-LOTOS Specification by C-DATA*. **2015**, 414-423

438 Deciding Concurrent Planar Monotonic Linear Hybrid Systems. **2015**, 256-269

437	Checking Integral Real-Time Automata for Extended Linear Duration Invariants. **2015**, 62-75	
436	A Logical Characterization of Timed Pushdown Languages. **2015**, 189-203	4
435	Relative Timed Model for Coordinated Multi Agent Systems. **2015**, 15-27	
434	Formal Verification of Service Level Agreements Through Distributed Monitoring. **2015**, 125-140	3
433	Fluid Model Checking of Timed Properties. **2015**, 172-188	3
432	An Over-Approximation Forward Analysis for Nested Timed Automata. **2015**, 62-80	1
431	The Timed Decentralised Label Model. **2015**, 27-43	1
430	A Virtual Workbench Applied to Automation: Student Response Analysis. **2015**, 709-719	1
429	Security of Numerical Sensors in Automata. **2015**, 76-88	1
428	A Design Method of the General Aviation Flight Plan Management Scheme Based on Quantitative Verification. **2015**, 453-464	
427	Logics for Weighted Timed Pushdown Automata. **2015**, 153-173	2
426	Automated Conflict-Free Concurrent Implementation of Timed Component-Based Models. **2015**, 359-374	1
425	Analysis of Timing Constraints in Heterogeneous Middleware Interactions. **2015**, 36-52	3
424	A Timed Semantics of Workflows. **2015**, 365-383	1
423	Quantifying Communication in Synchronized Languages. **2015**, 635-647	1
422	Towards a Family of Test Selection Criteria for Symbolic Models of Real-Time Systems. **2015**, 48-63	1
421	Synchronized Recursive Timed Automata. **2015**, 249-265	
420	Testen von Echtzeiteigenschaften f verteilte Ablaufsteuerungen. **2015**, 1-10	

419	Timed Aggregate Graph: A Finite Graph Preserving Event- and State-Based Quantitative Properties of Time Petri Nets. **2015**, 34-54	1
418	Indefinite waitings in MIRELA systems. 184, 5-18	2
417	Expected-Delay-Summing Weak Bisimilarity for Markov Automata. 194, 1-15	5
416	Modelling and Analysing Mixed Reality Applications. **2016**, 3-17	1
415	Applying RoBuSt Method for Robustness Testing of the Non-interference Property. **2016**, 171-188	
414	- Foundations of Compositional Model-Based System Designs. **2015**, 108-133	
413	Towards Satisfaction Checking of Power Contracts in Uppaal. **2016**, 157-179	
412	Modeling and Verification of an Interrupt System in (mu)C/OS-III with TMSVL. **2016**, 15-28	
411	Designing Schedulers for Hard Real-Time Tasks. **2016**, 1125-1134	
410	On Reachability Analysis of Updatable Timed Automata with One Updatable Clock. **2016**, 147-161	1
409	Weighted Register Automata and Weighted Logic on Data Words. **2016**, 370-384	3
408	Scaling BDD-based Timed Verification with Simulation Reduction. **2016**, 363-382	
407	Semiformal Assertion-Based Verification of Hardware/Software Systems in a Model-Driven Design Framework. **2016**, 1-38	
406	Local Planning of Multiparty Interactions with Bounded Horizons. **2016**, 199-216	1
405	Searching for Cost-Optimized Strategies: An Agricultural Application. **2016**, 31-43	
404	A Logical Characterization for Dense-Time Visibly Pushdown Automata. **2016**, 89-101	6
403	Verification of AUTOSAR Software Architectures with Timed Automata. **2016**, 189-204	2
402	On Expressiveness of TCTL(^{varDelta }_{h}) for Model Checking Distributed Systems. **2016**, 323-332	3

401	Abstracting an Operational Semantics to Finite Automata. **2016**, 109-123	
400	Conformance Checking of Real-Time Models. **2016**, 15-32	1
399	Distributed Algorithms for Time Optimal Reachability Analysis. **2016**, 157-173	1
398	Bibliography. **2016**, 247-271	
397	Time optimal reachability analysis using swarm verification. **2016**,	3
396	Analysis and Implementation of Embedded System Models: Example of Tags in Item Management Application. **2017**, 175-199	2
395	On Nonlinear Prices in Timed Automata. 232, 65-78	4
394	Building Faithful Embedded Systems Models: Challenges and Opportunities. **2017**, 1-24	
393	On Termination and Boundedness of Nested Updatable Timed Automata. **2017**, 15-31	
392	Accuracy of Message Counting Abstraction in Fault-Tolerant Distributed Algorithms. **2017**, 347-366	2
391	Nested Timed Automata with Diagonal Constraints. **2017**, 396-412	
390	Discretization of Continuous Dynamical Systems Using UPPAAL. **2017**, 297-315	2
389	Improving the Timed Automata Approach to Biological Pathway Dynamics. **2017**, 96-111	
388	Symbolic Verification and Strategy Synthesis for Linearly-Priced Probabilistic Timed Automata. **2017**, 289-309	2
387	A Unified Formalism for Monoprocessor Schedulability Analysis Under Uncertainty. **2017**, 100-115	2
386	Trace Relations and Logical Preservation for Continuous-Time Markov Decision Processes. **2017**, 192-209	3
385	From Scenarios to Timed Automata. **2017**, 33-51	5
384	The Reach-Avoid Problem for Constant-Rate Multi-mode Systems. **2017**, 463-479	1

383 Admissible Strategies in Timed Games. **2017**, 403-425 1

382 Extended Automata for Temporal Planning of Interacting Agents. **2017**, 5, 30-48

381 Nested Timed Automata with Invariants. **2017**, 77-93

380 TT-BIP: Using Correct-by-Design BIP Approach for Modelling Real-Time System with Time-Triggered Paradigm. **2017**, 171-188

379 Probabilistic Timed Automata with Clock-Dependent Probabilities. **2017**, 144-159 3

378 Shared-Variable Concurrency, Continuous Behaviour and Healthiness for Critical Cyberphysical Systems. **2017**, 109-125

377 Exposing Latent Mutual Exclusion by Work Automata. **2017**, 59-73 1

376 Optimal Reachability in Divergent Weighted Timed Games. **2017**, 162-178 1

375 Multi-timed Bisimulation for Distributed Timed Automata. **2017**, 52-67 2

374 Information Flow for Timed Automata. **2017**, 3-21 4

373 Optimal Control for Multi-mode Systems with Discrete Costs. **2017**, 77-96 1

372 Formal Specification and Verification of System of Systems Using UPPAAL: A Case Study of a Defensive Missile Systems. **2017**, 482-488 1

371 Adding Dense-Timed Stack to Integer Reset Timed Automata. **2017**, 9-25

370 Analysis of Timed Properties Using the Jump-Diffusion Approximation. **2017**, 69-84

369 On Global Scheduling Independency in Networks of Timed Automata. **2017**, 42-57 1

368 Logical Characterisation of Parameterised Bisimulations. **2017**, 51-69 1

367 Introduction to Hardware/Software Codesign. **2017**, 1-24

366 From Lotosphere to Thermosphere. **2017**, 357-367

365	Construction of Test Cases for Electronic Controllers Based on Timed Automata. **2017**, 123-133	
364	Compositional Testing of Real-Time Systems. **2017**, 107-124	1
363	A Formal Framework to Specify and Test Systems with Fuzzy-Time Information. **2017**, 403-414	3
362	Incremental Inductive Verification of Parameterized Timed Systems. **2017**, 16, 1-24	1
361	References. **2017**, 195-212	
360	Synthesis of ada code from graph-based task models. **2017**,	4
359	Robust Model Checking of Timed Automata under Clock Drifts. **2017**,	2
358	Preserving Partial-Order Runs in Parametric Time Petri Nets. **2017**, 16, 1-26	1
357	Driving time-dependent paths in clinical BPMN processes. **2017**,	1
356	Graphic Modelling Approach as a Support for Event-B Modelling. **2018**, 647-657	
355	Monitoring of Traffic Manoeuvres with Imprecise Information. 257, 43-58	0
354	Modelling and Analysis of Distributed Asynchronous Actor Systems Using Theatre. **2018**, 150-162	
353	Dimensioning of Civilian Avionics Networks. **2017**, 49-1-49-21	
352	Efficient Symbolic Representation of Convex Polyhedra in High-Dimensional Spaces. **2018**, 284-299	
351	Monitoring CTMCs by Multi-clock Timed Automata. **2018**, 507-526	3
350	The Compound Interest in Relaxing Punctuality. **2018**, 147-164	2
349	Trace Relations and Logical Preservation for Markov Automata. **2018**, 162-178	4
348	Timed Vacuity. **2018**, 438-455	1

347 Model-Based Analysis of Temporal Properties. **2018**, 208-223 1

346 COSRDL: An Event-Driven Control-Oriented System Requirement Modeling Method. **2018**, 103-117 1

345 A Cooperative Coevolutionary Algorithm for Real-Time Underground Mine Scheduling. **2018**, 410-418

344 Finite State Machines and Timed Automata: A Hierarchical Approach for Integrated Traffic Microsimulations. **2018**, 25-36

343 Algebraic Derivation of Until Rules and Application to Timer Verification. **2018**, 244-262

342 Refinement of Timing Constraints for Concurrent Tasks with Scheduling. **2018**, 219-233 2

341 Timed Scenarios: Consistency, Equivalence and Optimization. **2018**, 215-233 3

340 Non-bisimulation Based Behavioral Relations for Markov Automata. **2018**, 179-196 1

339 Adaptive Formal Framework for WMN Routing Protocols. **2018**, 175-195 1

338 MDP + TA = PTA: Probabilistic Timed Automata, Formalized (Short Paper). **2018**, 597-603 0

337 (TCTL^{varDelta }_h) Model Checking of Time Petri Nets. **2018**, 242-262 1

336 Verifying Probabilistic Timed Automata Against Omega-Regular Dense-Time Properties. **2018**, 122-139 1

335 On the Fly Model-Checking of TPN: (TPN-TCTL^{varDelta }_{h}). **2018**, 441-451

334 Quantitative Model Checking for a Controller Design. **2018**, 292-307

333 A Comparative Study of Decision Diagrams for Real-Time Model Checking. **2018**, 216-234

332 Timing Contracts for Multi-Core Embedded Control Systems. **2018**, 97-118

331 Event-Clock Nested Automata. **2018**, 80-92 2

330 Traditional Software Reliability Models Are Based on Brute Force and Reject Hoare Rule. **2019**, 295-304

329	Human in the Loop Least Violating Robot Control Synthesis under Metric Interval Temporal Logic Specifications. **2018**,	3
328	Complexity of Timeline-Based Planning over Dense Temporal Domains: Exploring the Middle Ground. 277, 191-205	
327	Temporal Logic and Model Checking for Operator Precedence Languages. 277, 161-175	1
326	Multi-Viewpoint State Machines for Rich ComponentModels. **2018**, 511-542	
325	On Urgency in Asynchronous Timed Session Types. 279, 85-94	1
324	Methods and Tools for Mapping Process Networks onto Multi-Processor Systems-On-Chip. **2019**, 685-719	4
323	Automated Scenario-Based Evaluation of Embedded Software and System Architectures. **2019**, 452-469	
322	Symbolic Simulation of Dataflow Synchronous Programs with Timers. **2019**, 45-70	
321	Software Engineering of Real-Time Systems. **2019**, 227-249	
320	The Linear Time-Branching Time Spectrum of Equivalences for Stochastic Systems with Non-determinism. **2019**, 41-58	
319	Robust Controller Synthesis in Timed B□hi Automata: A Symbolic Approach. **2019**, 572-590	1
318	Symbolic Monitoring Against Specifications Parametric in Time and Data. **2019**, 520-539	4
317	Inferring the Synaptical Weights of Leaky Integrate and Fire Asynchronous Neural Networks: Modelled as Timed Automata. **2019**, 149-166	
316	On the Distance Between Timed Automata. **2019**, 199-215	1
315	Continuous-Time Models for System Design and Analysis. **2019**, 452-477	4
314	On Board Autonomy Operations for OPS-SAT Experiment. **2019**, 182-195	1
313	Timed Automata for Video Games and Interaction. **2019**, 1-9	
312	Verification of Multi-agent Systems with Timeouts for Migration and Communication. **2019**, 134-151	

311 Optimal Run Problem for Weighted Register Automata. **2019**, 91-110

310 Multiple Mutation Testing for Timed Finite State Machine with Timed Guards and Timeouts. **2019**, 104-120 0

309 A State Class Construction for Computing the Intersection of Time Petri Nets Languages. **2019**, 79-95 3

308 Expected Reachability-Price Games. **2019**, 282-300

307 Alternating Automata Modulo First Order Theories. **2019**, 43-63

306 Using Models for Communication in Cyber-Physical Systems. **2019**, 55-81

305 On Reconciling Schedulability Analysis and Model Checking in Robotics. **2019**, 32-48 2

304 (textsf {ML}_{nu }): A Distributed Real-Time Modal Logic. **2019**, 19-35

303 The Timestamp of Timed Automata. **2019**, 181-198 0

302 Computing Bisimilarity Metrics for Probabilistic Timed Automata. **2019**, 303-321

301 Munta: A Verified Model Checker for Timed Automata. **2019**, 236-243 2

300 On Timed Scope-Bounded Context-Sensitive Languages. **2019**, 168-181

299 Predicting and Testing Latencies with Deep Learning: An IoT Case Study. **2019**, 93-111

298 A Testing Perspective on Algebraic, Denotational, and Operational Semantics. **2019**, 22-38

297 Automata-Based Behavioural Contracts with Action Correlation. **2019**, 131-151

296 Introduction. **2020**, 1-16

295 Conclusions and Further Work. **2020**, 219-226

294 Simulating Timing Behaviors for Cyber-Physical Systems Using Modelica. **2019**, 11, 44-67

293	A timeless model for the verification of quasi-periodic distributed systems. **2019**,	
292	Encyclopedia of Systems and Control. **2020**, 1-12	
291	Privacy Verification and Enforcement via Belief Manipulation. **2020**, 83-101	3
290	Time in SCCharts. **2020**, 1-25	
289	Teaching Formal Methods: An Experience Report. **2020**, 3-18	1
288	Synthesis of P-Stable Abstractions. **2020**, 214-230	2
287	Encyclopedia of Systems and Control. **2020**, 1-20	
286	Application I: Verification. **2020**, 77-103	
285	Safe Decomposition of Startup Requirements: Verification and Synthesis. **2020**, 155-172	
284	Improving Secure and Robust Patient Service Delivery. **2020**, 404-418	2
283	Two-Level Data Collection for an Energy-Efficient Solution in Wireless Sensor Networks. **2020**, 194-213	
282	Synthesizing Strategies for Needle Steering in Gelatin Phantoms. 316, 261-274	3
281	Model Checking MITL Formulae on Timed Automata. **2020**, 21, 1-44	1
280	Exact acceleration of complex real-time model checking based on overlapping cycle. **2020**, 6, e272	1
279	Timed Automaton and Petri Net models of Intersection Control. **2020**,	0
278	Firmness Analysis of Real-time Tasks. **2020**, 19, 1-24	1
277	Reliable and Secure Design-Space-Exploration for Cyber-Physical Systems. **2020**, 19, 1-29	0
276	Probabilistic and Timed Analysis of Security Protocols. **2021**, 142-151	0

275 Exploiting Dataflow Models for Parallel Simulation of Discrete Timed Systems. **2020**,

274 Weak Cost Register Automata are Still Powerful. **2020**, 31, 689-709 0

273 Decisiveness of Stochastic Systems and its Application to Hybrid Models. 326, 149-165 1

272 A Quest of Self-Explainability: When Causal Diagrams meet Autonomous Urban Traffic Manoeuvres. **2021**, 2

271 Graphic modeling in Distributed Autonomous and Asynchronous Automata (DA3). 1

270 Encyclopedia of Systems and Control. **2020**, 1-8

269 Co-simulation and Verification of a Non-linear Control System for Cogging Torque Reduction in Brushless Motors. **2020**, 3-19 0

268 AutoNet: Meta-model for Seamless Integration of Timed Automata and Colored Petri Nets. **2020**, 307-319 0

267 Finite Interval-Time Transition System for Real-Time Actors. **2020**, 85-100

266 Context-free timed formalisms: Robust automata and linear temporal logics. **2020**, 104673 0

265 Single-Agent Indirect Herding of Multiple Targets using Metric Temporal Logic Switching. **2020**,

264 Formal Modeling and Verification of Rate Adaptive Pacemakers for Heart Failure. **2020**, 0

263 Nonblocking Supervisory Control Synthesis of Timed Automata using Abstractions and Forcible Events. **2020**,

262 Configurable verification of timed automata with discrete variables. 1 1

261 Formal Verification of CAN Bus in Cyber Physical System. **2020**, 0

260 Real-Time Trigger-Response Properties for Event-B Applied to the Pacemaker. **2020**,

259 The Learning Algorithm of Real-Time Automata. **2020**,

258 On the Modeling of Sequential Reactive Systems by Means of Real Time Automata. **2020**, 27, 396-411

257	Dependence Analysis and Automated Partitioning for Scalable Formal Analysis of SystemC Designs. **2020**,	1
256	Model-Based Design of Resilient Systems Using Quantitative Risk Assessment. **2020**, 149-164	
255	PAC Learning of Deterministic One-Clock Timed Automata. **2020**, 129-146	
254	Uncertainty Theories for Real-Time Systems. **2020**, 1-34	0
253	Computing Maximally-Permissive Strategies in Acyclic Timed Automata. **2020**, 111-126	1
252	Formal Verification of Human-Robot Interaction in Healthcare Scenarios. **2020**, 303-324	4
251	Mutation-Based Test Case Generation of Train-Ground Transmission Function for CTCS. **2020**, 133-141	
250	Falsification of Cyber-Physical Systems with Constrained Signal Spaces. **2020**, 420-439	1
249	Methods for Modeling Urban Road Traffic Using Timed Automata. **2020**, 97-107	0
248	On Subgame Perfect Equilibria in Turn-Based Reachability Timed Games. **2020**, 94-110	
247	Priced Timed Automata Models for Control of Intelligent Product Agents in Manufacturing Systems. **2020**, 53, 136-142	2
246	Backward Symbolic Optimal Reachability in Weighted Timed Automata. **2020**, 41-57	
245	On the Semantics of Polychronous Polytimed Specifications. **2020**, 23-40	
244	Encyclopedia of Systems and Control. **2020**, 1-20	
243	Behavioral Specification Theories: An Algebraic Taxonomy. **2020**, 262-274	1
242	Fluid Model-Checking in UPPAAL for Covid-19. **2020**, 385-403	4
241	Synthesizing Clock-Efficient Timed Automata. **2020**, 276-294	2
240	Mixed-Initiative Control Synthesis: Estimating an Unknown Task Based on Human Control Input. **2020**, 53, 119-125	1

239 Non-blocking Supervisory Control of Timed Automata using Forcible Events. **2020**, 53, 356-362 0

238 Spatio-Temporal Model-Checking of Cyber-Physical Systems Using Graph Queries. **2020**, 59-79

237 An Approach to Modeling and Verifying Multi-level Interrupt Systems with TMSVL. **2020**, 61-72

236 From Statistical Model Checking to Run-Time Monitoring Using a Bayesian Network Approach. **2020**, 517-535 1

235 Towards A Test Paths Generation Method for CTCS Level Transition. **2020**, 325, 01001

234 Model-Based Testing of Post-WIMP Interactions Using Object Oriented Petri-Nets. **2020**, 486-502 1

233 Compositional Liveness-Preserving Conformance Testing of Timed I/O Automata. **2020**, 147-169

232 Improving and Optimizing Verification and Testing Techniques for Distributed Information Systems. **2020**, 457-472

231 Formal Verification of an Industrial Distributed Algorithm: An Experience Report. **2020**, 525-542 1

230 Learning Specifications for Labelled Patterns. **2020**, 76-93

229 Who Carries the Burden of Modularity?. **2020**, 3-21 1

228 Statistical Model Checking in Drug Repurposing for Alzheimer's Disease. **2020**, 64-80

227 Control of Real-time Systems with Integer Parameters. **2020**, 1-1

226 Control Synthesis for Cyber-Physical Systems to Satisfy Metric Interval Temporal Logic Objectives under Timing and Actuator Attacks. **2020**, 2

225 Transforming YAWL Workflows with Time Constraints into Timed Automata. **2021**, 0

224 Timed model checking of fault-tolerant nuclear I&C systems. **2020**,

223 Discrete-continuous dynamic simulation of plantwide batch process systems in MATLAB. **2020**, 159, 66-77

222 When Parameterized Model Driven Development Supports Aspect Based SOA. 211-230

221	A Service-Based Approach to Connect Context-Aware Platforms and Adaptable Android for Mobile Users. 302-332	
220	An Integrated Framework for Web Services Orchestration. 306-335	0
219	Multi-agent Planning for Autonomous Agents Coordination. **2005**, 53-68	1
218	NuSEE: Nuclear Software Engineering Environment. **2009**, 121-135	
217	Scheduling for Reliable Execution in Autonomic Systems. **2008**, 149-161	3
216	Updatable Timed Automata with Additive and Diagonal Constraints. **2008**, 407-416	
215	Placement Inference for a Client-Server Calculus. **2008**, 75-86	1
214	Generating Sound and Resource-Aware Code from Hybrid Systems Models. **2006**, 48-66	3
213	Towards Integrated Model-Driven Verification and Empirical Validation of Reusable Software Frameworks for Automotive Systems. **2006**, 118-132	
212	Sampled Universality of Timed Automata. **2007**, 2-16	2
211	Hybrid Control and Verification of a Pulsed Welding Process. **2007**, 357-370	
210	Theory and Practice of Probabilistic Timed Game for Embedded Systems. **2007**, 109-120	
209	Computing Minimal Elements of Upward-Closed Sets for Petri Nets. **2007**, 465-483	1
208	SC2SCFL: Automated SystemC to $SystemC^{mathbb{FL}}$ Translation. **2007**, 34-45	5
207	A Categorical Observation of Timed Testing Equivalence. **2007**, 35-46	
206	Translatable Finite State Time Machine. **2007**, 117-132	6
205	Undecidability of Universality for Timed Automata with Minimal Resources. **2007**, 25-37	3
204	Costs Are Expensive!. **2007**, 53-68	4

203	Hypervolume Approximation in Timed Automata Model Checking. **2007**, 69-81	
202	Zone-Based Universality Analysis for Single-Clock Timed Automata. **2007**, 98-112	3
201	Real-Time Embedded Software Design for Mobile and Ubiquitous Systems. **2007**, 718-729	1
200	Accomplishing Approximate FCFS Fairness Without Queues. **2007**, 540-551	
199	On Reachability Games of Ordinal Length. **2008**, 211-221	
198	A Kleene-Schützenberger Theorem for Weighted Timed Automata. **2008**, 142-156	4
197	Trading Infinite Memory for Uniform Randomness in Timed Games. **2008**, 87-100	2
196	Timed Parity Games: Complexity and Robustness. 124-140	7
195	On Scheduling Policies for Streams of Structured Jobs. 141-154	2
194	Model Checking HML on Piecewise-Constant Inhomogeneous Markov Chains. 203-217	4
193	When Is Reachability Intrinsically Decidable?. 216-227	
192	Control of guarded automata counting event occurrences.	
191	Real-time communication mechanism for distributed objects in the MCPU-DFO model for Internet enterprise.	1
190	Efficient test generation for army network protocols with conflicting timers.	1
189	Impact Analysis of Cyber-Physical Attacks on a Water Tank System via Statistical Model Checking. **2020**,	1
188	Tackling the equivalent mutant problem in real-time systems. **2020**,	1
187	Qualitative Fault Modeling in Safety Critical Cyber Physical Systems. **2020**,	0
186	Practical Approach in Verification of Security Systems Using Satisfiability Modulo Theories.	0

185	Costs and rewards in priced timed automata. **2020**, 282, 104656	
184	Temporal Reasoning Through Automatic Translation of tock-CSP into Timed Automata. **2021**, 70-86	
183	The p-adic Theory of Automata Functions. **2021**, 9-113	
182	Architecture-Level Schedulability Analysis with IO Constraint using AADL. **2021**,	
181	Runtime Verification for Distributed Cyber-Physical Systems. **2021**,	
180	Verifying Stochastic Hybrid Systems with Temporal Logic Specifications via Model Reduction. **2021**, 20, 1-27	
179	Efficient parallel derivation of short distinguishing sequences for nondeterministic finite state machines using MapReduce. **2021**, 8,	
178	Formal Verification. **2022**, 65-155	
177	Model-checking ecological state-transition graphs.	1
176	Timed protocol analysis of interconnected mobile IoT devices. **2021**, 12,	0
175	Introducing Formal Methods to First-Year Students in Three Intensive Weeks. **2021**, 1-17	1
174	Model Checking Collision Avoidance of Nonlinear Autonomous Vehicles. **2021**, 676-694	2
173	Foundations. **2021**, 9-37	
172	Teaching Formal Methods to Software Engineers through Collaborative Learning (Short Paper). **2021**, 75-83	
171	Partial-Order Reduction for Supervisory Controller Synthesis. **2021**, 1-1	0
170	Minimization of the Number of Clocks for Timed Scenarios. **2021**, 122-139	
169	TACoS: A Tool for MTL Controller Synthesis. **2021**, 372-379	2
168	State Space Minimization Preserving Embeddings for Continuous-Time Markov Chains. **2021**, 44-61	1

167	Complexity issues for timeline-based planning over dense time under future and minimal semantics. *Theoretical Computer Science*, **2022**, 901, 87-113	1.1
166	Development of closed-loop modelling framework for adaptive respiratory pacemakers.. **2021**, 141, 105136	0
165	A Hard Real Time Demonstrator for Dynamic Ticks and Timed SCCharts. **2020**,	
164	Temporal Property-Based Testing of a Timed C Compiler using Time-Flow Graph Semantics. **2020**,	
163	Automating GDPR Compliance Verification for Cloud-hosted Services. **2020**,	1
162	A Model-driven Approach for the Formal Analysis of Human-Robot Interaction Scenarios. **2020**,	1
161	Parametric non-interference in timed automata. **2020**,	0
160	Formal analysis and verification of industrial control system security via timed automata. **2020**,	
159	Control Synthesis for Parametric Timed Automata under Unavoidability Specifications. **2021**,	0
158	Control approach for a class of hybrid systems. **2021**,	
157	Formal modelling of attack scenarios and mitigation strategies in IEEE 1588. **2021**,	
156	Modeling and Verification of Real-Time Systems with the Event Calculus and s(CASP). **2022**, 181-190	1
155	Towards an Ontology for Scenario Definition for the Assessment of Automated Vehicles: An Object-Oriented Framework. **2022**, 1-1	3
154	Online Motion Planning With Soft Metric Interval Temporal Logic in Unknown Dynamic Environment. **2022**, 6, 2293-2298	
153	Embedding knowledge into BIM: A case study of extending BIM with firefighting plans. **2022**, 49, 103999	1
152	Diagnosability of fault patterns with labeled stochastic Petri nets. **2022**,	0
151	Deadline Verification for Web Services Using Timed Automata. **2022**, 12, 8013-8016	
150	Modeling and Verification of Asynchronous Systems Using Timed Integrated Model of Distributed Systems.. **2022**, 22,	

149	A Graph Transformation Approach for Modeling UML Diagrams. **2022**, 12, 0-0	
148	poST: A Process-Oriented Extension of the IEC 61131-3 Structured Text Language. **2022**, 10, 35238-35250	1
147	States of Attack Under Incomplete Information. **2022**,	
146	Model Verification of Resilient Third-Party Monitoring System Against Cyberattacks. **2022**,	1
145	Formal Methods for the Security of Medical Devices 1. **2022**, 31-65	
144	Toward a design model-oriented methodology to ensure QoS of a cyber-physical healthcare system. 1	
143	Model Checking for Real-Time Attack Detection in Water Distribution Systems. **2022**, 21, 219-242	0
142	An Overview of Modest Models and Tools for Real Stochastic Timed Systems. 355, 1-12	
141	Verifiable strategy synthesis for multiple autonomous agents: a scalable approach. 1	3
140	Discrete Time Model for Process Meta Language with Fictitious-Clock. **2022**, 12, 2990	2
139	Stochastic Sharing Calculus for Reasoning About Social Networks.	
138	Enhancing cybersecurity by generating user-specific security policy through the formal modelling of user behavior. **2022**, 100146	1
137	Decidability and complexity of action-based temporal planning over dense time. **2022**, 307, 103686	
136	MINTS: Unsupervised Temporal Specifications Miner. **2021**,	
135	Analyzable Publish-Subcribe Communication Through a Wait-Free FIFO Channel for MPSoC Real-Time Applications. **2021**,	
134	Decisiveness of stochastic systems and its application to hybrid models. **2021**, 104861	
133	On the Modeling of Sequential Reactive Systems by Means of Real Time Automata. **2021**, 55, 751-762	
132	Analyzing the Impact of Cyberattacks on Industrial Control Systems using Timed Automata. **2021**,	1

131 Event-Triggered and Time-Triggered Duration Calculus for Model-Free Reinforcement Learning. **2021**, 1

130 Assured Learning-Based Optimal Control subject to Timed Temporal Logic Constraints. **2021**, 0

129 Hybrid System Modeling for Middle-Pressure Air Resource Dispatching of Wind Tunnel Group. **2021**,

128 Quingo: A Programming Framework for Heterogeneous Quantum-Classical Computing with NISQ Features. **2021**, 2, 1-37

127 A Probabilistic Logic for Verifying Continuous-time Markov Chains. **2022**, 3-21

126 Modeling and analyzing neuromorphic SNNs as discrete event systems. **2022**,

125 ActivFORMS: A Formally-Founded Model-Based Approach to Engineer Self-Adaptive Systems. 2

124 Parametric Timed Pattern Matching. 1

123 Early timing analysis based on scenario requirements and platform models. 1

122 Temporal Robustness of Stochastic Signals. **2022**, 2

121 Linear Time Monitoring for One Variable TPTL. **2022**,

120 Reachability Analysis for Cyber-Physical Systems: Are We There Yet?. **2022**, 109-130 1

119 Diagnosability of Unambiguous Max-Plus Automata. **2022**, 1-10

118 Exemplifying Parametric Timed Specifications over Signals with Bounded Behavior. **2022**, 470-488 1

117 Zone Extrapolations in Parametric Timed Automata. **2022**, 451-469

116 Timed Automata Learning via SMT Solving. **2022**, 489-507 2

115 Using Gossip Enabled Distributed Circuit Breaking for Improving Resiliency of Distributed Systems. **2022**, 0

114 Artifact for Measuring the Relative Efficacy of Gossip Enabled Distributed Circuit Breaking. **2022**,

113 Detectability of labeled weighted automata over monoids.

112 Timed automata as a formalism for expressing security: A survey on theory and practice. 0

111 Untangling the Graphs of Timed Automata to Decrease the Number of Clocks. **2022**, 168-187

110 A Formal Modeling and Verification Scheme with an RNN-Based Attacker for CAN Communication System Authenticity. **2022**, 11, 1773

109 PyLSE: a pulse-transfer level language for superconductor electronics. **2022**,

108 Simulation of Cyber-Physical Intelligent Mechatronic Component Behavior Using Timed Automata Approach. **2022**, 72-85

107 Waiting Nets. **2022**, 67-89

106 On the Determinization of Event-Clock Input-Driven Pushdown Automata. **2022**, 256-268

105 Protecting Smart Homes from Unintended Application Actions. **2022**, 1

104 Decentralized Multi-agent Coordination under MITL Tasks and Communication Constraints. **2022**,

103 References. **2022**, 211-217

102 Model-checking ecological state-transition graphs. **2022**, 18, e1009657

101 A Graph Based Semantics for Logical Functional Diagrams in Power Plant Controllers. **2022**, 55-74

100 Guaranteeing Timed Opacity using Parametric Timed Model Checking. 0

99 Using Formal Methods to Specify Data-Driven Cyber-Physical Systems. **2022**,

98 Uncertainty Theories for Real-Time Systems. **2022**, 99-132 0

97 An Efficient Subsumption Test Pipeline for BS(LRA) Clauses. **2022**, 147-168

96 A Formal Analysis of the Efficacy of Rebooting as a Countermeasure Against IoT Botnets. **2022**, 1

95	Transforming YAWL Workflows with Time Interval Constraints into Timed Automata. **2022**,	
94	Reachability in timed automata. **2022**, 9, 6-28	
93	Temporal Robustness of Temporal Logic Specifications: Analysis and Control Design.	3
92	Abstractions for the local-time semantics of timed automata: a foundation for partial-order methods. **2022**,	1
91	Bridging the gap between single- and multi-model predictive runtime verification.	
90	Towards a Digital Highway Code using Formal Modelling and Verification of Timed Automata. 371, 77-85	0
89	An Automatic Refinement for Event-B Through Annotated Temporal Logic Patterns. **2022**, 624-637	0
88	Timed Automata Networks for SCADA Attacks Real-Time Mitigation. **2022**, 549-559	0
87	Playing Wordle with Uppaal Stratego. **2022**, 283-305	0
86	Automated Surgical Procedure Assistance Framework Using Deep Learning and Formal Runtime Monitoring. **2022**, 25-44	0
85	Supervisory control of real-time discrete event systems. **2022**,	0
84	Zone-Based Verification of Timed Automata: Extrapolations, Simulations and What Next?. **2022**, 16-42	1
83	Spatial and Timing Properties in Highway Traffic. **2022**, 114-131	0
82	Towards Reusable Formal Models for Custom Real-Time Operating Systems. **2022**, 69-85	0
81	Human-Cyber-Physical Automata and Their Synthesis. **2022**, 36-41	0
80	Efficient Convex Zone Merging in Parametric Timed Automata. **2022**, 200-218	0
79	Bounded Delay Timed Channel Coding. **2022**, 65-79	0
78	Timed Games with Bounded Window Parity Objectives. **2022**, 165-182	0

77	Monitoring Timed Properties (Revisited). **2022**, 43-62	0
76	Statistical Model Checking for Probabilistic Hyperproperties of Real-Valued Signals. **2022**, 61-78	0
75	Equivalence Checking 40 Years After: A Review of Bisimulation Tools. **2022**, 213-265	1
74	FalsifAI: Falsification of AI-Enabled Hybrid Control Systems Guided by Time-Aware Coverage Criteria. **2022**, 1-17	1
73	Eulero: A Tool for Quantitative Modeling and Evaluation of Complex Workflows. **2022**, 255-272	0
72	Self-Healing in Web Service-Based Systems Using QoS. **2022**, 13, 1-17	0
71	Two-Phase Motion Planning under Signal Temporal Logic Specifications in Partially Unknown Environments. **2022**, 1-10	0
70	Formally Verifying Decompositions of Stochastic Specifications. **2022**, 193-210	0
69	On the Verification of Detectability for Timed Systems. **2022**,	0
68	Verification of Bounded Inclusion Problem for Timed Automata with Diagonal Constraints. **2022**,	0
67	Equivalence checking and intersection of deterministic timed finite state machines.	0
66	Static detection of equivalent mutants in real-time model-based mutation testing. **2022**, 27,	0
65	From Specification Models to Explanation Models: An Extraction and Refinement Process for Timed Automata. 371, 20-37	0
64	Dynamic controllability of temporal networks with instantaneous reaction. **2022**,	0
63	Formal modeling of the gPTP clock synchronization algorithm in automotive ethernet.	0
62	Control of cooperative manipulator-endowed systems under high-level tasks and uncertain dynamics. **2022**,	0
61	Dynamic Data Structures for Timed Automata Acceptance.	0
60	Using Homing Traces for Simplifying Passive Testing of Discrete Event Systems. **2022**,	0

59	Automated repair for timed systems.	0
58	Weight-deterministic max-plus automata. **2022**, 55, 291-296	0
57	A Bounded Model Checker for Timed Automata and Its Application to LTL Properties. **2022**, 207, 532-541	0
56	History-Deterministic Timed Automata Are Not Determinizable. **2022**, 67-76	1
55	Learning Deterministic One-Clock Timed Automata via Mutation Testing. **2022**, 233-248	0
54	POLAR: A Polynomial Arithmetic Framework for Verifying Neural-Network Controlled Systems. **2022**, 414-430	2
53	Reductions and Abstractions for Optimization of Modular Timed Automata. **2022**, 55, 344-349	0
52	On Model-Based Performance Analysis of Collective Adaptive Systems. **2022**, 266-282	1
51	Active Learning of One-Clock Timed Automata Using Constraint Solving. **2022**, 249-265	0
50	Verifiers for the detection of timed patterns in discrete event systems. **2022**, 55, 264-269	0
49	Repairing Real-Time Requirements. **2022**, 371-387	0
48	Importance Splitting in Uppaal. **2022**, 433-447	1
47	Specifying Source Code and Signal-based Behaviour of Cyber-Physical System Components. **2022**, 20-38	0
46	LTL falsification in infinite-state systems. **2022**, 104977	0
45	A formal toolchain for offline and run-time verification of robotic systems. **2022**, 104301	1
44	Distributed parametric model checking timed automata under non-Zenoness assumption.	0
43	Correctness-guaranteed strategy synthesis and compression for multi-agent autonomous systems. **2022**, 224, 102894	0
42	Verification and synthesis of co-simulation algorithms subject to algebraic loops and adaptive steps.	0

41	Hybrid Automata in Systems Biology. **2022**, 305-338	0
40	Timed network games. **2023**, 290, 104996	0
39	Model-Based Fault Classification for Automotive Software. **2022**, 110-131	1
38	An Efficient Customized Clock Allocation Algorithm for a Class of Timed Automata. **2022**, 3-21	0
37	Zeroing Control Barrier Function with Time Scale Transformation for Time Interval Signal Temporal Logic Task. **2022**,	0
36	Runtime Verification for Clinically Interpretable Arrhythmia Classification. **2022**,	0
35	strategFTO: Untimed Control for Timed Opacity. **2022**,	0
34	Automated Verification for Real-Time Systems using Implicit Clocks and an Extended Antimirov Algorithm. **2022**,	0
33	Compositional Analysis of Probabilistic Timed Graph Transformation Systems.	0
32	Model verification of fallback control system under cyberattacks via UPPAAL. 1-13	0
31	Rewriting Logic Semantics and Symbolic Analysis for Parametric Timed Automata. **2022**,	0
30	A scheme for train communication information management in onboard-centered train control system. **2022**,	0
29	Language Inclusion Checking of Timed Automata Based on Property Patterns. **2022**, 12, 12946	0
28	Bounded Model Checking for Metric Temporal Logic Properties of Timed Automata with Digital Clocks. **2022**, 22, 9552	0
27	Reachability Analysis of Cost-Reward Timed Automata for Energy Efficiency Scheduling. **2014**,	0
26	Policy-Based Hypertension Monitoring Using Formal Runtime Verification Monitors. **2022**, 169-179	0
25	Asynchronous Correspondences Between Hybrid Trajectory Semantics. **2022**, 131-159	0
24	On Specifications and Proofs of Timed Circuits. **2022**, 107-130	0

23	Improving consistency of AADL models: A composition approach.	0
22	Control strategies for off-line testing of timed systems.	0
21	Robustness Evaluation Process for Scheduling under Uncertainties. **2023**, 11, 371	1
20	A proven translation from a UML state machine subset to timed automata.	0
19	Cross-chain payment protocols with success guarantees.	0
18	Model-based design of resilient systems using quantitative risk assessment.	0
17	Modeling and formal analysis of meta-ecosystems with dynamic structure using graph transformation. **2023**, 75, 101908	0
16	Simulation Relations and Applications in Formal Methods. **2022**, 272-291	0
15	Design of Cyber Bio-analytical Physical Systems: Formal methods, architectures, and multi-system interaction strategies. **2023**, 97, 104780	0
14	Time-Sensitive Software. **2023**, 1,	0
13	Safety Verification of Multiple Industrial Robot Manipulators with Path Conflicts Using Model Checking. **2023**, 11, 282	0
12	Serial Compositional Runtime Enforcement of Safety Timed Properties. **2023**,	0
11	Learning Mealy machines with one timer. **2023**, 105013	0
10	Transforming Probabilistic Timed Automata to PRISM Model. **2022**,	0
9	A Fault-Tolerant and Reconfigurable Control Framework: Modeling, Design, and Synthesis. **2023**, 11, 701	0
8	Automatic modelling and verification of Autosar architectures. **2023**, 201, 111675	0
7	Computers as Interactive Machines: Can We Build an Explanatory Abstraction?. **2023**, 33, 83-112	0
6	Defense Models for Data Recovery in Industrial Control Systems. **2023**, 271-286	0

5	Robust Satisfaction of Metric Interval Temporal Logic Objectives in Adversarial Environments. **2023**, 14, 30	0
4	The Generalized Graph Real-Time Task Model. **2022**,	0
3	Timed Automata Verification and Synthesis via Finite Automata Learning. **2023**, 329-349	0
2	Automated Verification for Real-Time Systems. **2023**, 569-587	0
1	Probabilistic verification of diagnosability for a certain class of timed stochastic systems. **2023**, 176, 105531	0