

# Fast Random and Sequential Access to Dynamic Memory

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#	ARTICLE	IF	CITATIONS
1	Dynamic Memories with Faster Random and Sequential Access. IBM Journal of Research and Development, 1977, 21, 281-288.	3.1	4
2	Parallel Permutations of Data: A Benes Network Control Algorithm for Frequently Used Permutations. IEEE Transactions on Computers, 1978, C-27, 637-647.	3.4	122
3	Design and Validation of a Connection Network for Many-Processor Multiprocessor Systems. Computer, 1981, 14, 31-41.	1.1	40
4	Dynamic Memory Interconnections for Rapid Access. IEEE Transactions on Computers, 1984, C-33, 923-927.	3.4	1
5	The design and control of dynamic memory networks. , 0, , .		2
6	A UNIFIED APPROACH TO THE DESIGN AND CONTROL OF DYNAMIC MEMORY NETWORKS. Parallel Processing Letters, 1993, 03, 445-456.	0.6	5