

Minsu Kim

List of Publications by Year in descending order

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Version: 2024-02-01

17
papers

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2682572

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17
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31
citing authors

#	ARTICLE	IF	CITATIONS
1	Reinforcement-Learning-Based Signal Integrity Optimization and Analysis of a Scalable 3-D X-Point Array Structure. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2022, 12, 100-110.	2.5	5
2	Unsupervised Disentangling of Viewpoint and Residues Variations by Substituting Representations for Robust Face Recognition. , 2021, , .		1
3	Interpretation of Lesional Detection via Counterfactual Generation. , 2021, , .		3
4	Sequential Policy Network-based Optimal Passive Equalizer Design for an Arbitrary Channel of High Bandwidth Memory using Advantage Actor Critic. , 2021, , .		4
5	Crosstalk-included PAM-4 Worst Eye Diagram Estimation Method for High-speed Serial Links. , 2021, , .		0
6	Deep Reinforcement Learning Framework for Optimal Decoupling Capacitor Placement on General PDN with an Arbitrary Probing Port. , 2021, , .		3
7	Speech Reconstruction With Reminiscent Sound Via Visual Voice Memory. IEEE/ACM Transactions on Audio Speech and Language Processing, 2021, 29, 3654-3667.	5.8	10
8	PAM-4 based PCIe 6.0 Channel Design Optimization Method using Bayesian Optimization. , 2021, , .		3
9	Deep Reinforcement Learning-based Pin Assignment Optimization of BGA Packages considering Signal Integrity with Graph Representation. , 2021, , .		3
10	Learning Style Correlation for Elaborate Few-Shot Classification. , 2020, , .		0
11	Robust Video Facial Authentication With Unsupervised Mode Disentanglement. , 2020, , .		1
12	Reinforcement Learning-based Auto-router considering Signal Integrity. , 2020, , .		7
13	A 22.4 mW competitive fuzzy edge detection processor for volume rendering. , 2010, , .		0
14	Intelligent NoC with neuro-fuzzy bandwidth regulation for a 51 IP object recognition processor. , 2010, , .		6
15	A 76.8 GB/s 46 mW low-latency network-on-chip for real-time object recognition processor. , 2008, , .		3
16	The brain mimicking Visual Attention Engine: An 80×60 digital Cellular Neural Network for rapid global feature extraction. , 2008, , .		8
17	A 211 GOPS/W dual-mode real-time object recognition processor with Network-on-Chip. , 2008, , .		1