

# Bo-Yuan Huang

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/990289/publications.pdf>

Version: 2024-02-01

8  
papers

108  
citations

1937685

4  
h-index

2272923

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g-index

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8  
docs citations

8  
times ranked

84  
citing authors

#	ARTICLE	IF	CITATIONS
1	Generating Architecture-Level Abstractions from RTL Designs for Processors and Accelerators Part I: Determining Architectural State Variables. , 2021, , .		5
2	Intelligent REST API data fuzzing. , 2020, , .		28
3	ILAng: A Modeling and Verification Platform for SoCs Using Instruction-Level Abstractions. Lecture Notes in Computer Science, 2019, , 351-357.	1.3	7
4	Instruction-Level Abstraction (ILA). ACM Transactions on Design Automation of Electronic Systems, 2019, 24, 1-24.	2.6	24
5	A formal instruction-level GPU model for scalable verification. , 2018, , .		5
6	Formal security verification of concurrent firmware in SoCs using instruction-level abstraction for hardware. , 2018, , .		14
7	Template-Based Parameterized Synthesis of Uniform Instruction-Level Abstractions for SoC Verification. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2018, 37, 1692-1705.	2.7	10
8	Protocol Design and Game Theoretic Solutions for Device-to-Device Radio Resource Allocation. IEEE Transactions on Vehicular Technology, 2016, , 1-1.	6.3	15