

Daniel Reiser

List of Publications by Year in descending order

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5
papers

18
citations

3311381

1
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3475538

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g-index

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all docs

5
docs citations

5
times ranked

19
citing authors

#	ARTICLE	IF	CITATIONS
1	A Framework for Ultra Low-Power Hardware Accelerators Using NNs for Embedded Time Series Classification. Journal of Low Power Electronics and Applications, 2022, 12, 2.	2.0	0
2	Simulating large neural networks embedding MLC RRAM as weight storage considering device variations. , 2021, , .		1
3	Fast HBM Access with FPGAs: Analysis, Architectures, and Applications. , 2021, , .		9
4	A Low-Power RRAM Memory Block for Embedded, Multi-Level Weight and Bias Storage in Artificial Neural Networks. Micromachines, 2021, 12, 1277.	2.9	4
5	A Model-to-Circuit Compiler for Evaluation of DNN Accelerators based on Systolic Arrays and Multibit Emerging Memories. , 2020, , .		4