Daniel Reiser

List of Publications by Year
in descending order

		3311381	3475538
5 papers	**18** citations	**1** h-index	**1** g-index
5 all docs	**5** docs citations	**5** times ranked	**19** citing authors

#	ARTICLE	IF	CITATIONS
1	Fast HBM Access with FPGAs: Analysis, Architectures, and Applications. , 2021, , .		9
2	A Model-to-Circuit Compiler for Evaluation of DNN Accelerators based on Systolic Arrays and Multibit Emerging Memories. , 2020, , .		4
3	A Low-Power RRAM Memory Block for Embedded, Multi-Level Weight and Bias Storage in Artificial Neural Networks. Micromachines, 2021, 12, 1277.	2.9	4
4	Simulating large neural networks embedding MLC RRAM as weight storage considering device variations. , 2021, , .		1
5	A Framework for Ultra Low-Power Hardware Accelerators Using NNs for Embedded Time Series Classification. Journal of Low Power Electronics and Applications, 2022, 12, 2.	2.0	0