

Tao B Schardl

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/9880426/publications.pdf>

Version: 2024-02-01

24
papers

621
citations

1478505

6
h-index

1281871

11
g-index

25
all docs

25
docs citations

25
times ranked

380
citing authors

#	ARTICLE	IF	CITATIONS
1	There's plenty of room at the Top: What will drive computer performance after Moore's law?. Science, 2020, 368, .	12.6	171
2	A work-efficient parallel breadth-first search algorithm (or how to cope with the nondeterminism of) Tj ETQq0 0 0 rgBT /Overlock 10 Tf 5		136
3	Ordering heuristics for parallel graph coloring. , 2014, , .		69
4	On-the-Fly Pipeline Parallelism. ACM Transactions on Parallel Computing, 2015, 2, 1-42.	1.4	36
5	Executing dynamic data-graph computations deterministically using chromatic scheduling. , 2014, , .		32
6	Deterministic parallel random-number generation for dynamic-multithreading platforms. ACM SIGPLAN Notices, 2012, 47, 193-204.	0.2	29
7	The Cilkprof Scalability Profiler. , 2015, , .		29
8	Tapir. , 2017, , .		29
9	Deterministic parallel random-number generation for dynamic-multithreading platforms. , 2012, , .		26
10	Tapir. ACM Transactions on Parallel Computing, 2019, 6, 1-33.	1.4	13
11	The CSI Framework for Compiler-Inserted Program Instrumentation. Proceedings of the ACM on Measurement and Analysis of Computing Systems, 2017, 1, 1-25.	1.8	12
12	Tapir. ACM SIGPLAN Notices, 2017, 52, 249-265.	0.2	7
13	Executing Dynamic Data-Graph Computations Deterministically Using Chromatic Scheduling. ACM Transactions on Parallel Computing, 2016, 3, 1-31.	1.4	6
14	On the efficiency of localized work stealing. Information Processing Letters, 2016, 116, 100-106.	0.6	6
15	Finding a Hamiltonian Path in a Cube with Specified Turns is Hard. Journal of Information Processing, 2013, 21, 368-377.	0.4	2
16	TapirXLA: Embedding Fork-Join Parallelism into the XLA Compiler in TensorFlow Using Tapir. , 2019, , .		2
17	Cilkmem: Algorithms for Analyzing the Memory High-Water Mark of Fork-Join Parallel Programs. , 2020, , 162-176.		2
18	The CSI Framework for Compiler-Inserted Program Instrumentation. , 2018, , .		2

#	ARTICLE	IF	CITATIONS
19	Upper Bounds on Number of Steals in Rooted Trees. Theory of Computing Systems, 2016, 58, 223-240.	1.1	1
20	A Hybrid Scheduling Scheme for Parallel Loops. , 2021, , .		1
21	The CSI Framework for Compiler-Inserted Program Instrumentation. Performance Evaluation Review, 2019, 46, 100-102.	0.6	1
22	Folding Equilateral Plane Graphs. Lecture Notes in Computer Science, 2011, , 574-583.	1.3	0
23	Efficient Race Detection for Reducer Hyperobjects. ACM Transactions on Parallel Computing, 2018, 4, 1-40.	1.4	0
24	The CSI Framework for Compiler-Inserted Program Instrumentation. Performance Evaluation Review, 2019, 46, 100-102.	0.6	0