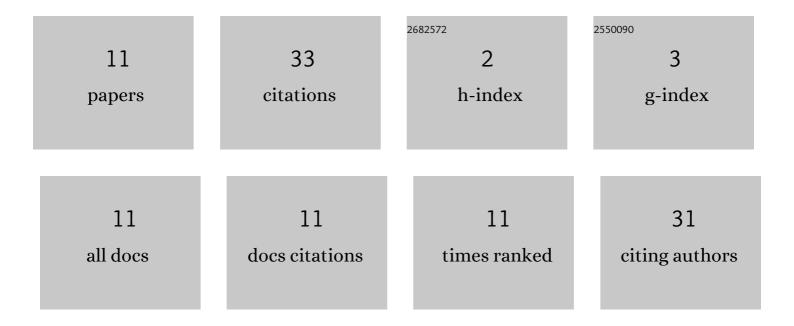
VojtÄ>ch MiÅ;kovský

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/98535/publications.pdf Version: 2024-02-01



#	Article	IF	CITATIONS
1	A fair experimental evaluation of distance correlation side-channel distinguisher. , 2022, , .		0
2	High-level synthesis, cryptography, and side-channel countermeasures: A comprehensive evaluation. Microprocessors and Microsystems, 2021, 85, 104311.	2.8	2
3	Efficient algorithmic evaluation of correlation power analysis: Key distinguisher based on the correlation trace derivative. Microprocessors and Microsystems, 2019, 71, 102858.	2.8	1
4	First-Order and Higher-Order Power Analysis: Computational Approaches and Aspects. , 2019, , .		0
5	Dummy Rounds as a DPA Countermeasure in Hardware. , 2018, , .		5
6	Correlation Power Analysis Distinguisher Based on the Correlation Trace Derivative. , 2018, , .		3
7	Speeding up differential power analysis using integrated power traces. , 2018, , .		1
8	Influence of passive hardware redundancy on differential power analysis resistance of AES cipher implemented in FPGA. Microprocessors and Microsystems, 2017, 51, 220-226.	2.8	4
9	Optimization of Pearson correlation coefficient calculation for DPA and comparison of different approaches. , 2017, , .		12
10	Influence of Fault-Tolerance Techniques on Power-Analysis Resistance of Cryptographic Design. , 2017, , .		2
11	Influence of fault-tolerant design methods on differential power analysis resistance of AES cipher: Methodics and challenges. , 2016, , .		3