

# Vojtěch Miškovský<sup>1/2</sup>

## List of Publications by Year in descending order

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11  
papers

33  
citations

2682572

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2550090

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all docs

11  
docs citations

11  
times ranked

31  
citing authors

#	ARTICLE	IF	CITATIONS
1	Optimization of Pearson correlation coefficient calculation for DPA and comparison of different approaches. , 2017, , .		12
2	Dummy Rounds as a DPA Countermeasure in Hardware. , 2018, , .		5
3	Influence of passive hardware redundancy on differential power analysis resistance of AES cipher implemented in FPGA. Microprocessors and Microsystems, 2017, 51, 220-226.	2.8	4
4	Influence of fault-tolerant design methods on differential power analysis resistance of AES cipher: Methodics and challenges. , 2016, , .		3
5	Correlation Power Analysis Distinguisher Based on the Correlation Trace Derivative. , 2018, , .		3
6	Influence of Fault-Tolerance Techniques on Power-Analysis Resistance of Cryptographic Design. , 2017, , .		2
7	High-level synthesis, cryptography, and side-channel countermeasures: A comprehensive evaluation. Microprocessors and Microsystems, 2021, 85, 104311.	2.8	2
8	Speeding up differential power analysis using integrated power traces. , 2018, , .		1
9	Efficient algorithmic evaluation of correlation power analysis: Key distinguisher based on the correlation trace derivative. Microprocessors and Microsystems, 2019, 71, 102858.	2.8	1
10	First-Order and Higher-Order Power Analysis: Computational Approaches and Aspects. , 2019, , .		0
11	A fair experimental evaluation of distance correlation side-channel distinguisher. , 2022, , .		0