Wantong Li

List of Publications by Year
in descending order

7 papers	86 citations	1937685 4 h-index	2272923 4 g-index
7 all docs	7 docs citations	7 times ranked	35 citing authors

#	Article	IF	Citations
1	Analog-to-Digital Converter Design Exploration for Compute-in-Memory Accelerators. IEEE Design and Test, 2022, 39, 48-55.	1.2	16
2	A 40-nm MLC-RRAM Compute-in-Memory Macro With Sparsity Control, On-Chip Write-Verify, and Temperature-Independent ADC References. IEEE Journal of Solid-State Circuits, 2022, 57, 2868-2877.	5.4	21
3	NeuroSim Simulator for Compute-in-Memory Hardware Accelerator: Validation and Benchmark. Frontiers in Artificial Intelligence, 2021, 4, 659060.	3.4	23
4	Secure XOR-CIM Engine: Compute-In-Memory SRAM Architecture With Embedded XOR Encryption. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 2027-2039.	3.1	8
5	Compute-in-Memory: From Device Innovation to 3D System Integration. , 2021, , .		5
6	A 40nm RRAM Compute-in-Memory Macro Featuring On-Chip Write-Verify and Offset-Cancelling ADC References. , 2021, , .		0
7	XOR-CIM. , 2020, , .		13