

Wantong Li

List of Publications by Year in descending order

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papers

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1937685
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35
citing authors

#	ARTICLE	IF	CITATIONS
1	NeuroSim Simulator for Compute-in-Memory Hardware Accelerator: Validation and Benchmark. <i>Frontiers in Artificial Intelligence</i> , 2021, 4, 659060.	3.4	23
2	A 40-nm MLC-RRAM Compute-in-Memory Macro With Sparsity Control, On-Chip Write-Verify, and Temperature-Independent ADC References. <i>IEEE Journal of Solid-State Circuits</i> , 2022, 57, 2868-2877.	5.4	21
3	Analog-to-Digital Converter Design Exploration for Compute-in-Memory Accelerators. <i>IEEE Design and Test</i> , 2022, 39, 48-55.	1.2	16
4	XOR-CIM., 2020, , .		13
5	Secure XOR-CIM Engine: Compute-In-Memory SRAM Architecture With Embedded XOR Encryption. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2021, 29, 2027-2039.	3.1	8
6	Compute-in-Memory: From Device Innovation to 3D System Integration. , 2021, , .		5
7	A 40nm RRAM Compute-in-Memory Macro Featuring On-Chip Write-Verify and Offset-Cancelling ADC References. , 2021, , .		0