

Asher Sajid

List of Publications by Year in descending order

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citing authors

#	ARTICLE	IF	CITATIONS
1	An Optimized Architecture for Binary Huff Curves With Improved Security. IEEE Access, 2021, 9, 88498-88511.	4.2	11
2	A Low-Complexity Edward-Curve Point Multiplication Architecture. Electronics (Switzerland), 2021, 10, 1080.	3.1	11
3	AREEBA: An Area Efficient Binary Huff-Curve ARchitecture. Electronics (Switzerland), 2021, 10, 1490.	3.1	4
4	A Novel Low-Area Point Multiplication Architecture for Elliptic-Curve Cryptography. Electronics (Switzerland), 2021, 10, 2698.	3.1	6
5	An Efficient Elliptic-Curve Point Multiplication Architecture for High-Speed Cryptographic Applications. Electronics (Switzerland), 2020, 9, 2126.	3.1	8