Amirhossein Moeini

List of Publications by Year
in descending order

24	625	933447 10	1281871 11
papers	citations	h-index	g-index

24	24	24	451
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	Reinforcement Learning-Based Load Forecasting of Electric Vehicle Charging Station Using Q-Learning Technique. IEEE Transactions on Industrial Informatics, 2021, 17, 4229-4237.	11.3	103
2	Deep Learning-Based Real-Time Switching of Hybrid AC/DC Transmission Networks. IEEE Transactions on Smart Grid, 2021, 12, 2331-2342.	9.0	6
3	Machine learning technique for low-frequency modulation techniques in power converters. , 2021, , 149-167.		2
4	Modeling and Control of Cascaded Bridgeless Multilevel Rectifier Under Unbalanced Load Conditions. , 2021, , .		3
5	A Hybrid Phase Shift-Pulsewidth Modulation and Asymmetric Selective Harmonic Current Mitigation-Pulsewidth Modulation Technique to Reduce Harmonics and Inductance of Single-Phase Grid-Tied Cascaded Multilevel Converters. IEEE Transactions on Industrial Electronics, 2020, 67, 10388-10398.	7.9	10
6	DC Link Voltage Balancing of the Active Front-End for the Extreme Fast Charging Stations. , 2020, , .		2
7	Q-Learning-Based Smart Selective Harmonic Current Mitigation-PWM (S²HCM-PWM) for Grid-Connected Converters. , 2020, , .		2
8	Critical Parameter Design for a Cascaded H-Bridge With Selective Harmonic Elimination/Compensation Based on Harmonic Envelope Analysis for Single-Phase Systems. IEEE Transactions on Industrial Electronics, 2019, 66, 2914-2925.	7.9	24
9	Analyzing and Reducing Current Harmonics of AC and DC sides of Cascaded H-Bridge Converters for Electric Vehicle Charging Stations. , 2019, , .		4
10	An Asymmetric Selective Harmonic Current and Voltage Modulation-PWM Technique for Electric Vehicle Charging Stations with Cascaded H-Bridge Converters to Meet Power Quality Standards. , 2019, , .		1
11	Design of fast charging technique for electrical vehicle charging stations with grid-tied cascaded H-bridge multilevel converters. , 2018, , .		18
12	The state of charge balancing techniques for electrical vehicle charging stations with cascaded H-bridge multilevel converters. , 2018, , .		14
13	A Current-Reference-Based Selective Harmonic Current Mitigation PWM Technique to Improve the Performance of Cascaded H-Bridge Multilevel Active Rectifiers. IEEE Transactions on Industrial Electronics, 2018, 65, 727-737.	7.9	86
14	A DC Link Sensor-Less Voltage Balancing Technique for Cascaded H-Bridge Multilevel Converters With Asymmetric Selective Harmonic Current Mitigation-PWM. IEEE Transactions on Power Electronics, 2018, 33, 7571-7581.	7.9	26
15	Fast and Precise Detection of Internal Short Circuit on Li-Ion Battery. , 2018, , .		6
16	DC link voltage balancing technique for cascaded H-bridge multilevel converter with selective harmonic current mitigation-PWM. , 2017, , .		1
17	A cascaded hybrid phase shift-PWM and asymmetric selective harmonic mitigation-PWM modulation technique for grid-tied converter to reduce the switching frequency and meet the grid current harmonic requirement. , 2017, , .		7
18	Improve Control to Output Dynamic Response and Extend Modulation Index Range With Hybrid Selective Harmonic Current Mitigation-PWM and Phase-Shift PWM for Four-Quadrant Cascaded H-Bridge Converters. IEEE Transactions on Industrial Electronics, 2017, 64, 6854-6863.	7.9	23

#	Article	IF	Citations
19	High efficiency, hybrid Selective Harmonic Elimination phase-shift PWM technique for Cascaded H-Bridge inverters to improve dynamic response and operate in complete normal modulation indices. , 2016, , .		15
20	Optimal Selective Harmonic Mitigation Technique on Variable DC Link Cascaded H-Bridge Converter to Meet Power Quality Standards. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2016, 4, 1107-1116.	5.4	75
21	Asymmetric selective harmonic elimination technique using partial derivative for cascaded modular active rectifiers tied to a power grid with voltage harmonics. , 2016, , .		4
22	DC link voltage balancing approach for cascaded Hâ€bridge active rectifier based on selective harmonic eliminationâ€pulse width modulation. IET Power Electronics, 2015, 8, 583-590.	2.1	47
23	Selective harmonic mitigationâ€pulseâ€width modulation technique with variable DCâ€link voltages in single and threeâ€phase cascaded Hâ€bridge inverters. IET Power Electronics, 2014, 7, 924-932.	2.1	76
24	An optimal selective harmonic mitigation technique for high power converters. International Journal of Electrical Power and Energy Systems, 2013, 49, 34-39.	5.5	70