

Teyuh Chou

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/973051/publications.pdf>

Version: 2024-02-01

5
papers

129
citations

2682572

2
h-index

2917675

2
g-index

5
all docs

5
docs citations

5
times ranked

269
citing authors

#	ARTICLE	IF	CITATIONS
1	Mitigating Asymmetric Nonlinear Weight Update Effects in Hardware Neural Network Based on Analog Resistive Synapse. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2018, 8, 116-124.	3.6	87
2	Challenges and opportunities toward online training acceleration using RRAM-based hardware neural network. , 2017, , .		26
3	A 1.87-mm ² 56.9-GOPS Accelerator for Solving Partial Differential Equations. IEEE Journal of Solid-State Circuits, 2020, 55, 1709-1718.	5.4	11
4	Neuromorphic pattern learning using HBM electronic synapse with excitatory and inhibitory plasticity. , 2015, , .		5
5	Development of three-dimensional synaptic device and neuromorphic computing hardware. , 2016, , .		0