Vandana Nath

List of Publications by Year in descending order

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933447 1058476 33 355 10 14 citations h-index g-index papers 33 33 33 223 docs citations times ranked citing authors all docs

#	Article	IF	CITATIONS
1	Dual metal Schottky barrier asymmetric gate stack cylindrical gate all around (DM-SB-ASMGS-CGAA) MOSFET for improved analog performance for high frequency application. Microsystem Technologies, 2022, 28, 761-770.	2.0	21
2	Dual-band elliptical wide-slot antenna with high BDR for portable wireless applications. International Journal of Electronics, 2021, 108, 442-461.	1.4	1
3	Circularly Polarized Microstrip-Line-Fed Antenna with Rotated Elliptical Slot Serving Satellite Communications. Wireless Personal Communications, 2020, 110, 1443-1458.	2.7	4
4	Design and development of triple-band compact ACS-fed MIMO antenna for 2.4/3.5/5ÂGHz WLAN/WiMAX applications. Analog Integrated Circuits and Signal Processing, 2020, 103, 461-470.	1.4	13
5	A circularly polarized printed elliptical wide-slot antenna with high bandwidth-dimension-ratio for wireless applications. Wireless Networks, 2020, 26, 5485-5499.	3.0	7
6	A high BDR microstripâ€line fed antenna with multiple asymmetric elliptical wideâ€slots for wideband applications. International Journal of RF and Microwave Computer-Aided Engineering, 2020, 30, e22202.	1.2	3
7	Impact of Reverse Gate Oxide Stacking on Gate All Around Tunnel FET for High Frequency Analog and RF Applications. , 2020, , .		15
8	Schottky Barrier Double Surrounding Gate MOSFET for High-Frequency Implementation. , 2020, , .		2
9	Open Ended Microstrip-line-fed Compact Wideband MIMO-Diversity Antenna with Multiple Asymmetric Elliptical Wide-Slots. , 2019, , .		O
10	A Compact Flowerâ€Shaped Printed Monopole MIMO Antenna for Wideband Applications. Radio Science, 2019, 54, 963-974.	1.6	7
11	Optimization of Asymmetric π Gate HEMT for Improved Reliability & Frequency Applications. , 2019, ,		7
12	Comparison of Linearity and Intermodulation Distortion Metrics for T - and Pi - Gate HEMT., 2019, , .		6
13	Novel design to improve band to band tunneling and gate induced drain leakages (GIDL) in cylindrical gate all around (GAA) MOSFET. Microsystem Technologies, 2019, 25, 1537-1546.	2.0	25
14	Hafnium oxide based cylindrical junctionless double surrounding gate (CJLDSG) MOSFET for high speed, high frequency digital and analog applications. Microsystem Technologies, 2019, 25, 1527-1536.	2.0	31
15	Introducing Multiband and Wideband Microstrip Patch Antennas Using Fractal Geometries: Development in Last Decade. Wireless Personal Communications, 2018, 98, 2079-2105.	2.7	30
16	Multiband CPW-fed Circular Microstrip Antenna with Modified Cantor Fractal Slot for DCS/GPS/WiMAX/WLAN/HiPERLAN2 Applications. , 2018, , .		3
17	Dual-Band Dual-Polarized Stacked Octagonal Fractal Patch Antenna with Nonlinear Manipulation. , $2018, \ldots$		1
18	Triple Band Non-Linear Manipulated Sierpinski-Knopp Fractal Wide-Slot Microstrip Antenna with Inverted L-shaped Strip., 2018,,.		1

#	Article	IF	Citations
19	Microstripâ€lineâ€fed elliptical wideâ€slot antenna with similar parasitic patch for multiband applications. IET Microwaves, Antennas and Propagation, 2018, 12, 2172-2178.	1.4	12
20	Gate-Induced Drain Leakage Reduction in Cylindrical Dual-Metal Hetero-Dielectric Gate All Around MOSFET. IEEE Transactions on Electron Devices, 2017, , 1-8.	3.0	14
21	A numerical model of GaN based cylindrical junctionless gate all around MOSFET for subthreshold region at cryogenic temperatures. , 2017, , .		0
22	Dual metal (DM) Insulated Shallow Extension (ISE) Gate All Around (GAA) MOSFET to reduce gate induced drain leakages (GIDL) for improved analog performance. , 2017, , .		7
23	Improved cross polarization and wideband multilayer wide-slot microstrip antenna with rotated parasitic patch., 2017,,.		1
24	GaN based Junctionless Double Surrounding Gate (JLDSG) MOSFET for high power, high voltage and high frequency applications. , 2016 , , .		3
25	Dual-band microstrip line-fed antenna with fractal Spidron defected ground structure. , 2016, , .		4
26	Analysis of low mutual coupling compact multi-band microstrip patch antenna and its array using defected ground structure. Engineering Science and Technology, an International Journal, 2016, 19, 866-874.	3.2	25
27	Improved analog and AC performance with increased noise immunity using nanotube junctionless field effect transistor (NJLFET). Applied Physics A: Materials Science and Processing, 2016, 122, 1.	2.3	34
28	AC analysis of Junctionless Double Surrounding Gate (JLDSG) MOSFET for Tera Hertz applications. , 2016, , .		0
29	Design and simulation of tri-band spidron fractal equilateral triangle microstrip antenna. , 2016, , .		5
30	Numerical modeling of Subthreshold region of junctionless double surrounding gate MOSFET (JLDSG). Superlattices and Microstructures, 2016, 90, 8-19.	3.1	58
31	Mitigation of Atmospheric Turbulence in Free Space Optics: A Review. , 2015, , .		8
32	An efficient high performance turbo code implementation in Rayleigh fading channel. , 2014, , .		0
33	Silicon carbide based DSG MOSFET for high power, high speed and high frequency applications. , 2014, ,		7