

# Manikandan Subbu

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/9640211/publications.pdf>

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5  
papers

36  
citations

2682572

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2917675

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g-index

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docs citations

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times ranked

33  
citing authors

#	ARTICLE	IF	CITATIONS
1	A Quasi 2-D Electrostatic Potential and Threshold Voltage Model for Junctionless Triple Material Cylindrical Surrounding Gate Si Nanowire Transistor. Journal of Nanoelectronics and Optoelectronics, 2021, 16, 318-323.	0.5	0
2	Analytical Model of Double Gate Stacked Oxide Junctionless Transistor Considering Source/Drain Depletion Effects for CMOS Low Power Applications. Silicon, 2020, 12, 2053-2063.	3.3	16
3	The improved RF/stability and linearity performance of the ultrathin-body Gaussian-doped junctionless FinFET. Journal of Computational Electronics, 2020, 19, 613-621.	2.5	19
4	A Physics Based Threshold Voltage Modeling of Trigate Junctionless FinFETs Considering Gaussian Doping. Silicon, 0, , 1.	3.3	1
5	A 2D Unified Subthreshold Drain Current Investigation for Junctionless Cylindrical Surrounding Gate(JCSG) Silicon Nanowire Transistor. Silicon, 0, , 1.	3.3	0