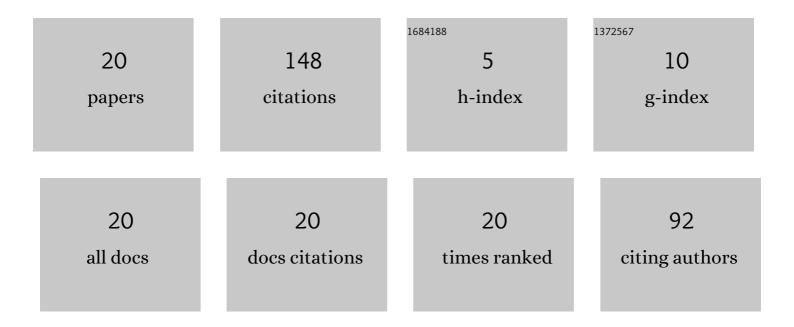
## Marko S Andjelković

List of Publications by Year in descending order

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| #  | Article   | IF  | CITATIONS |
|----|---|-----|-----------|
| 1  | Commercial P-Channel Power VDMOSFET as X-ray Dosimeter. Electronics (Switzerland), 2022, 11, 918.   | 3.1 | 1         |
| 2  | A design concept for radiation hardened RADFET readout system for space applications.<br>Microprocessors and Microsystems, 2022, 90, 104486.  | 2.8 | 1         |
| 3  | Fast Error Propagation Probability Estimates by Answer Set Programming and Approximate Model<br>Counting. IEEE Access, 2022, 10, 51814-51825.   | 4.2 | 1         |
| 4  | Response of Commercial P-Channel Power VDMOS Transistors to Ionizing Irradiation and Bias<br>Temperature Stress. Journal of Circuits, Systems and Computers, 2022, 31, .                            | 1.5 | 1         |
| 5  | Radiation sensitive MOSFETs irradiated with various positive gate biases. Journal of Radiation<br>Research and Applied Sciences, 2021, 14, 353-357.   | 1.2 | 2         |
| 6  | Design and Evaluation of Radiation-Hardened Standard Cell Flip-Flops. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 4796-4809.   | 5.4 | 16        |
| 7  | Reliability Analysis in Less than 200 Lines of Code. , 2021, , .  |     | 2         |
| 8  | Monitoring of Particle Count Rate and LET Variations With Pulse Stretching Inverters. IEEE<br>Transactions on Nuclear Science, 2021, 68, 1772-1781.   | 2.0 | 4         |
| 9  | PISA: Power-robust Multiprocessor Design for Space Applications. , 2020, , .  |     | 12        |
| 10 | Design of Radiation Hardened RADFET Readout System for Space Applications. , 2020, , .  |     | 3         |
| 11 | Design Concept for Radiation-Hardening of Triple Modular Redundancy TSPC Flip-Flops. , 2020, , .  |     | 2         |
| 12 | A Particle Detector Based on Pulse Stretching Inverter Chain. , 2019, , .   |     | 4         |
| 13 | Design of SRAM-Based Low-Cost SEU Monitor for Self-Adaptive Multiprocessing Systems. , 2019, , .  |     | 11        |
| 14 | D-SET Mitigation Using Common Clock Tree Insertion Techniques for Triple-Clock TMR Flip-Flop. , 2018, ,   |     | 6         |
| 15 | SET response of a SEL protection switch for 130 and 250 nm CMOS technologies. , 2016, , .   |     | 3         |
| 16 | Using RADFET for the real-time measurement of gamma radiation dose rate. Measurement Science and Technology, 2015, 26, 025004.  | 2.6 | 26        |
| 17 | The behavior of fixed and switching oxide traps of RADFETs during irradiation up to high absorbed doses. Applied Radiation and Isotopes, 2015, 102, 29-34.  | 1.5 | 16        |
| 18 | Circuit-Level Simulation of the Single Event Transients in an On-Chip Single Event Latchup Protection<br>Switch. Journal of Electronic Testing: Theory and Applications (JETTA), 2015, 31, 275-289. | 1.2 | 8         |

| #  | Article   | IF  | CITATIONS |
|----|---|-----|-----------|
| 19 | Simulation-Based Analysis of the Single Event Transient Response of a Single Event Latchup Protection Switch. , 2015, , .   |     | 3         |
| 20 | Feasibility study of a current mode gamma radiation dosimeter based on a commercial pin photodiode<br>and a custom made auto-ranging electrometer. Nuclear Technology and Radiation Protection, 2013, 28,<br>73-83. | 0.8 | 26        |