# Yi-Shan Lu

## List of Publications by Year
## in descending order

| 3 | 20 | 3311381 | 3475538 |
|---|---|---|---|
| papers | citations | 1 | 1 |
| | | h-index | g-index |

| 3 | 3 | 3 | 8 |
|---|---|---|---|
| all docs | docs citations | times ranked | citing authors |

| # | Article | IF | Citations |
|---|---------|-----|-----------|
| 1 | An Open-Source EDA Flow for Asynchronous Logic. IEEE Design and Test, 2021, 38, 27-37. | 1.2 | 10 |
| 2 | Cyclone: A Static Timing and Power Engine for Asynchronous Circuits. , 2020, , . | | 5 |
| 3 | Unlocking fine-grain parallelism for AIG rewriting. , 2018, , . | | 5 |