

Ali Mahani

List of Publications by Year in descending order

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all docs

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docs citations

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times ranked

337
citing authors

#	ARTICLE	IF	CITATIONS
1	A novel component mixing and mixed redundancy strategy for reliability optimization. International Journal of Systems Assurance Engineering and Management, 2022, 13, 328-346.	2.4	5
2	Accelerating Deep Convolutional Neural Network base on stochastic computing. The Integration VLSI Journal, 2021, 76, 113-121.	2.1	11
3	Reliable advanced encryption standard hardware implementation: 32-bit and 64-bit data-paths. Microprocessors and Microsystems, 2021, 81, 103740.	2.8	10
4	Noise-based logic locking scheme against signal probability skew analysis. IET Computers and Digital Techniques, 2021, 15, 279-295.	1.2	2
5	Strengthened 32-bit AES implementation: Architectural error correction configuration with a new voting scheme. IET Computers and Digital Techniques, 2021, 15, 395.	1.2	1
6	High-throughput configurable SIMON architecture for flexible security. Microelectronics Journal, 2021, 113, 105085.	2.0	4
7	A low cost fault-attack resilient AES for IoT applications. Microelectronics Reliability, 2021, 123, 114202.	1.7	14
8	Hardware acceleration of the novel two dimensional Burrows-Wheeler Aligner algorithm with maximal exact matches seed extension kernel. IET Circuits, Devices and Systems, 2021, 15, 94-103.	1.4	0
9	H-V scan and diagonal trajectory: accurate and low power localization algorithms in WSNs. Journal of Ambient Intelligence and Humanized Computing, 2020, 11, 2871-2882.	4.9	8
10	A Novel Energy-Efficient Clustering Protocol Using Two-Stage Genetic Algorithm for Improving the Lifetime of Wireless Sensor Networks. International Journal of Computational Intelligence and Applications, 2020, 19, .	0.8	2
11	Built-in self-repair structure for real-time fault recovery applications. Microelectronics Reliability, 2020, 111, 113726.	1.7	1
12	Obstacle-resistant hybrid localisation algorithm. IET Wireless Sensor Systems, 2020, 10, 242-252.	1.7	4
13	High throughput fault-resilient AES architecture. IET Computers and Digital Techniques, 2019, 13, 312-323.	1.2	5
14	Practical fault resilient hardware implementations of AES. IET Circuits, Devices and Systems, 2019, 13, 596-606.	1.4	9
15	Reliable S-Box Hardware Implementation by Gate-Level Fault Masking Enhancement. Journal of Control, Automation and Electrical Systems, 2019, 30, 214-228.	2.0	0
16	Memristor-Based Hybrid Fault Tolerant Structure With Concurrent Reconfigurability. IEEE Embedded Systems Letters, 2019, 11, 73-76.	1.9	1
17	Gravitational search algorithm with both attractive and repulsive forces. Soft Computing, 2019, 23, 783-825.	3.6	23
18	Efficient reliability-redundancy allocation with uniform importance measure in presence of correlated failure. International Journal of Computers and Applications, 2019, 41, 378-391.	1.3	1

#	ARTICLE	IF	CITATIONS
19	Accurate 3D-Tracking System for Wireless Indoor Personal Positioning. , 2018, , .		2
20	Fault-tolerant delay cell for ring oscillator application in 65-nm CMOS technology. IET Circuits, Devices and Systems, 2018, 12, 233-241.	1.4	12
21	A New ASIC Structure With Self-Repair Capability Using Field-Programmable Nanowire Interconnect Architecture. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 2268-2278.	3.1	1
22	Availability Improvement Method for Repairable Systems Using Modified Ant Colony Optimisation. International Journal of Computational Intelligence and Applications, 2017, 16, 1750011.	0.8	1
23	Particle Swarm Optimization with Intelligent Mutation for Nonlinear Mixed-Integer Reliability-Redundancy Allocation. International Journal of Computational Intelligence and Applications, 2017, 16, 1750003.	0.8	8
24	A novel 2-phase reliability improvement of digital circuits. AIP Conference Proceedings, 2016, , .	0.4	1
25	A novel anchor-based localization method. , 2016, , .		0
26	A novel gate grading approach for soft error tolerance in combinational circuits. , 2016, , .		1
27	Load balanced energy-aware genetic algorithm clustering in wireless sensor networks. , 2016, , .		1
28	Low power modular redundancy: a power efficient fault tolerant approach for digital circuits. COMPEL - the International Journal for Computation and Mathematics in Electrical and Electronic Engineering, 2016, 35, .	0.9	7
29	A high-resolution time-to-digital converter using a three-level resolution. International Journal of Electronics, 2016, 103, 1248-1261.	1.4	6
30	Reliability Evaluation Methods for Resilient Wireless Sensor Networks. , 2016, , 1306-1331.		1
31	An accurate and fast reliability analysis method for combinational circuits. COMPEL - the International Journal for Computation and Mathematics in Electrical and Electronic Engineering, 2015, 34, 979-995.	0.9	4
32	Time-to-digital convertor based on resolution control. IET Circuits, Devices and Systems, 2015, 9, 370-376.	1.4	0
33	New Design of Scan Flip-Flop to Increase Speed and Reduce Power Consumption. Journal of Circuits, Systems and Computers, 2015, 24, 1550159.	1.5	2
34	Energy efficient fast predictor for WSN-based target tracking. Annales Des Telecommunications/Annals of Telecommunications, 2015, 70, 63-71.	2.5	16
35	A NOVEL FAULT TOLERANT SWITCH FOR RELIABLE NoC DESIGN. Journal of Circuits, Systems and Computers, 2014, 23, 1450142.	1.5	2
36	A Novel Distributed Clustering Protocol Using Fuzzy Logic. Procedia Technology, 2014, 17, 742-748.	1.1	11

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37	On the security of RFID anti-counting security protocol (ACSP). Journal of Computational and Applied Mathematics, 2014, 259, 512-521.	2.0	8
38	A new automated design and optimization method of CMOS logic circuits based on Modified Imperialistic Competitive Algorithm. Applied Soft Computing Journal, 2014, 21, 423-432.	7.2	7
39	Channel assignment in multi-radio wireless mesh networks using an improved gravitational search algorithm. Journal of Network and Computer Applications, 2014, 38, 163-171.	9.1	47
40	Routing and wavelength assignment in optical networks using Artificial Bee Colony algorithm. Optik, 2013, 124, 1243-1249.	2.9	18
41	Sybil attack Detection: Improving security of WSNs for smart power grid application. , 2013, , .		10
42	Energy-efficient network design via modelling: optimal designing point for energy, reliability, coverage and end-to-end delay. IET Networks, 2013, 2, 11-18.	1.8	7
43	Reliability analysis of logic circuits using binary probabilistic transfer matrix. , 2013, , .		3
44	PROBABILISTIC TRANSFER MATRIX WITH MIXED BINARY-DECIMAL CODING FOR LOGIC CIRCUIT RELIABILITY ANALYSIS. Journal of Circuits, Systems and Computers, 2013, 22, 1350064.	1.5	3
45	Reliability or performance: A tradeoff in wireless sensor networks. , 2012, , .		2
46	Evolutionary algorithms for solving routing and wavelength assignment problem in optical networks: A comparative study. , 2012, , .		2
47	FPGA implementation of LMS self correcting adaptive filter (SCAF) and hardware analysis. , 2012, , .		8
48	Artificial Bee Colony model for survivable DWDM optical networks design. , 2012, , .		0
49	Duplication avoidance for energy efficient wireless sensor networks. , 2012, , .		0
50	Non-uniform distribution of multi-hop sensor networks: performance improvement and energy hole mitigation. IET Wireless Sensor Systems, 2012, 2, 302-308.	1.7	8
51	Two tiers wireless mesh networks: Optimal configuration. , 2011, , .		2
52	Reliability Evaluation Methods for Resilient Wireless Sensor Networks. Advances in Wireless Technologies and Telecommunication Book Series, 0, , 146-171.	0.4	1
53	Object Tracking Algorithms in Wireless Sensor Networks. Advances in Wireless Technologies and Telecommunication Book Series, 0, , 287-320.	0.4	0