

Changju Yang

List of Publications by Year in descending order

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28
papers

1,406
citations

687363

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794594

19
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28
all docs

28
docs citations

28
times ranked

935
citing authors

#	ARTICLE	IF	CITATIONS
1	Memristor Emulator for Memristor Circuit Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 2422-2431.	5.4	326
2	Neural Synaptic Weighting With a Pulse-Based Memristor Circuit. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 148-158.	5.4	307
3	Memristor Bridge Synapses. Proceedings of the IEEE, 2012, 100, 2061-2070.	21.3	229
4	A Circuit-Based Learning Architecture for Multilayer Neural Networks With Memristor Bridge Synapses. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 215-223.	5.4	129
5	Memristor-based multilevel memory. , 2010, , .		86
6	A memristor emulator as a replacement of a real memristor. Semiconductor Science and Technology, 2015, 30, 015007.	2.0	52
7	Mutator-Based Meminductor Emulator for Circuit Applications. Circuits, Systems, and Signal Processing, 2014, 33, 2363-2383.	2.0	44
8	Memristive Imitation of Synaptic Transmission and Plasticity. IEEE Transactions on Neural Networks and Learning Systems, 2019, 30, 3458-3470.	11.3	41
9	Oscillation with 4-Lobe Chua Corsage Memristor. IEEE Circuits and Systems Magazine, 2018, 18, 14-27.	2.3	33
10	EXPANDABLE CIRCUITS OF MUTATOR-BASED MEMCAPACITOR EMULATOR. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2013, 23, 1330017.	1.7	26
11	Transient Behaviors of Multiple Memristor Circuits Based on Flux Charge Relationship. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2014, 24, 1430006.	1.7	20
12	A Circuit-Based Neural Network with Hybrid Learning of Backpropagation and Random Weight Change Algorithms. Sensors, 2017, 17, 16.	3.8	19
13	Exact Analysis and Physical Realization of the 6-Lobe Chua Corsage Memristor. Complexity, 2018, 2018, 1-21.	1.6	16
14	Accurate Natural Trail Detection Using a Combination of a Deep Neural Network and Dynamic Programming. Sensors, 2018, 18, 178.	3.8	13
15	HIGHLY ACCURATE DOUBLET GENERATOR FOR MEMRISTOR-BASED ANALOG MEMORY. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2012, 22, 1250153.	1.7	10
16	On Learning With Nonlinear Memristor-Based Neural Network and its Replication. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3906-3916.	5.4	10
17	Memristor circuit for artificial synaptic weighting of pulse inputs. , 2012, , .		9
18	Memristor bridge circuit for neural synaptic weighting. , 2012, , .		9

#	ARTICLE	IF	CITATIONS
19	Excitatory and inhibitory actions of a memristor bridge synapse. Science China Information Sciences, 2018, 61, 1.	4.3	7
20	Design of a Low-Frequency Oscillator with PTC Memristor and an Inductor. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2016, 26, 1630021.	1.7	6
21	Precise Modeling of the Protective Effects of Quercetin against Mycotoxin via System Identification with Neural Networks. International Journal of Molecular Sciences, 2019, 20, 1725.	4.1	4
22	Features of memristor emulator-based artificial neural synapses. , 2013, , .		3
23	Learning with memristor bridge synapse-based neural networks. , 2014, , .		2
24	Linearized Programming of Memristors for Artificial Neuro-Sensor Signal Processing. Sensors, 2016, 16, 1320.	3.8	2
25	Memristor emulator with off-the-shelf solid state components for memristor application circuits. , 2012, , .		1
26	Operational characteristics of multi-memristor circuits. , 2014, , .		1
27	Linear programming of voltage-controlled memristors with an anti-serial memristor circuit. , 2015, , .		1
28	Circular-buffered architecture for Cellular Neural Networks-based analog Viterbi decoder. , 2010, , .		0