

Paul Rigge

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/9527272/publications.pdf>

Version: 2024-02-01

6
papers

51
citations

2258059

3
h-index

2550090

3
g-index

6
all docs

6
docs citations

6
times ranked

72
citing authors

#	ARTICLE	IF	CITATIONS
1	Invited: Chipyard - An Integrated SoC Research and Implementation Environment. , 2020, , .		4
2	A Mixed-Signal RISC-V Signal Analysis SoC Generator With a 16-nm FinFET Instance. IEEE Journal of Solid-State Circuits, 2019, 54, 2786-2801.	5.4	11
3	A Real-Time, 1.89-GHz Bandwidth, 175-kHz Resolution Sparse Spectral Analysis RISC-V SoC in 16-nm FinFET. IEEE Journal of Solid-State Circuits, 2019, 54, 1993-2008.	5.4	5
4	Wireless Channel Dynamics and Robustness for Ultra-Reliable Low-Latency Communications. IEEE Journal on Selected Areas in Communications, 2019, 37, 705-720.	14.0	23
5	A Real-Time, Analog/Digital Co-Designed 1.89-GHz Bandwidth, 175-kHz Resolution Sparse Spectral Analysis RISC-V SoC in 16-nm FinFET. , 2018, , .		4
6	ACED. , 2018, , .		4