Martti Forsell

List of Publications by Year
in descending order

14 papers	72 citations	1684188 5 h-index	1720034 7 g-index
15 all docs	15 docs citations	15 times ranked	13 citing authors

#	Article	IF	Citations
1	Performance and programmability comparison of the thick control flow architecture and current multicore processors. Journal of Supercomputing, 2022, 78, 3152-3183.	3.6	5
2	REPLICA MBTAC: multithreaded dual-mode processor. Journal of Supercomputing, 2018, 74, 1911-1933.	3.6	3
3	Supporting concurrent memory access in TCF processor architectures. Microprocessors and Microsystems, 2018, 63, 226-236.	2.8	2
4	Implementation of Multioperations in Thick Control Flow Processors. , 2018, , .		2
5	Supporting concurrent memory access in TCF-aware processor architectures. , 2017, , .		2
6	Towards a language framework for thick control flows. , 2017, , .		4
7	The REPLICA on-chip network. , 2016, , .		2
8	Outline of a Thick Control Flow Architecture. , 2016, , .		8
9	Prototyping the MBTAC Processor for the REPLICA CMP. , 2014, , .		5
10	An Extended PRAM-NUMA Model of Computation for TCF Programming. International Journal of Networking and Computing, 2013, 3, 98-115.	0.4	7
11	Performance comparison of some shared memory organizations for 2D mesh-like NOCs. Microprocessors and Microsystems, 2011, 35, 274-284.	2.8	6
12	A PRAM-NUMA Model of Computation for Addressing Low-TLP Workloads. International Journal of Networking and Computing, 2011, 1, 21-35.	0.4	8
13	ON THE PERFORMANCE AND COST OF SOME PRAM MODELS ON CMP HARDWARE. International Journal of Foundations of Computer Science, 2010, 21, 387-404.	1.1	8
14	Realising constant time parallel algorithms with active memory modules. International Journal of Electronic Business, 2005, 3, 255.	0.4	9