

# Martti Forsell

## List of Publications by Year in descending order

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Version: 2024-02-01

14  
papers

72  
citations

1684188  
5  
h-index

1720034  
7  
g-index

15  
all docs

15  
docs citations

15  
times ranked

13  
citing authors

#	ARTICLE	IF	CITATIONS
1	Realising constant time parallel algorithms with active memory modules. International Journal of Electronic Business, 2005, 3, 255.	0.4	9
2	ON THE PERFORMANCE AND COST OF SOME PRAM MODELS ON CMP HARDWARE. International Journal of Foundations of Computer Science, 2010, 21, 387-404.	1.1	8
3	Outline of a Thick Control Flow Architecture. , 2016, , .		8
4	A PRAM-NUMA Model of Computation for Addressing Low-TLP Workloads. International Journal of Networking and Computing, 2011, 1, 21-35.	0.4	8
5	An Extended PRAM-NUMA Model of Computation for TCF Programming. International Journal of Networking and Computing, 2013, 3, 98-115.	0.4	7
6	Performance comparison of some shared memory organizations for 2D mesh-like NOCs. Microprocessors and Microsystems, 2011, 35, 274-284.	2.8	6
7	Prototyping the MBTAC Processor for the REPLICA CMP. , 2014, , .		5
8	Performance and programmability comparison of the thick control flow architecture and current multicore processors. Journal of Supercomputing, 2022, 78, 3152-3183.	3.6	5
9	Towards a language framework for thick control flows. , 2017, , .		4
10	REPLICA MBTAC: multithreaded dual-mode processor. Journal of Supercomputing, 2018, 74, 1911-1933.	3.6	3
11	The REPLICA on-chip network. , 2016, , .		2
12	Supporting concurrent memory access in TCF-aware processor architectures. , 2017, , .		2
13	Supporting concurrent memory access in TCF processor architectures. Microprocessors and Microsystems, 2018, 63, 226-236.	2.8	2
14	Implementation of Multioperations in Thick Control Flow Processors. , 2018, , .		2