

# Guangyin Feng

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/9500906/publications.pdf>

Version: 2024-02-01

32  
papers

405  
citations

840119

11  
h-index

794141

19  
g-index

32  
all docs

32  
docs citations

32  
times ranked

427  
citing authors

#	ARTICLE	IF	CITATIONS
1	An Injection-Locked Wireless Power Transfer Transmitter With Automatic Maximum Efficiency Tracking. IEEE Transactions on Industrial Electronics, 2021, 68, 5733-5743.	5.2	8
2	A Parallel Sliding-IF Receiver Front-End With Sub-2-dB Noise Figure for 5â€“6-GHz WLAN Carrier Aggregation. IEEE Journal of Solid-State Circuits, 2021, 56, 392-403.	3.5	4
3	Bandwidth Extension Techniques for CMOS Low-Noise Amplifiers. , 2021, , .		3
4	A Carrier Aggregation Transmitter Front End for 5-GHz WLAN 802.11ax Application in 40-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 264-276.	2.9	14
5	A D-band CMOS power amplifier for short-range data center communication. IEICE Electronics Express, 2020, 17, 20200159-20200159.	0.3	5
6	A 93.4â€“104.8-GHz 57-mW Fractional- $\{N\}$ Cascaded PLL With True In-Phase Injection-Coupled QVCO in 65-nm CMOS Technology. IEEE Transactions on Microwave Theory and Techniques, 2019, 67, 2370-2381.	2.9	20
7	A 24/77 GHz Dual-Band Receiver for Automotive Radar Applications. IEEE Access, 2019, 7, 48053-48059.	2.6	18
8	Tunable Surface-Plasmon-Polariton Filter Constructed By Corrugated Metallic Line and High Permittivity Material. IEEE Access, 2018, 6, 10358-10364.	2.6	14
9	Injection-Locked Power Oscillator for Resonance Frequency Tracking in Wireless Power Transfer. , 2018, , .		2
10	A D-Band Amplifier in 65 nm Bulk CMOS for Short-Distance Data Center Communication. IEEE Access, 2018, 6, 53191-53200.	2.6	8
11	A broadband CMOS amplifier in D band using pole-tuning technique with T-type network. , 2018, , .		1
12	A W-Band Switch-Less Dicke Receiver for Millimeter-Wave Imaging in 65 nm CMOS. IEEE Access, 2018, 6, 39233-39240.	2.6	9
13	Pole-Converging Intrastage Bandwidth Extension Technique for Wideband Amplifiers. IEEE Journal of Solid-State Circuits, 2017, 52, 769-780.	3.5	75
14	An Eight-Phase In-Phase Injection-Coupled VCO in 65-nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2017, 27, 299-301.	2.0	3
15	An Energy-Efficient and Low-Crosstalk Sub-THz I/O by Surface Plasmonic Polariton Interconnect in CMOS. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 2762-2774.	2.9	43
16	A label-free and non-invasive CMOS sub-THz plasmonic sensor for circulating tumor cell detection. , 2017, , .		2
17	A 35 GHz composite right/left hand T-line based continuous tunable phase shifter in 65 nm CMOS. , 2017, , .		0
18	A sub-terahertz multi-pixel imaging system with surface wave resonator for isolation. , 2017, , .		0

#	ARTICLE	IF	CITATIONS
19	A CMOS sub-terahertz full-duplex phased-array transceiver for short-distance data center communication. , 2017, , .		0
20	An efficient 4-way-combined 291 GHz signal source with 1.75 mW peak output power in 65 nm CMOS. , 2017, , .		2
21	A D-band SPST switch using parallel-stripline swap with defected ground structure. IEICE Electronics Express, 2017, 14, 20171104-20171104.	0.3	6
22	A 100-GHz 0.21-K NETD 0.9-mW/pixel Charge-Accumulation Super-Regenerative Receiver in 65-nm CMOS. IEEE Microwave and Wireless Components Letters, 2016, 26, 531-533.	2.0	11
23	A 93.4-to-104.8 GHz 57 mW fractional-N cascaded sub-sampling PLL with true in-phase injection-coupled QVCO in 65 nm CMOS. , 2016, , .		9
24	An Energy-efficient Adaptive Sub-THz Wireless Interconnect with MIMO-Beamforming between Cores and DRAMs. , 2016, , .		2
25	A 65nm CMOS carrier-aggregation transceiver for IEEE 802.11 WLAN applications. , 2016, , .		11
26	A Compact 57-67 GHz Bidirectional LNAPA in 65-nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2016, 26, 628-630.	2.0	25
27	An Energy Efficient CMOS Sub-THz Interconnect with Surface Plasmonic Converter and Oscillator. , 2016, , .		2
28	A 65-nm CMOS LNA for Bolometer Application. Journal of Infrared, Millimeter, and Terahertz Waves, 2016, 37, 356-372.	1.2	9
29	An 88.5-110 GHz CMOS Low-Noise Amplifier for Millimeter-Wave Imaging Applications. IEEE Microwave and Wireless Components Letters, 2016, 26, 134-136.	2.0	65
30	A CMOS W-Band $4 \times 4$ Quasi-Subharmonic Mixer. IEEE Microwave and Wireless Components Letters, 2015, 25, 385-387.	2.0	22
31	A 50-59 GHz CMOS Injection Locking Power Amplifier. IEEE Microwave and Wireless Components Letters, 2015, 25, 52-54.	2.0	11
32	Design of a fully integrated CMOS dual K- and W-band lumped wilkinson power divider. , 2013, , .		1