

Mst Shamim Ara Shawkat

List of Publications by Year in descending order

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Version: 2024-02-01

19
papers

141
citations

1478505

6
h-index

1588992

8
g-index

19
all docs

19
docs citations

19
times ranked

119
citing authors

#	ARTICLE	IF	CITATIONS
1	Minimizing the Energy Hole Problem in Wireless Sensor Networks: A Wedge Merging Approach. Sensors, 2020, 20, 277.	3.8	32
2	A priority based energy harvesting scheme for charging embedded sensor nodes in wireless body area networks. PLoS ONE, 2019, 14, e0214716.	2.5	28
3	An Analog CMOS Silicon Photomultiplier Using Perimeter-Gated Single-Photon Avalanche Diodes. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 3830-3841.	5.4	20
4	A Digital CMOS Silicon Photomultiplier Using Perimeter Gated Single Photon Avalanche Diodes With Asynchronous AER Readout. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4818-4828.	5.4	12
5	A system design perspective on neuromorphic computer processors. Neuromorphic Computing and Engineering, 2021, 1, 022001.	5.9	8
6	A differential ISFET pH sensor. , 2017, , .		7
7	Design of a Robust Memristive Spiking Neuromorphic System with Unsupervised Learning in Hardware. ACM Journal on Emerging Technologies in Computing Systems, 2021, 17, 1-26.	2.3	7
8	A tunable single photon avalanche diode pixel with improved time resolution. , 2017, , .		6
9	A CMOS Perimeter Gated SPAD Based Digital Silicon Photomultiplier with Asynchronous AER Readout for PET Applications. , 2018, , .		6
10	Modeling and Simulation of PGSPAD-Based Silicon Photomultipliers. IEEE Transactions on Nuclear Science, 2021, 68, 279-291.	2.0	4
11	Macromodel of G^4 FET Enabling Fast and Reliable SPICE Simulation for Innovative Circuit Applications. International Journal of High Speed Electronics and Systems, 2018, 27, 1840015.	0.7	3
12	Fundamentals of Perimeter Gated Single Photon Avalanche Diodes used in Silicon Photomultipliers for Nuclear Imaging. , 2019, , .		3
13	Single Photon Avalanche Diode based Vision Sensor with On-Chip Memristive Spiking Neuromorphic Processing. , 2020, , .		3
14	A Differential Low-Power Voltage-Clamped ISFET Topology for Biomedical Applications. , 2018, , .		1
15	Modeling of Silicon Photomultiplier Based on Perimeter Gated Single Photon Avalanche Diode. , 2019, , .		1
16	Perimeter Gated Single Photon Avalanche Diodes in Sub-Micron and Deep-Submicron CMOS Processes. International Journal of High Speed Electronics and Systems, 2018, 27, 1840018.	0.7	0
17	Modeling Emerging Semiconductor Devices for Circuit Simulation. , 2020, , .		0
18	Macromodel of G^4 FET Enabling Fast and Reliable SPICE Simulation for Innovative Circuit Applications. Selected Topics in Electronics and Systems, 2019, , 17-36.	0.2	0

#	ARTICLE	IF	CITATIONS
19	Perimeter Gated Single Photon Avalanche Diodes in Sub-Micron and Deep-Submicron CMOS Processes. Selected Topics in Electornics and Systems, 2019, , 71-89.	0.2	0