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List of Publications by Year in descending order

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| # | ARTICLE | IF | CITATIONS |
|----|---|-----|-----------|
| 1 | Methodology for the prediction of paroxysmal atrial fibrillation based on heart rate variability feature analysis. Heliyon, 2021, 7, e08244. | 3.2 | 9 |
| 2 | Adaptive Algorithm based on Renyi's Entropy for Task Mapping in a Hierarchical Wireless Network-on-Chip Architecture. Computacion Y Sistemas, 2018, 22, . | 0.3 | 1 |
| 3 | Performance evaluation of M-ary algorithm using reprogrammable hardware. DYNA (Colombia), 2017, 84, 75-79. | 0.4 | 0 |
| 4 | Genetic algorithm for task mapping in embedded systems on a hierarchical architecture based on wireless network on chip WiNoC. DYNA (Colombia), 2017, 84, 202. | 0.4 | 5 |
| 5 | Implementaci3n en FPGA del algoritmo AES-128 en modos de operaci3n no realimentados. DYNA (Colombia), 2016, 83, 37-43. | 0.4 | 2 |
| 6 | Implementaci3n en FPGA del algoritmo AES-128 en modos de operaci3n no realimentados. DYNA (Colombia), 2016, 83, 37. | 0.4 | 0 |
| 7 | Learning manager for remote experimentation with optoelectronic devices. DYNA (Colombia), 2015, 82, 23-31. | 0.4 | 0 |
| 8 | Integraci3n de gestores de aprendizaje y contenidos Web en la ense1anza de los sistemas digitales: Integrating learning and Web-Content Management Systems on digital systems teaching. , 2012, , . | | 1 |
| 9 | Estado del arte en las metodolog1as de dise1o de circuitos digitales as1ncronos. Ingenieria Y Competitividad, 2011, 7, 84-91. | 0.1 | 0 |
| 10 | Implementaci3n de un Hardware econfigurable de los Bloques de un Sistema RSA. Ingenieria Y Competitividad, 2011, 6, 25. | 0.1 | 0 |