

# Alvaro Bernal Noreña

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/9463384/publications.pdf>

Version: 2024-02-01

10

papers

18

citations

2682572

2

h-index

2272923

4

g-index

10

all docs

10

docs citations

10

times ranked

19

citing authors

#	ARTICLE	IF	CITATIONS
1	Methodology for the prediction of paroxysmal atrial fibrillation based on heart rate variability feature analysis. <i>Heliyon</i> , 2021, 7, e08244.	3.2	9
2	Genetic algorithm for task mapping in embedded systems on a hierarchical architecture based on wireless network on chip WiNoC. <i>DYNA (Colombia)</i> , 2017, 84, 202.	0.4	5
3	ImplementaciÃ³n en FPGA del algoritmo AES-128 en modos de operaciÃ³n no realimentados. <i>DYNA (Colombia)</i> , 2016, 83, 37-43.	0.4	2
4	IntegraciÃ³n de gestores de aprendizaje y contenidos Web en la enseÃ±anza de los sistemas digitales: Integrating learning and Web-Content Management Systems on digital systems teaching. , 2012,,.	1	
5	Adaptive Algorithm based on Renyiâ€™s Entropy for Task Mapping in a Hierarchical Wireless Network-on-Chip Architecture. <i>Computacion Y Sistemas</i> , 2018, 22, .	0.3	1
6	Performance evaluation of M-ary algorithm using reprogrammable hardware. <i>DYNA (Colombia)</i> , 2017, 84, 75-79.	0.4	0
7	Estado del arte en las metodologÃas de diseÃ±o de circuitos digitales asÃncronos. <i>Ingenieria Y Competitividad</i> , 2011, 7, 84-91.	0.1	0
8	ImplementaciÃ³n de un Hardware econfigurable de los Bloques de un Sistema RSA. <i>Ingenieria Y Competitividad</i> , 2011, 6, 25.	0.1	0
9	Learning manager for remote experimentation with optoelectronic devices. <i>DYNA (Colombia)</i> , 2015, 82, 23-31.	0.4	0
10	ImplementaciÃ³n en FPGA del algoritmo AES-128 en modos de operaciÃ³n no realimentados. <i>DYNA (Colombia)</i> , 2016, 83, 37.	0.4	0