

# Shih-Hsiung Chien

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/9441718/publications.pdf>

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#	ARTICLE	IF	CITATIONS
1	A Monolithic Capacitor-Current-Controlled Hysteretic Buck Converter With Transient-Optimized Feedback Circuit. IEEE Journal of Solid-State Circuits, 2015, 50, 2524-2532.	5.4	26
2	A 100 W 5.1-Channel Digital Class-D Audio Amplifier With Single-Chip Design. IEEE Journal of Solid-State Circuits, 2012, 47, 1344-1354.	5.4	23
3	A Low Quiescent Current, Low THD+N Class-D Audio Amplifier With Area-Efficient PWM-Residual-Aliasing Reduction. IEEE Journal of Solid-State Circuits, 2018, 53, 3377-3385.	5.4	11
4	A 2.4 mA Quiescent Current, 1 W Output Power Class-D Audio Amplifier With Feed-Forward PWM-Intermodulated-Distortion Reduction. IEEE Journal of Solid-State Circuits, 2016, 51, 1436-1445.	5.4	8
5	Optimal Design for Delta- $\Sigma$ Modulators With Root Loci Inside Unit Circle. IEEE Transactions on Circuits and Systems II: Express Briefs, 2012, 59, 83-87.	3.0	3
6	A monolithic capacitor-current-controlled hysteretic buck converter with transient-optimized feedback circuit. , 2014, , .		3
7	23.5 A 0.41mA Quiescent Current, 0.00091% THD+N Class-D Audio Amplifier with Frequency Equalization for PWM-Residual-Aliasing Reduction. , 2020, , .		2
8	A 0.4-mA-Quiescent-Current, 0.00091%-THD+N Class-D Audio Amplifier With Low-Complexity Frequency Equalization for PWM-Residual- Aliasing Reduction. IEEE Journal of Solid-State Circuits, 2022, 57, 423-433.	5.4	2