

Jos Huisken

List of Publications by Year in descending order

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Version: 2024-02-01

24
papers

557
citations

1478505

6
h-index

1372567

10
g-index

24
all docs

24
docs citations

24
times ranked

698
citing authors

| # | ARTICLE | IF | CITATIONS |
|----|--|-----|-----------|
| 1 | Dilate-Invariant Temporal Convolutional Network for Real-Time Edge Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 1210-1220. | 5.4 | 0 |
| 2 | Reducing Library Characterization Time for Cell-aware Test while Maintaining Test Quality. Journal of Electronic Testing: Theory and Applications (JETTA), 2021, 37, 161-189. | 1.2 | 0 |
| 3 | Converter-Free Power Delivery Using Voltage Stacking for Near/Subthreshold Operation. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 1039-1051. | 3.1 | 3 |
| 4 | Multi-Level Optimization of an Ultra-Low Power BrainWave System for Non-Convulsive Seizure Detection. IEEE Transactions on Biomedical Circuits and Systems, 2021, 15, 1107-1121. | 4.0 | 3 |
| 5 | Tightening the Mesh Size of the Cell-Aware ATPG Net for Catching All Detectable Weakest Faults. , 2020, , . | | 3 |
| 6 | Low Complexity Multi-directional In-Air Ultrasonic Gesture Recognition Using a TCN. , 2020, , . | | 7 |
| 7 | An Automated Approximation Methodology for Arithmetic Circuits. , 2019, , . | | 8 |
| 8 | Voltage Stacked Design of a Microcontroller for Near/Sub-threshold Operation. , 2019, , . | | 3 |
| 9 | Application of Cell-Aware Test on an Advanced 3nm CMOS Technology Library. , 2019, , . | | 8 |
| 10 | Blocks: Redesigning Coarse Grained Reconfigurable Architectures for Energy Efficiency. , 2019, , . | | 10 |
| 11 | Keyword Spotting using Time-Domain Features in a Temporal Convolutional Network. , 2019, , . | | 8 |
| 12 | Designing Energy Efficient Approximate Multipliers for Neural Acceleration. , 2018, , . | | 8 |
| 13 | A Configurable and Low-Power Mixed Signal SoC for Portable ECG Monitoring Applications. IEEE Transactions on Biomedical Circuits and Systems, 2014, 8, 257-267. | 4.0 | 214 |
| 14 | Ultra Low-Energy SRAM Design for Smart Ubiquitous Sensors. IEEE Micro, 2012, 32, 10-24. | 1.8 | 15 |
| 15 | Ultra low power litho friendly local assist circuitry for variability resilient 8T SRAM. , 2012, , . | | 3 |
| 16 | 8T SRAM with Mimicked Negative Bit-lines and Charge Limited Sequential sense amplifier for wireless sensor nodes. , 2011, , . | | 11 |
| 17 | A 4.4 pJ/Access 80 MHz, 128 kbit Variability Resilient SRAM With Multi-Sized Sense Amplifier Redundancy. IEEE Journal of Solid-State Circuits, 2011, 46, 2416-2430. | 5.4 | 26 |
| 18 | A 2.4 GHz ULP OOK Single-Chip Transceiver for Healthcare Applications. IEEE Transactions on Biomedical Circuits and Systems, 2011, 5, 523-534. | 4.0 | 95 |

| # | ARTICLE | IF | CITATIONS |
|----|---|-----|-----------|
| 19 | An Ultra Low Energy Biomedical Signal Processing System Operating at Near-Threshold. IEEE Transactions on Biomedical Circuits and Systems, 2011, 5, 546-554. | 4.0 | 34 |
| 20 | A Lightweight Security Scheme for Wireless Body Area Networks: Design, Energy Evaluation and Proposed Microprocessor Design. Journal of Medical Systems, 2011, 35, 1289-1298. | 3.6 | 41 |
| 21 | Sub-threshold synchronizer. Microelectronics Journal, 2011, 42, 840-850. | 2.0 | 6 |
| 22 | Evaluation of 90nm 6T-SRAM as Physical Unclonable Function for secure key generation in wireless sensor nodes. , 2011, , . | | 39 |
| 23 | Extending Synchronization from Super-Threshold to Sub-threshold Region. , 2010, , . | | 9 |
| 24 | Design of 100 μ W Wireless Sensor Nodes on Energy Scavengers for Biomedical Monitoring. Lecture Notes in Computer Science, 2007, , 385-395. | 1.3 | 3 |