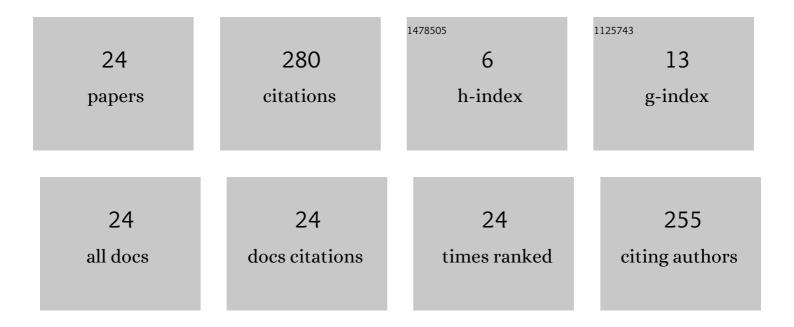
Rodrigo Loera-Palomo

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	Evaluation of SnS:Cu Thin Film Properties Obtained by USP Technique to Implement It as an Absorbent Layer in Solar Cells Using SCAPS. Coatings, 2021, 11, 754.	2.6	7
2	Noncascading Quadratic Buck-Boost Converter for Photovoltaic Applications. Micromachines, 2021, 12, 984.	2.9	6
3	Averaged current mode control for maximum power point tracking in high-gain photovoltaic applications. Journal of Power Electronics, 2020, 20, 1650-1661.	1.5	6
4	Quadratic Buck/Boost Converter in Series Connection for Photovoltaic Applications. , 2020, , .		1
5	Effect of Asynchronous Data Processing on Solar Irradiance and Clearness Index Estimation by Sky Imagery. Applied Solar Energy (English Translation of Geliotekhnika), 2020, 56, 508-516.	1.6	2
6	Noncascading quadratic boost converter for PV applications. , 2018, , .		2
7	Solar irradiance estimation based on image analysis. , 2018, , .		3
8	Wind park electric power estimation based on the Jensen wake model. , 2018, , .		2
9	A new methodology to extend the validity of the Hargreaves-Samani model to estimate global solar radiation in different climates: Case study Mexico. Renewable Energy, 2017, 114, 1340-1352.	8.9	17
10	Switching regulator using a DC–DC stepâ€down nonâ€cascading converter. IET Power Electronics, 2017, 10, 413-420.	2.1	1
11	A maximum power point control scheme applied to a noncascading dc-dc converter for a PV system. , 2017, , .		Ο
12	Three-phase converter based on reduced redundant power processing concept. , 2017, , .		2
13	Maximum power point tracker based on PFC control scheme. , 2016, , .		Ο
14	Optimized design of a quadratic boost converter based on the R2P2principle. , 2016, , .		0
15	Design methodology for quadratic step-up dc-dc converters based on non-cascading structures. , 2016, , .		4
16	Design methodology for quadratic step-down dc-dc converters based on non-cascading structures. , 2016, , .		0
17	Family of quadratic stepâ€up dc–dc converters based on nonâ€cascading structures. IET Power Electronics, 2015, 8, 793-801.	2.1	55
18	Parameters selection criteria of proportional–integral controller for a quadratic buck converter. IET Power Electronics, 2014, 7, 1527-1535.	2.1	14

#	Article	IF	CITATIONS
19	Modelling and control of a DC–DC quadratic boost converter with <i>R</i> ² <i>P</i> ² . IET Power Electronics, 2014, 7, 11-22.	2.1	81
20	Quadratic stepâ€down dc–dc converters based on reduced redundant power processing approach. IET Power Electronics, 2013, 6, 136-145.	2.1	48
21	Signal flow graphs for modelling of switching converters with reduced redundant power processing. IET Power Electronics, 2012, 5, 1008-1016.	2.1	15
22	Analysis of Quadratic Step-Down DC-DC Converters Based on Noncascading Structures. , 2012, , .		9
23	Controller design for a power factor correction regulator R2P2. IET Power Electronics, 2010, 3, 784.	2.1	4
24	Modeling and control of a PFC regulator with reduced redundant power processing. Power Electronics Specialist Conference (PESC), IEEE, 2008, , .	0.0	1