

Emad Hegazi

List of Publications by Year in descending order

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40
papers

1,416
citations

1040056

9
h-index

1058476

14
g-index

40
all docs

40
docs citations

40
times ranked

801
citing authors

#	ARTICLE	IF	CITATIONS
1	SALAH: Simulation-Assisted LAYout Hierarchy Construction. , 2021, , .		0
2	A low power Charge Steering Based Frequency Divider. , 2020, , .		1
3	ANtarticica: ANalog Thermal Aware, with Reduced Constraint, Technique for Checking & Analysis. , 2020, , .		0
4	A PFM-regulated switched-capacitor DC-DC converter with enhanced-ripples technique. , 2017, , .		2
5	A battery-connected all-digital capacitive DC-DC converter with load tracking controller. , 2017, , .		2
6	Electronic frequency compensation of AlN-on-Si MEMS reference oscillators. Microelectronics Journal, 2016, 54, 72-84.	2.0	3
7	On the use of a programmable front-end for multi-band/multi-standard applications. Microelectronics Journal, 2016, 49, 1-9.	2.0	0
8	A low voltage low power and high speed binary search analog to digital converter. , 2015, , .		1
9	A tunable receiver architecture utilizing time-varying matching network for a universal receiver. , 2015, , .		0
10	A 76.8â€™ MHz temperature compensated MEMS reference oscillator for wireless handsets. Microelectronics Journal, 2015, 46, 496-505.	2.0	7
11	A second-order noise-shaping time-to-digital converter using switched-ring oscillator. , 2015, , .		4
12	Charge-Pump Folded Noise Cancelation in Fractional-N Phase-Locked Loops. IEEE Transactions on Circuits and Systems II: Express Briefs, 2014, 61, 378-382.	3.0	11
13	D4. Dynamic Supply RF Power Amplifier. , 2013, , .		0
14	An All-Analog Method to Enhance Amplitude Stability in Pierce Crystal Oscillators. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 463-470.	5.4	3
15	A low-jitter video clock recovery circuit. , 2010, , .		0
16	Digital enhancement of frequency synthesizers. , 2010, , .		0
17	Analytical design of circular CMUT cells in immersion. , 2010, , .		2
18	A low-voltage CMOS bandgap reference circuit with improved power supply rejection. , 2010, , .		13

#	ARTICLE	IF	CITATIONS
19	A top-down approach for substrate noise assessment flow in mixed-signal and SOC designs. , 2010, , .		0
20	A fast automatic tuning algorithm for Voltage Controlled Oscillators. , 2010, , .		0
21	Model order reduction of on-chip interconnects. , 2009, , .		0
22	A technique for truly linear LC VCO tuning, a proof of concept. Microelectronics Journal, 2009, 40, 1541-1546.	2.0	2
23	Introduction to the special issue on the 2007 International Conference on Microelectronics. Microelectronics Journal, 2009, 40, 1503.	2.0	0
24	Digital on-chip phase noise measurement. , 2009, , .		12
25	RF MEMS resonators: Material properties extraction. , 2009, , .		0
26	Design of low noise transimpedance amplifier for Intravascular Ultrasound. , 2009, , .		4
27	Model order reduction of on-chip interconnects. , 2009, , .		0
28	A Third-Order 9-Bit 10-MHz CMOS Δ Sigma Modulator With One Active Stage. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 2469-2482.	5.4	25
29	An Algorithm for Automatic Tuning of PLLs. , 2007, , .		2
30	On the Design of Error Cancellation Network for MASH $\hat{\Sigma}^2$ -Frequency discriminators. , 2007, , .		0
31	A Low-Power Wideband CMOS LNA for WiMAX. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2007, 54, 4-8.	2.2	88
32	A 90-nm Wideband Merged CMOS LNA and Mixer Exploiting Noise Cancellation. IEEE Journal of Solid-State Circuits, 2007, 42, 323-328.	5.4	71
33	A technique for truly linear LC VCO tuning, a proof of concept. , 2007, , .		5
34	A DLL-Biased, 14-Bit DS Analog-to-Digital Converter for GSM/GPRS/EDGE Handsets. IEEE Journal of Solid-State Circuits, 2006, 41, 330-338.	5.4	6
35	A Multigigahertz Multimodulus Frequency Divider in 90-nm CMOS. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2006, 53, 1333-1337.	2.2	14
36	A MEMS Disk Resonator-Based Oscillator. , 2006, , .		0

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37	A 17-mW transmitter and frequency synthesizer for 900-MHz GSM fully integrated in 0.35- μ m CMOS. IEEE Journal of Solid-State Circuits, 2003, 38, 782-792.	5.4	60
38	Varactor characteristics, oscillator tuning curves, and am-fm conversion. IEEE Journal of Solid-State Circuits, 2003, 38, 1033-1039.	5.4	185
39	A filtering technique to lower LC oscillator phase noise. IEEE Journal of Solid-State Circuits, 2001, 36, 1921-1930.	5.4	834
40	A 900-MHz dual-conversion low-IF GSM receiver in 0.35- μ m CMOS. IEEE Journal of Solid-State Circuits, 2001, 36, 1992-2002.	5.4	59