

Abumoslem Jannesari

List of Publications by Year in descending order

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citing authors

#	ARTICLE	IF	CITATIONS
1	Joint digital pre-distortion model based on Chebyshev expansion. International Journal of Electrical and Computer Engineering, 2022, 12, 3781.	0.5	0
2	A Higher-Order Highly Linear N-Path Band-Pass Filter. Circuits, Systems, and Signal Processing, 2021, 40, 50-69.	1.2	3
3	High-Q, High-Rejection Ratio Complex Second-Order Charge-Sampling Switched gm-C Semi-Passive Band-Pass Filter. Journal of Circuits, Systems and Computers, 2021, 30, 2150192.	1.0	0
4	Wideband Inductorless True Time Delay Cell Based on CMOS Inverter for Timed Array Receivers. Circuits, Systems, and Signal Processing, 2021, 40, 3703-3726.	1.2	1
5	A 280mv Input Fast Transient Startup Charge Pump with a 4 Phase Ring Oscillator for Energy Harvesting Applications. , 2021, , .		1
6	Fast-Transient-Response Low-Voltage Integrated, Interleaved DC-DC Converter for Implantable Devices. Journal of Circuits, Systems and Computers, 2020, 29, 2050013.	1.0	0
7	Cell Weighting and Gate Inductive Peaking Techniques for Wideband Noise Suppression in Distributed Amplifiers. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4507-4520.	3.5	0
8	Charge-sharing bandpass filter with independent bandwidth and centre frequency adjustment. Electronics Letters, 2019, 55, 638-640.	0.5	2
9	A parasitic insensitive passive switched-capacitor interpolation finite impulse response filter. International Journal of Circuit Theory and Applications, 2019, 47, 1736-1761.	1.3	0
10	Noise shaping in low noise amplifiers using active feedback and pole-zero adjustment. Microelectronics Journal, 2019, 89, 1-15.	1.1	1
11	Design of a Highly Linear Gain Stage with Complementary Derivative Superposition Technique. Wireless Personal Communications, 2019, 107, 1709-1716.	1.8	1
12	High dynamic range pseudo-two-level digital pulse-width modulation for power-efficient RF transmitters. International Journal of Circuit Theory and Applications, 2019, 47, 65-86.	1.3	3
13	Power-efficient burst-mode RF transmitter based on reference-adaptive multilevel pulse-width modulation. International Journal of Circuit Theory and Applications, 2018, 46, 427-452.	1.3	6
14	Noise suppression in a common-gate UWB LNA with an inductor resonating at the source node. AEU - International Journal of Electronics and Communications, 2018, 96, 144-153.	1.7	7
15	Pre-distortion technique to improve linearity of low noise amplifier. Microelectronics Journal, 2017, 61, 95-105.	1.1	26
16	Harmonic fold back reduction at the N-path filters. International Journal of Circuit Theory and Applications, 2017, 45, 419-438.	1.3	10
17	A Low-Power Three-Tap DFE with Switched Resistor Slicer and CTLE in 0.18 μ m CMOS Technology. Journal of Circuits, Systems and Computers, 2017, 26, 1750199.	1.0	1
18	A linear ultra wide band low noise amplifier using pre-distortion technique. AEU - International Journal of Electronics and Communications, 2017, 79, 172-183.	1.7	22

#	ARTICLE	IF	CITATIONS
19	A new approach to frequency-domain noise analysis and design of a very-low noise amplifier in radio and microwave frequencies. <i>Microelectronics Journal</i> , 2017, 68, 14-22.	1.1	5
20	A sub-2-dB noise figure linear wideband low noise amplifier in 0.18 μm CMOS. <i>Microelectronics Journal</i> , 2017, 67, 135-142.	1.1	11
21	Analysis and design of discrete-time charge domain filters with complex conjugate poles. <i>International Journal of Circuit Theory and Applications</i> , 2017, 45, 530-549.	1.3	2
22	Second-order charge sampling structure utilising passive scheme to implement complex conjugate poles. <i>Electronics Letters</i> , 2016, 52, 1015-1016.	0.5	1
23	A 2-GHz ROM-less direct digital frequency synthesizer based on an analog sine-mapper circuit. , 2016, , .		1
24	A 670 $\frac{1}{4}$ W inductorless low noise amplifier employing dual capacitive cross coupling and dual negative feedback. , 2016, , .		3
25	Low-phase-noise CMOS VCO with new drain-gate feedback path. <i>Analog Integrated Circuits and Signal Processing</i> , 2016, 88, 89-95.	0.9	2
26	Design of a High-Frequency Very Low-Power Direct Digital Frequency Synthesizer. <i>Journal of Circuits, Systems and Computers</i> , 2016, 25, 1650085.	1.0	2
27	A low power low noise amplifier employing negative feedback and current reuse techniques. <i>Microelectronics Journal</i> , 2016, 49, 49-56.	1.1	28
28	Two-path inverter-based low noise amplifier for 10 $\frac{1}{2}$ GHz applications. <i>Microelectronics Journal</i> , 2016, 50, 76-82.	1.1	11
29	A 128 $\frac{1}{2}$ channel discrete cosine transform-based neural signal processor for implantable neural recording microsystems. <i>International Journal of Circuit Theory and Applications</i> , 2015, 43, 489-501.	1.3	9
30	Complex conjugate poles implementation in discrete-time charge domain filters. <i>Electronics Letters</i> , 2015, 51, 1236-1238.	0.5	3
31	A new pole-zero technique for reducing thermal noise to design a very low noise figure UWB LNA. , 2014, , .		2
32	A wideband low noise distributed amplifier with active termination. , 2014, , .		5
33	Direct Digital Frequency Synthesizer design with non-uniform squared-sine-weighted Digital-to-Analog Converter. , 2014, , .		1
34	A merged LNA and mixer with improved noise figure and gain for software defined radio applications. <i>IEICE Electronics Express</i> , 2012, 9, 165-171.	0.3	0
35	The design of reconfigurable Delta-Sigma modulator for software defined radio applications. , 2012, , .		5
36	Data compression based on Discrete Cosine Transform for implantable neural recording microsystems. , 2012, , .		4

#	ARTICLE	IF	CITATIONS
37	Multi-Level 2D LUT as digital pre-distorter for linearizing memory affected RF power amplifiers. IEICE Electronics Express, 2011, 8, 1569-1575.	0.3	2
38	A high gain, wide-band, fast settling amplifier with no-miller capacitor compensation. IEICE Electronics Express, 2011, 8, 1751-1756.	0.3	0
39	Sinusoidal shaping of the ISF in LC oscillators. International Journal of Circuit Theory and Applications, 2008, 36, 757-768.	1.3	16
40	Sinusoidal-switched serial-coupled CMOS LC quadrature VCO. IEICE Electronics Express, 2007, 4, 423-429.	0.3	4
41	Source-injection serial coupled CMOS LC quadrature VCO. IEICE Electronics Express, 2007, 4, 467-471.	0.3	4
42	Comments on "A General Theory of Phase Noise in Electrical Oscillators". IEEE Journal of Solid-State Circuits, 2007, 42, 2314-2314.	3.5	8