

# Grzegorz Andrzejewski

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/9262069/publications.pdf>

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11  
papers

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1937685

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#	ARTICLE	IF	CITATIONS
1	Finite State Machine Based Modelling of Discrete Control Algorithm in LAD Diagram Language With Use of New Generation Engineering Software. Procedia Computer Science, 2019, 159, 2560-2569.	2.0	22
2	On some aspects of Concurrent Control Processes Modelling and Implementation in LAD Diagram Language With Use of New Generation Engineering Software. Procedia Computer Science, 2020, 176, 2173-2183.	2.0	12
3	IPNES - Interpreted Petri Net for Embedded Systems. Procedia Computer Science, 2021, 192, 2012-2021.	2.0	7
4	Implementation of an example of Hierarchical Petri Net (HPN) in LAD language in TIA Portal. Procedia Computer Science, 2021, 192, 3657-3666.	2.0	6
5	Hierarchical Decomposition of Petri Nets for Digital Microsystems Design. , 2006, , .		4
6	Hierarchical Petri Nets for Digital Controller Design. , 2005, , 27-36.		4
7	EmbedCloud – Design and Implementation Method of Distributed Embedded Systems. IFIP Advances in Information and Communication Technology, 2015, , 157-164.	0.7	4
8	Bee Swarm Detection Based on Comparison of Estimated Distributions Samples of Sound. IEEE Transactions on Instrumentation and Measurement, 2019, 68, 3776-3784.	4.7	3
9	Time Dependencies Modelling in Traffic Control Algorithms. Communications in Computer and Information Science, 2013, , 1-6.	0.5	3
10	SEMANTIC INTEGRITY OF FLAT AND HIERARCHICAL PETRI NETS. IFAC Postprint Volumes IPPV / International Federation of Automatic Control, 2006, 39, 233-236.	0.4	1
11	CloudBus protocol hardware multi-converter gateway for distributed embedded systems. , 2017, , .		1