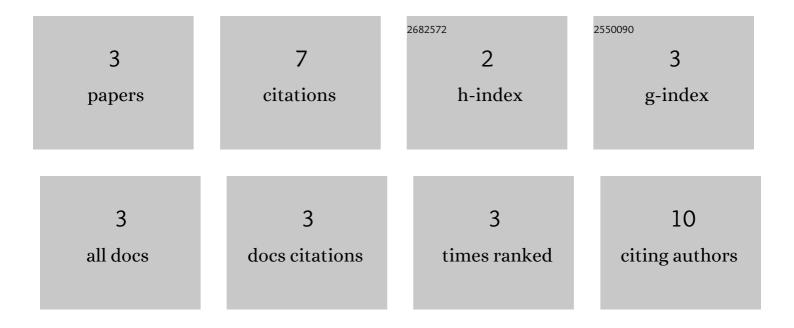
Vinicius N Possani

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/9256624/publications.pdf Version: 2024-02-01



#	Article	IF	CITATIONS
1	Exact Benchmark Circuits for Logic Synthesis. IEEE Design and Test, 2020, 37, 51-58.	1.2	3
2	Parallel Combinational Equivalence Checking. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 3081-3092.	2.7	2
3	Transistor Count Optimization in IG FinFET Network Design. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2017, 36, 1483-1496.	2.7	2