

Yong-Chang Choi

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/9241578/publications.pdf>

Version: 2024-02-01

8
papers

64
citations

2258059

3
h-index

2272923

4
g-index

8
all docs

8
docs citations

8
times ranked

97
citing authors

#	ARTICLE	IF	CITATIONS
1	A CMOS Transceiver for a Multistandard 13.56-MHz RFID Reader SoC. IEEE Transactions on Industrial Electronics, 2010, 57, 1563-1572.	7.9	25
2	A 0.7-MHzâ€“10-MHz $\{m\ CT\}+\{m\ DT\}$ Hybrid Baseband Chain With Improved Passband Flatness for LTE Application. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 244-253.	5.4	13
3	Multi-Standard Hybrid PLL With Low Phase-Noise Characteristics for GSM/EDGE and LTE Applications. IEEE Transactions on Microwave Theory and Techniques, 2015, 63, 3254-3264.	4.6	12
4	A fully digital polar transmitter using a digital-to-time converter for high data rate system. , 2009, , .		7
5	A 5.9 GHz LC-based digitally controlled oscillator with high frequency resolution using novel varactor pairs. , 2009, , .		4
6	An efficient, wide range time-to-digital converter using cascaded time-interpolation stages for electrical impedance spectroscopy. , 2016, , .		2
7	A Fully Digital Phase Modulator With a Highly Linear Phase Calibration Loop for Wideband Polar Transmitters. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 222-226.	3.0	1
8	Capacitance-to-digital converter based on power detection. , 2015, , .		0