

Hyunmi Kim

List of Publications by Year in descending order

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6
papers

37
citations

1937685

4
h-index

2272923

4
g-index

6
all docs

6
docs citations

6
times ranked

52
citing authors

#	ARTICLE	IF	CITATIONS
1	Efficient In-Loop Filtering Across Tile Boundaries for Multi-Core HEVC Hardware Decoders With 4 K/8 K-UHD Video Applications. IEEE Transactions on Multimedia, 2015, 17, 778-791.	7.2	15
2	Automated optimization for memory-efficient high-performance deep neural network accelerators. ETRI Journal, 2020, 42, 505-517.	2.0	8
3	An Efficient Architecture of In-Loop Filters for Multicore Scalable HEVC Hardware Decoders. IEEE Transactions on Multimedia, 2018, 20, 810-824.	7.2	6
4	AB9: A neural processor for inference acceleration. ETRI Journal, 2020, 42, 491-504.	2.0	5
5	On FHD 300MHz@60fps, intra/inter CU mode decision hardware architecture for the Hypernova H.265 encoder. , 2014, , .		3
6	Live Demonstration: A Neural Processor for AI Acceleration. , 2021, , .		0