Young-Gun Pu

List of Publications by Year in Descending Order

54 papers	341 citations	10 h-index	16 g-index
60 ext. papers	496 ext. citations	3.6 avg, IF	3.33 L-index

#	Paper	IF	Citations
54	A 5.8 GHz RF Receiver Front-End with 77.6 dB Dynamic Range AGC for a DSRC Transceiver. *IEEE Access*, **2022**, 1-1	3.5	1
53	Low Phase-Noise, 2.4 and 5.8 GHz Dual-Band Frequency Synthesizer with Class-C VCO and Bias-Controlled Charge Pump for RF Wireless Charging System in 180 nm CMOS Process. *Electronics (Switzerland)*, **2022**, 11, 1118	2.6	0
52	A Design of Peak to Average Power Ratio Based SWIPT System in 180 nm CMOS Process for IoT Sensor Applications. *IEEE Access*, **2022**, 1-1	3.5	1
51	A High-Efficiency Triple-Mode Active Rectifier With Gate Charge Recycling Technique for Wireless Power Transfer System. *IEEE Access*, **2022**, 10, 45943-45953	3.5	
50	A Wideband Multi-Level Reconfigurable Class E/F23 Power Amplifier with A Band-Selecting Tracking Reactance Compensation Automatic Calibration Algorithm. *IEEE Access*, **2022**, 1-1	3.5	
49	A 1.82.7 GHz Triple-Band Low Noise Amplifier with 31.5 dB Dynamic Range of Power Gain and Adaptive Power Consumption for LTE Application. *Sensors*, **2022**, 22, 4039	3.8	0
48	A Low-Band Multi-Gain LNA Design for Diversity Receive Module with 1.2 dB NF.. *Sensors*, **2021**, 21,	3.8	1
47	. *IEEE Access*, **2021**, 9, 152984-152992	3.5	0
46	A Low-Power 12-Bit 20 MS/s Asynchronously Controlled SAR ADC for WAVE ITS Sensor Based Applications. *Sensors*, **2021**, 21,	3.8	1
45	A 15-W Triple-Mode Wireless Power Transmitting Unit With High System Efficiency Using Integrated Power Amplifier and DCDC Converter. *IEEE Transactions on Industrial Electronics*, **2021**, 68, 9574-9585	8.9	2
44	A 15-W Quadruple-Mode Reconfigurable Bidirectional Wireless Power Transceiver With 95% System Efficiency for Wireless Charging Applications. *IEEE Transactions on Power Electronics*, **2021**, 36, 3814-3827	7.2	6
43	A Design of Adaptive Control and Communication Protocol for SWIPT System in 180 nm CMOS Process for Sensor Applications. *Sensors*, **2021**, 21,	3.8	1
42	A 2.4 GHz Power Receiver Embedded With a Low-Power Transmitter and PCE of 53.8%, for Wireless Charging of IoT/Wearable Devices. *IEEE Transactions on Microwave Theory and Techniques*, **2021**, 69, 4315-4325	4.1	2
41	A 77-dB Dynamic-Range Analog Front-End for Fine-Dust Detection Systems with Dual-Mode Ultra-Low Noise TIA. *Sensors*, **2021**, 21,	3.8	1
40	A High-Efficient Wireless Power Receiver for Hybrid Energy-Harvesting Sources. *IEEE Transactions on Power Electronics*, **2021**, 36, 11148-11162	7.2	6
39	Design of High Performance Hybrid Type Digital-Feedback Low Drop-Out Regulator Using SSCG Technique. *IEEE Access*, **2021**, 9, 28167-28176	3.5	3
38	An Efficient Reconfigurable RF-DC Converter With Wide Input Power Range for RF Energy Harvesting. *IEEE Access*, **2020**, 8, 79310-79318	3.5	18

37	A Low-Power Multichannel Time-to-Digital Converter Using All-Digital Nested Delay-Locked Loops With 50-ps Resolution and High Throughput for LiDAR Sensors. *IEEE Transactions on Instrumentation and Measurement*, **2020**, 69, 9262-9271	5.2	10
36	Design of a Low Power 10-b 8-MS/s Asynchronous SAR ADC with On-Chip Reference Voltage Generator. *Electronics (Switzerland)*, **2020**, 9, 872	2.6	7
35	A High-Efficiency and Wide-Input Range RF Energy Harvester Using Multiple Rectenna and Adaptive Matching. *Energies*, **2020**, 13, 1023	3.1	3
34	A Design of Wide-Range and Low Phase Noise Linear Transconductance VCO with 193.76 dBc/Hz FoMT for mm-Wave 5G Transceivers. *Electronics (Switzerland)*, **2020**, 9, 935	2.6	7
33	A Design of 6.8 mW All Digital Delay Locked Loop With Digitally Controlled Dither Cancellation for TDC in Ranging Sensor. *IEEE Access*, **2020**, 8, 57722-57732	3.5	1
32	A Highly Accurate, Polynomial-Based Digital Temperature Compensation for Piezoresistive Pressure Sensor in 180 nm CMOS Technology. *Sensors*, **2020**, 20,	3.8	1
31	A Design of Low-Power 10-bit 1-MS/s Asynchronous SAR ADC for DSRC Application. *Electronics (Switzerland)*, **2020**, 9, 1100	2.6	8
30	A 2.45 GHz High Efficiency CMOS RF Energy Harvester with Adaptive Path Control. *Electronics (Switzerland)*, **2020**, 9, 1107	2.6	5
29	A Highly Reliable, 5.8 GHz DSRC Wake-Up Receiver with an Intelligent Digital Controller for an ETC System. *Sensors*, **2020**, 20,	3.8	3
28	A High Performance Adaptive Digital LDO Regulator With Dithering and Dynamic Frequency Scaling for IoT Applications. *IEEE Access*, **2020**, 8, 132200-132211	3.5	4
27	A Fully Integrated Bluetooth Low-Energy Transceiver with Integrated Single Pole Double Throw and Power Management Unit for IoT Sensors. *Sensors*, **2019**, 19,	3.8	7
26	A CMOS RF Energy Harvester With 47% Peak Efficiency Using Internal Threshold Voltage Compensation. *IEEE Microwave and Wireless Components Letters*, **2019**, 29, 415-417	2.6	13
25	Design of a 900 MHz Dual-Mode SWIPT for Low-Power IoT Devices. *Sensors*, **2019**, 19,	3.8	8
24	A design of a 5.6□GHz frequency synthesizer with switched bias LIT VCO and low noise on-chip LDO regulator for 5G applications. *International Journal of Circuit Theory and Applications*, **2019**, 47, 1856-1868	2	2
23	A 3.9 mW Bluetooth Low-Energy Transmitter Using All-Digital PLL-Based Direct FSK Modulation in 55 nm CMOS. *IEEE Transactions on Circuits and Systems I: Regular Papers*, **2018**, 65, 3037-3048	3.9	12
22	260- mu W DCO With Constant Current Over PVT Variations Using FLL and Adjustable LDO. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2018**, 65, 739-743	3.5	6
21	A Highly Linear, AEC-Q100 Compliant Signal Conditioning IC for Automotive Piezo-Resistive Pressure Sensors. *IEEE Transactions on Industrial Electronics*, **2018**, 65, 7363-7373	8.9	5
20	A 39.5-dB SNR, 300-Hz Frame-Rate, 56 □70-Channel Read-Out IC for Electromagnetic Resonance Touch Panels. *IEEE Transactions on Industrial Electronics*, **2018**, 65, 5001-5011	8.9	0

19	A Design of Fast-Settling, Low-Power 4.19-MHz Real-Time Clock Generator With Temperature Compensation and 15-dB Noise Reduction. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, **2018**, 26, 1151-1158	2.6	4
18	A Triple-Mode Wireless Power-Receiving Unit With 85.5% System Efficiency for A4WP, WPC, and PMA Applications. *IEEE Transactions on Power Electronics*, **2018**, 33, 3141-3156	7.2	17
17	Design of a High Efficiency DCDC Buck Converter With Two-Step Digital PWM and Low Power Self-Tracking Zero Current Detector for IoT Applications. *IEEE Transactions on Power Electronics*, **2018**, 33, 1428-1439	7.2	33
16	A Design of Small Area, 0.95 mW, 612?1152 MHz Open Loop Injection-Locked Frequency Multiplier for IoT Sensor Applications. *Sensors*, **2018**, 18,	3.8	2
15	Design of Peak Efficiency of 85.3% WPC/PMA Wireless Power Receiver Using Synchronous Active Rectifier and Multi Feedback Low-Dropout Regulator. *Energies*, **2018**, 11, 479	3.1	3
14	Single Inductor-Multiple Output DPWM DC-DC Boost Converter with a High Efficiency and Small Area. *Energies*, **2018**, 11, 725	3.1	4
13	A High Noise Immunity, 28 16-Channel Finger Touch Sensing IC Using OFDM and Frequency Translation Technique. *Sensors*, **2018**, 18,	3.8	2
12	A 6-bit 4MS/s 26fJ/conversion-step segmented SAR ADC with reduced switching energy for BLE. *International Journal of Circuit Theory and Applications*, **2018**, 46, 375-383	2	5
11	An Inductive 2-D Position Detection IC With 99.8% Accuracy for Automotive EMR Gear Control System. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, **2017**, 25, 1731-1741	2.6	2
10	A Design of a 92.4% Efficiency Triple Mode Control DCDC Buck Converter With Low Power Retention Mode and Adaptive Zero Current Detector for IoT/Wearable Applications. *IEEE Transactions on Power Electronics*, **2017**, 32, 6946-6960	7.2	33
9	A design of wide input range triple-mode active rectifier with peak efficiency of 94.2 % and maximum output power of 8 W for wireless power receiver in 0.18 M BCD. *Analog Integrated Circuits and Signal Processing*, **2016**, 86, 255-265	1.2	2
8	A low phase noise 30-GHz frequency synthesizer with linear transconductance VCO and dual-injection-locked frequency divider. *Analog Integrated Circuits and Signal Processing*, **2016**, 86, 365-376	1.2	4
7	A Design of a Wireless Power Receiving Unit With a High-Efficiency 6.78-MHz Active Rectifier Using Shared DLLs for Magnetic-Resonant A4 WP Applications. *IEEE Transactions on Power Electronics*, **2016**, 31, 4484-4498	7.2	51
6	Low power FSK transceiver using ADPLL with direct modulation and integrated SPDT for BLE application **2016**,		2
5	A design of power managements IC with peak efficiency of 92.8 % step-up converter and peak efficiency of 93.8 % step-down converter for power transmitting unit of A4WP applications in 0.18 m BCD. *Analog Integrated Circuits and Signal Processing*, **2016**, 88, 115-125	1.2	2
4	A Wide-Locking-Range Dual Injection-Locked Frequency Divider With an Automatic Frequency Calibration Loop in 65-nm CMOS. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2015**, 62, 327-331	3.5	15
3	A 1.2482.918 Gb/s low-power transmitter for MIPI M-PHY with 2-step impedance calibration loop in 0.11 m CMOS. *Analog Integrated Circuits and Signal Processing*, **2015**, 83, 129-142	1.2	0
2	A Highly Linear, Small-Area Analog Front End With Gain and Offset Compensation for Automotive Capacitive Pressure Sensors in 0.35- $mu $ m CMOS. *IEEE Sensors Journal*, **2015**, 15, 1967-1976	4	10

1	A design of 50/150/200 kbps, low power FSK transceiver using phase-locked loop with programmable loop bandwidth and integrated SPDT for IEEE 802.15.4g application. *Analog Integrated Circuits and Signal Processing*, **2015**, 84, 261-282	1.2