

# Khuram Shehzad

## List of Publications by Year in descending order

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15  
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#	ARTICLE	IF	CITATIONS
1	A Highly Efficient RF-DC Converter for Energy Harvesting Applications Using a Threshold Voltage Cancellation Scheme. <i>Sensors</i> , 2022, 22, 2659.	3.8	6
2	A Design of 44.1 fJ/Conv-Step 12-Bit 80 ms/s Time Interleaved Hybrid Type SAR ADC With Redundancy Capacitor and On-Chip Time-Skew Calibration. <i>IEEE Access</i> , 2021, 9, 133143-133155.	4.2	6
3	A Low-Power 12-Bit 20 MS/s Asynchronously Controlled SAR ADC for WAVE ITS Sensor Based Applications. <i>Sensors</i> , 2021, 21, 2260.	3.8	8
4	A 77-dB Dynamic-Range Analog Front-End for Fine-Dust Detection Systems with Dual-Mode Ultra-Low Noise TIA. <i>Sensors</i> , 2021, 21, 6360.	3.8	3
5	A Highly Accurate, Polynomial-Based Digital Temperature Compensation for Piezoresistive Pressure Sensor in 180 nm CMOS Technology. <i>Sensors</i> , 2020, 20, 5256.	3.8	9
6	A Design of Low-Power 10-bit 1-MS/s Asynchronous SAR ADC for DSRC Application. <i>Electronics (Switzerland)</i> , 2020, 9, 1100.	3.1	14
7	A 2.45 GHz High Efficiency CMOS RF Energy Harvester with Adaptive Path Control. <i>Electronics (Switzerland)</i> , 2020, 9, 1107.	3.1	9
8	An Efficient Reconfigurable RF-DC Converter With Wide Input Power Range for RF Energy Harvesting. <i>IEEE Access</i> , 2020, 8, 79310-79318.	4.2	41
9	A Design of 8 fJ/Conversion-Step 10-bit 8MS/s Low Power Asynchronous SAR ADC for IEEE 802.15.1 IoT Sensor Based Applications. <i>IEEE Access</i> , 2020, 8, 85869-85879.	4.2	20
10	Design of a Low Power 10-b 8-MS/s Asynchronous SAR ADC with On-Chip Reference Voltage Generator. <i>Electronics (Switzerland)</i> , 2020, 9, 872.	3.1	13
11	A CMOS RF Energy Harvester With 47% Peak Efficiency Using Internal Threshold Voltage Compensation. <i>IEEE Microwave and Wireless Components Letters</i> , 2019, 29, 415-417.	3.2	28
12	A Sigma-Delta ADC for Signal Conditioning IC of Automotive Piezo-Resistive Pressure Sensors with over 80 dB SNR. <i>Sensors</i> , 2018, 18, 4199.	3.8	9
13	A 10- and 12-Bit Multi-Channel Hybrid Type Successive Approximation Register Analog-to-Digital Converter for Wireless Power Transfer System. <i>Energies</i> , 2018, 11, 2673.	3.1	5
14	Design of asynchronous SAR ADC for low power mixed signal applications. , 2017, , .		4
15	Low-power 10-bit SAR ADC using class-AB type amplifier for IoT applications. , 2017, , .		4