

Wang, Li

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/9066335/publications.pdf>

Version: 2024-02-01

17
papers

106
citations

1478505

6
h-index

1720034

7
g-index

18
all docs

18
docs citations

18
times ranked

138
citing authors

#	ARTICLE	IF	CITATIONS
1	A 75-Mb/s RGB PAM-4 Visible Light Communication Transceiver System With Pre- and Post-Equalization. <i>Journal of Lightwave Technology</i> , 2021, 39, 1381-1390.	4.6	15
2	An RGB-LED Driver with Feed-Forward Equalization Used for PAM-4 Visible Light Communication. <i>Lecture Notes of the Institute for Computer Sciences, Social-Informatics and Telecommunications Engineering</i> , 2021, , 228-234.	0.3	0
3	Sensing and Cancellation Circuits for Mitigating EMI-Related Common Mode Noise in High-Speed PAM-4 Transmitter. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2021, , 1-11.	5.4	2
4	A W-Band Single-Antenna FMCW Radar Transceiver With Adaptive Leakage Cancellation. <i>IEEE Journal of Solid-State Circuits</i> , 2021, 56, 1655-1667.	5.4	10
5	A 52-Gb/s Sub-1-pJ/bit PAM4 Receiver in 40-nm CMOS for Low-Power Interconnects. <i>IEEE Open Journal of Circuits and Systems</i> , 2021, 2, 46-55.	1.9	0
6	A Low-Power PAM4 Receiver With an Adaptive Variable-Gain Rectifier-Based Decoder. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2020, 28, 2099-2108.	3.1	7
7	A RGB LED PAM-4 Visible Light Communication Transmitter Based on a System Design with Equalization. , 2020, , .		3
8	A 32-Gb/s 0.46-pJ/bit PAM4 CDR Using a Quarter-Rate Linear Phase Detector and a Self-Biased PLL-Based Multiphase Clock Generator. <i>IEEE Journal of Solid-State Circuits</i> , 2020, 55, 2734-2746.	5.4	15
9	Simultaneous Magnetic Resonance Wireless Power and High-Speed Data Transfer System With Cascaded Equalizer for Variable Channel Compensation. <i>IEEE Transactions on Power Electronics</i> , 2019, 34, 11594-11604.	7.9	13
10	Design of a Real-Time Visible Laser Light Communication System with Basedband in FPGA for High Definition video Transmission. , 2019, , .		2
11	A 32-Gb/s 0.46-pJ/bit PAM4 CDR Using a Quarter-Rate Linear Phase Detector and a Low-Power Multiphase Clock Generator. , 2019, , .		2
12	Smart Table Applications Based on Magnetic Resonance Wireless Power Transfer. , 2019, , .		0
13	A Dual-Resonance Matching Circuit for Magnetic Resonance Wireless Power Transfer Systems. , 2019, , .		1
14	Design of a 2.2-mW 24-Mb/s CMOS VLC Receiver SoC With Ambient Light Rejection and Post-Equalization for Li-Fi Applications. <i>Journal of Lightwave Technology</i> , 2018, 36, 2366-2375.	4.6	31
15	Compact Modeling of Laser Diode for Visible Laser Light Communication (VLLC) Systems. , 2018, , .		0
16	Modulation optimization for visible laser light communication systems. , 2017, , .		1
17	A 2.2-mW 24-Mb/s CMOS LiFi receiver system-on-a-chip with ambient light rejection and post-equalization. , 2017, , .		4