Wang, Li

List of Publications by Year
in descending order

17 papers	106 citations	1478505 6 h-index	1720034 7 g-index
18 all docs	18 docs citations	18 times ranked	138 citing authors

#	Article	IF	Citations
1	Design of a 2.2-mW 24-Mb/s CMOS VLC Receiver SoC With Ambient Light Rejection and Post-Equalization for Li-Fi Applications. Journal of Lightwave Technology, 2018, 36, 2366-2375.	4.6	31
2	A 32-Gb/s 0.46-pJ/bit PAM4 CDR Using a Quarter-Rate Linear Phase Detector and a Self-Biased PLL-Based Multiphase Clock Generator. IEEE Journal of Solid-State Circuits, 2020, 55, 2734-2746.	5.4	15
3	A 75-Mb/s RGB PAM-4 Visible Light Communication Transceiver System With Pre- and Post-Equalization. Journal of Lightwave Technology, 2021, 39, 1381-1390.	4.6	15
4	Simultaneous Magnetic Resonance Wireless Power and High-Speed Data Transfer System With Cascaded Equalizer for Variable Channel Compensation. IEEE Transactions on Power Electronics, 2019, 34, 11594-11604.	7.9	13
5	A W-Band Single-Antenna FMCW Radar Transceiver With Adaptive Leakage Cancellation. IEEE Journal of Solid-State Circuits, 2021, 56, 1655-1667.	5.4	10
6	A Low-Power PAM4 Receiver With an Adaptive Variable-Gain Rectifier-Based Decoder. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 2099-2108.	3.1	7
7	A 2.2-mW 24-Mb/s CMOS LiFi receiver system-on-a-chip with ambient light rejection and post-equalization. , 2017, , .		4
8	A RGB LED PAM-4 Visible Light Communication Transmitter Based on a System Design with Equalization. , 2020, , .		3
9	Design of a Real-Time Visible Laser Light Communication System with Basedband in FPGA for High Definition video Transmission. , 2019, , .		2
10	A 32-Gb/s 0.46-pJ/bit PAM4 CDR Using a Quarter-Rate Linear Phase Detector and a Low-Power Multiphase Clock Generator. , 2019, , .		2
11	Sensing and Cancellation Circuits for Mitigating EMI-Related Common Mode Noise in High-Speed PAM-4 Transmitter. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, , 1-11.	5.4	2
12	Modulation optimization for visible laser light communication systems. , 2017, , .		1
13	A Dual-Resonance Matching Circuit for Magnetic Resonance Wireless Power Transfer Systems. , 2019, , .		1
14	Smart Table Applications Based on Magnetic Resonance Wireless Power Transfer. , 2019, , .		0
15	An RGB-LED Driver with Feed-Forward Equalization Used for PAM-4 Visible Light Communication. Lecture Notes of the Institute for Computer Sciences, Social-Informatics and Telecommunications Engineering, 2021, , 228-234.	0.3	0
16	A 52-Gb/s Sub-1-pJ/bit PAM4 Receiver in 40-nm CMOS for Low-Power Interconnects. IEEE Open Journal of Circuits and Systems, 2021, 2, 46-55.	1.9	0
17	Compact Modeling of Laser Diode for Visible Laser Light Communication (VLLC) Systems. , 2018, , .		0