

Stefan Pechmann

List of Publications by Year in descending order

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papers

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2258059

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2272923

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8
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8
docs citations

8
times ranked

32
citing authors

#	ARTICLE	IF	CITATIONS
1	Accelerated Addition in Resistive RAM Array Using Parallel-Friendly Majority Gates. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 1108-1121.	3.1	25
2	A Parallel-friendly Majority Gate to Accelerate In-memory Computation. , 2020, , .		6
3	A Versatile, Voltage-Pulse Based Read and Programming Circuit for Multi-Level RRAM Cells. Electronics (Switzerland), 2021, 10, 530.	3.1	5
4	An Integrated Bistatic 4TX/4RX Six-Port MIMO-Transceiver at 60 GHz in a 130-nm SiGe BiCMOS Technology for Radar Applications. , 2020, , .		4
5	A Low-Power RRAM Memory Block for Embedded, Multi-Level Weight and Bias Storage in Artificial Neural Networks. Micromachines, 2021, 12, 1277.	2.9	4
6	Mitigating the Effects of RRAM Process Variation on the Accuracy of Artificial Neural Networks. Lecture Notes in Computer Science, 2022, , 401-417.	1.3	2
7	Simulating large neural networks embedding MLC RRAM as weight storage considering device variations. , 2021, , .		1
8	A Framework for Ultra Low-Power Hardware Accelerators Using NNs for Embedded Time Series Classification. Journal of Low Power Electronics and Applications, 2022, 12, 2.	2.0	0