

Frank Herzel

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/9038494/publications.pdf>

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13
papers

139
citations

1937685

4
h-index

1872680

6
g-index

13
all docs

13
docs citations

13
times ranked

106
citing authors

#	ARTICLE	IF	CITATIONS
1	A Novel architecture for low-jitter multi-GHz frequency synthesis. Frequenz, 2022, 76, 337-344.	0.9	1
2	Multiplexed Twin PLLs for Wide-Band FMCW Chirp Generation in 130-nm BiCMOS. IEEE Microwave and Wireless Components Letters, 2019, 29, 483-485.	3.2	2
3	Numerical Jitter Minimization for PLL-Based FMCW Radar Systems. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 2478-2488.	5.4	8
4	A Study of Phase Noise and Frequency Error of a Fractional-N PLL in the Course of FMCW Chirp Generation. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 1670-1680.	5.4	27
5	Analysis of Ranging Precision in an FMCW Radar Measurement Using a Phase-Locked Loop. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 783-792.	5.4	38
6	A 15-50GHz Multiplexer Circuit in 130nm SiGe BiCMOS Technology for Ultra-Wide Frequency Ramps in FMCW Radar. , 2018, , .		1
7	Error Analysis of Target Angle and Angular Velocity in a PLL-based FMCW Radar Measurement. , 2018, , .		1
8	Phase noise analysis of a homodyne radar system driven by a phase-locked loop. , 2017, , .		11
9	Design of a low-jitter wideband frequency synthesizer for 802.11ad wireless OFDM systems using a frequency sextupler. , 2017, , .		7
10	Modeling of range accuracy for a radar system driven by a noisy phase-locked loop. , 2017, , .		4
11	Time-domain simulation of quantization noise mixing and charge pump device noise in fractional-N PLLs. , 2015, , .		8
12	Phase Noise Modeling for Integrated PLLs in FMCW Radar. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 137-141.	3.0	24
13	Jitter and phase noise in oscillators and phase-locked loops. , 2004, , .		7